

Fast Infrared Transceiver Module (MIR, 1.152 Mbit/s) for IrDA[®] applications

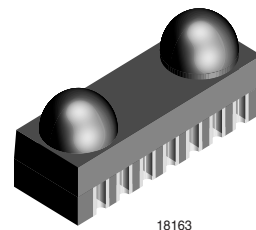
Description

The TFBS5614 transceiver is an infrared transceiver module compliant to the latest IrDA physical layer standard for fast infrared data communication, supporting IrDA speeds up to 1.152 Mbit/s (MIR), HP-SIR[®], Sharp ASK[®] and carrier based remote control modes up to 2 MHz. Integrated within the transceiver module are a photo PIN diode, an infrared emitter (IRED), and a low-power control IC to provide a total front-end solution in a single package.

This new Vishay MIR transceiver is built in a new smaller package using the experiences of the lead frame babyface technology. The transceivers are capable of directly interfacing with a wide variety of I/O devices, which perform the modulation/ demodulation function. At a minimum, a V_{CC} bypass capacitor and a serial resistor for current control are the only external components required implementing a complete solution. TFBS5614 has a tri-state output and is floating in shut-down mode with a weak pull-up.

Features

- Compliant to the latest IrDA physical layer specification (up to 1.152 Mbit/s), HP-SIR[®], Sharp ASK[®] and TV Remote Control, bi-directional operation included.
- For 3.0 V and 5 V Applications. Operates from 2.7 V to 5.5 V within specification
- Low Power Consumption (< 0.55 mA Supply Current in receive mode, no signal)
- Power Shutdown Mode (< 5 μ A Shutdown Current in Full Temperature Range, up to 85 °C)
- Surface Mount Package, low profile (2.7 mm) Universal (8.0 \times 2.7 \times 3.3 mm³)
- Backward Pin Compatible to Vishay Transceivers as IRM1030 and TFBS5615
- High Efficiency Emitter
- Low Profile (Universal) Package Capable of Surface Mount Soldering to Side and Top View Orientation
- Directly Interfaces with Various Super I/O and Controller Devices
- Tri-state-Receiver Output, floating in shut down with a weak pull-up



- Split power supply, transmitter and receiver can be operated from two power supplies with relaxed requirements saving costs, US - Patent No. 6,157,476
- Logic voltage 1.5 V to 5.5 V (optional at pin 6 on request) independent of IRED driver and analog supply voltage
- Only One External Component Required
- TV Remote Control supported

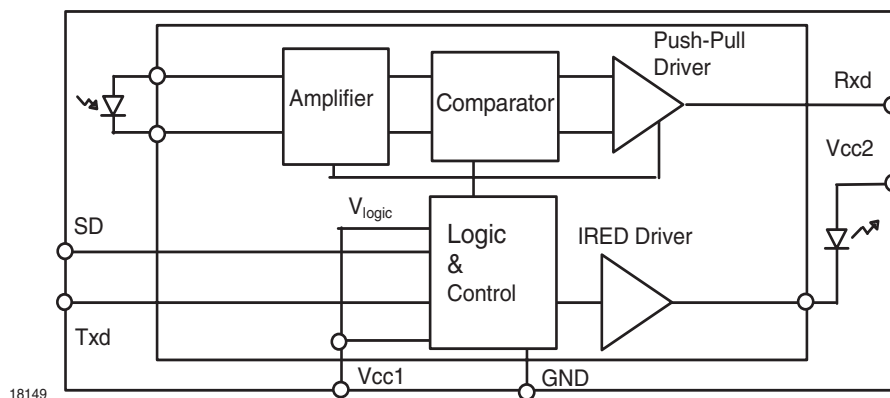
Applications

- Telecommunication Products (Cellular Phones, Pagers)
- Digital Still and Video Cameras
- Printers, Fax Machines, Photocopiers, Screen Projectors
- Medical and Industrial Data Collection
- Notebook Computers, Desktop PCs, Palmtop Computers (Win CE, Palm PC), PDAs
- Internet TV Boxes, Video Conferencing Systems
- External Infrared Adapters (Dongles)
- Kiosks, POS, Point and Pay Devices including IrFM - Applications

Parts Table

Part	Description	Qty / Reel
TFBS5614-TR1	Oriented in carrier tape for side view surface mounting	1000 pcs
TFBS5614-TR3	Oriented in carrier tape for side view surface mounting	2500 pcs

Functional Block Diagram

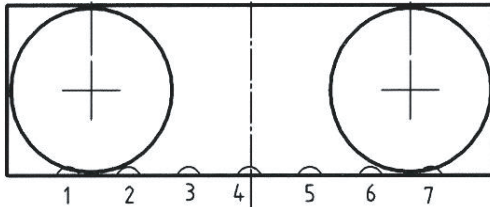


Pin Description

Pin Number	Function	Description	I/O	Active
1	V _{CC2} IRED Anode	IRED anode to be externally connected to V _{CC2} . An external resistor is necessary for controlling the IRED current. This pin is allowed to be supplied from an uncontrolled power supply separated from the controlled V _{CC1} - supply		
2	Txd	This Schmitt-Trigger input is used to transmit serial data when SD is low. An on-chip protection circuit disables the LED driver if the Txd pin is asserted for longer than 80 μs. When used in conjunction with the SD pin, this pin is also used to control receiver output pulse duration.	I	HIGH
3	Rxd	Received Data Output, push-pull CMOS driver output capable of driving standard CMOS or TTL loads. No external pull-up or pull-down resistor is required. Floating with a weak pull-up of 500 kΩ (typ.) in shutdown mode.	O	LOW
4	SD	Shutdown, also used for setting the output pulse duration. Setting this pin active for more than 1.5 ms places the module into shutdown mode. Before that (t < 0.7 ms) on the falling edge of this signal, the state of the Txd pin is sampled and used to set the receiver output to long pulse duration (2 μs) or to short pulse duration (0.4 μs) mode	I	HIGH
5	V _{CC1}	Supply Voltage		
6	NC	Internally not connected	I	
7	GND	Ground		

Pinout

TFBS5614
weight 80 mg



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Definitions:

In the Vishay transceiver data sheets the following nomenclature is used for defining the IrDA operating modes:

SIR: 2.4 kbit/s to 115.2 kbit/s, equivalent to the basic serial infrared standard with the physical layer version IrPhy 1.0

MIR: 576 kbit/s to 1152 kbit/s

FIR: 4 Mbit/s

VFIR: 16 Mbit/s

MIR and FIR were implemented with IrPhy 1.1, followed by IrPhy 1.2, adding the SIR Low Power Standard. IrPhy 1.3 extended the Low Power Option to MIR and FIR and VFIR was added with IrPhy 1.4. A new version of the standard in any case obsoletes the former version.

With introducing the updated versions the old versions are obsolete. Therefore the only valid IrDA standard is the actual version IrPhy 1.4 (in Oct. 2002).

Absolute Maximum Ratings

Reference point Ground (Pin 7) unless otherwise noted.

Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

Parameter	Test Conditions	Symbol	Min	Typ.	Max	Unit
Supply voltage range, transceiver	$-0.3\text{ V} < V_{CC2} < 6\text{ V}$	V_{CC1}	- 0.3		+ 6.0	V
Supply voltage range, transmitter	$-0.5\text{ V} < V_{CC1} < 6\text{ V}$	V_{CC2}	- 0.3		+ 6.5	V
Input currents	for all pins, except IRED anode pin				10	mA
Output sinking current					25	mA
Power dissipation	see derating curve	P_D			500	mW
Junction temperature		T_J			125	°C
Ambient temperature range (operating)		T_{amb}	- 25		+ 85	°C
Storage temperature range		T_{stg}	- 25		+ 85	°C
Soldering temperature	see recommended solder profile				240	°C
Average output current, pin 1		$I_{IRED(DC)}$			125	mA
Repetitive pulsed output current, pin 1	$t < 90\ \mu\text{s}$, $t_{on} < 20\%$	$I_{IRED(RP)}$			600	mA
IRED anode voltage, pin 1		$V_{IRED A}$	- 0.5		+ 6.5	V
Voltage at all inputs and outputs	$V_{in} < V_{CC1}$ is allowed	V_{in}	- 0.5		+ 5.5	V
Load at mode pin when used as mode indicator					50	pF

Eye safety information

Parameter	Test Conditions	Symbol	Min	Typ.	Max	Unit
Virtual source size	Method: (1-1/e) encircled energy	d	1.8	2.0		mm
Maximum intensity for class 1	IEC60825-1 or EN60825-1, edition Jan. 2001, operating below the absolute maximum ratings	I_e			^{*)} (500) ^{**)}	mW/sr

^{*)} Due to the internal limitation measures the device is a "class 1" device.

^{**)} IrDA specifies the max. intensity with 500 mW/sr.

Electrical Characteristics

Transceiver

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC1} = V_{CC2} = 2.7\text{ V}$ to 5.5 V unless otherwise noted.

Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

Parameter	Test Conditions	Symbol	Min	Typ.	Max	Unit
Supply voltage		V_{CC1}	2.7		5.5	V
Dynamic supply current	SD = Low, $E_e = 1\text{ klx}$	I_{CC}		550	900	μA
Average dynamic supply current, transmitting	$I_{IRED} = 500\text{ mA}$, 25 % Duty Cycle	I_{CC}		1100	1500	μA
Standby supply current	SD = High, $T = 25\text{ }^{\circ}\text{C}$, $E_e = 0\text{ klx}$	I_{SD}			1	μA
	SD = High, $T = 25\text{ }^{\circ}\text{C}$, $E_e = 1\text{ klx}$ ^{*)}	I_{SD}			2.5	μA
	SD = High, $T = 85\text{ }^{\circ}\text{C}$, not ambient light sensitive	I_{SD}			5	μA
Operating temperature range		T_A	- 25		+ 85	$^{\circ}\text{C}$
Output voltage low, Rxd	$C_{Load} = 15\text{ pF}$, $I_{OL} = 1\text{ mA}$	V_{OL}			0.4	V
Output voltage high, Rxd	$I_{OH} = - 500\text{ }\mu\text{A}$	V_{OH}	$0.8 \times V_{CC1}$			V
	$I_{OH} = - 250\text{ }\mu\text{A}$, $C_{Load} = 15\text{ pF}$	V_{OH}	$0.9 \times V_{CC1}$			V
Rxd to V_{CC1} impedance		R_{Rxd}	400	500	600	$\text{k}\Omega$
Input voltage low (Txd, SD)		V_{IL}	- 0.5		0.5	V
Input voltage high (Txd, SD)	CMOS level ^{**)}	V_{IH}	$V_{CC1} - 0.5$		$V_{CC1} + 0.5$	V
Input leakage current (Txd, SD)	$V_{in} = 0.9 \times V_{CC1}$	I_{ICH}	- 2		+ 2	μA
Controlled pull down current	SD, Txd = "0" to "1", $0 < V_{in} < 0.15 V_{CC1}$	I_{IRTx}			+ 150	μA
	SD, Txd = "0" to "1", $V_{in} > 0.7 V_{CC1}$	I_{IRTx}	- 1	0	1	μA
Input capacitance (Txd, SD)		C_I			5	pF

^{*)} Standard illuminant A

^{**)} The typical threshold level is $0.5 \times V_{CC1}$. It is recommended to use the specified min/max values to avoid increased operating current.

**Optoelectronic Characteristics****Receiver**

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC1} = V_{CC2} = 2.7\text{ V}$ to 5.5 V unless otherwise noted.

Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

Parameter	Test Conditions	Symbol	Min	Typ.	Max	Unit
Minimum detection threshold irradiance, SIR Mode	9.6 kbit/s to 1152 kbit/s $\lambda = 850\text{ nm} - 900\text{ nm}$	E_e		40 (4)	90 (9)	mW/m^2 $(\mu\text{W}/\text{cm}^2)$
Maximum detection threshold irradiance	$\lambda = 850\text{ nm} - 900\text{ nm}$	E_e		5 (500)		kW/m^2 (mW/cm^2)
Logic LOW receiver input irradiance		E_e	4 (0.4)			mW/m^2 $(\mu\text{W}/\text{cm}^2)$
Rise time of output signal	10 % to 90 %, $C_L = 15\text{ pF}$	$t_{r(\text{Rxd})}$	20		60	ns
Fall time of output signal	90 % to 10 %, $C_L = 15\text{ pF}$	$t_{f(\text{Rxd})}$	20		60	ns
Rxd pulse width of output signal, default mode after power on	input pulse length $t_{PW_{opt}} > 0.140\text{ }\mu\text{s}$	t_{PW}	300	400	500	ns
SIR ENDEC compatibility mode ^{*)} : Rxd pulse width of output signal	input pulse length $0.140\text{ }\mu\text{s} < t_{PW_{opt}}$, see chapter "Programming"	t_{PW}	1.7	2.0	2.2	μs
Stochastic jitter, leading edge	input irradiance = $100\text{ mW}/\text{m}^2$, 1.152 Mbit/s, 576 kbit/s				80	ns
	input irradiance = $100\text{ mW}/\text{m}^2$, $\leq 115.2\text{ kbit/s}$				350	ns
Standby /Shutdown delay	after shutdown active or (SD low to high transition)		0.6		1.5	ms
Shutdown active time window for programming	During this time the pulse duration of the output can be programmed to the application mode. see chapter "Programming"				600	μs
Receiver start up time power on delay shutdown recovery delay	after shutdown inactive (SD high to low transition) and after power-on				300	μs
Latency		t_L			200	μs

^{*)} Some ENDECs are not able to decode short pulses as valid SIR pulses. Therefore this additional mode was added in TFDU5307. TFDU5307 is set to the "short output pulse" as default after power on, also after recovering from the shutdown mode (SD must have been longer active than 1.5 ms). For mode changing see the chapter "Programming"

Transmitter

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC1} = V_{CC2} = 2.7\text{ V}$ to 5.5 V unless otherwise noted.

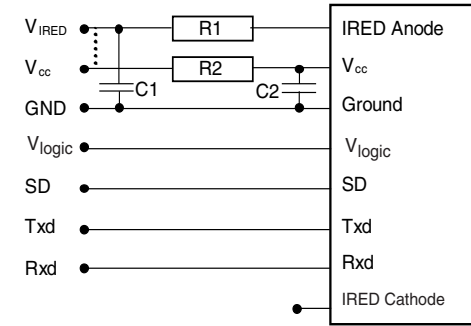
Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

Parameter	Test Conditions	Symbol	Min	Typ.	Max	Unit
IRED operating current, recommended serial resistor for MIR applications	$V_{CC2} = 3.3\text{ V}$: $R_S = 2.0\text{ }\Omega$ $V_{CC2} = 5.0\text{ V}$: $R_S = 5.6\text{ }\Omega$	I_D		400	500	mA
Output leakage IRED current	$T_{xd} = 0\text{ V}$, $0 < V_{CC1} < 5.5\text{ V}$	I_{IRED}	- 1		1	μA
Output radiant intensity recommended application circuit, see figure 1	$\alpha = 0\text{ }^{\circ}$, 15 ° $T_{xd} = \text{High}$, $SD = \text{Low}$	I_e		150		mW/sr
Output radiant intensity	$V_{CC1} = 5.0\text{ V}$, $\alpha = 0\text{ }^{\circ}$, 15 ° $T_{xd} = \text{Low}$ or $SD = \text{High}$ (Receiver is inactive as long as $SD = \text{High}$)	I_e			0.04	mW/sr
Output radiant intensity, angle of half intensity		α		± 24		$^{\circ}$
Peak - emission wavelength ^{*)}		λ_P	880		900	nm
Spectral bandwidth		$\Delta\lambda$		45		nm
Optical rise time, fall time		t_{ropt} , t_{fopt}	10		40	ns
Optical output pulse duration	input pulse width 217 ns, 1.152 Mbit/s	t_{opt}	200	217	240	ns
	input pulse width $t_{Txd} < 80\text{ }\mu\text{s}$	t_{opt}	20		t_{Txd}	μs
	input pulse width $t_{Txd} \geq 80\text{ }\mu\text{s}$	t_{opt}	20		85	μs
Optical overshoot					25	%

^{*)} Note: Due to this wavelength restriction compared to the IrDA spec of 850 nm to 900 nm the transmitter is able to operate as source for the standard Remote Control applications with codes as e.g. Phillips RC5/RC6[®] or RECS 80. When operated under IrDA full range conditions >110 mW/sr) the RC range to be covered is in the range from 8 m to 12 m, provided that state of the art remote control receivers are used.

Recommended Circuit Diagram

Used with a clean low impedance power supply the TFBS5614 only needs an external series current limiting resistor. However, depending on the entire system design and board layout, additional external components may be required (see figure 1).



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Figure 1. Recommended Application Circuit

The capacitor C1 is buffering the supply voltage and eliminates the inductance of the power supply line. This one should be a Tantalum or other fast capacitor to guarantee the fast rise time of the IRED current. The resistor R1 is the current limiting resistor and this is supply voltage dependent, see derating curve in figure 4, to avoid too high internal power dissipation. Vishay's transceivers integrate a sensitive receiver and a built-in power driver. The combination of both needs a careful circuit board layout. The use of thin, long, resistive and inductive wiring should be avoided.

The inputs (Txd, SD) and the output Rxd should be directly (DC) coupled to the I/O circuit.

The capacitor C2 combined with the resistor R2 is the low pass filter for smoothing the supply voltage.

R2, C1 and C2 are optional and dependent on the quality of the supply voltages V_{CCx} and injected noise. An unstable power supply with dropping voltage during transmission may reduce the sensitivity (and transmission range) of the transceiver.

The placement of these parts is critical. It is strongly recommended to position C2 as close as possible to the transceiver power supply pins. An Tantalum capacitor should be used for C1 while a ceramic capacitor is used for C2.

In addition, when connecting the described circuit to the power supply, low impedance wiring should be used.

When extended wiring is used the inductance of the power supply can cause dynamically a voltage drop at V_{CC2} . Often some power supplies are not apply to follow the fast current rise time. In that case another $4.7 \mu\text{F}$ (type, see table under C1) at V_{CC2} will be helpful.

Under extreme EMI conditions as placing an RF-transmitter antenna on top of the transceiver, we recommend to protect all inputs by a low-pass filter, as a minimum a 12 pF capacitor, especially at the Rxd port.

Keep in mind that basic RF - design rules for circuit design should be taken into account. Especially longer signal lines should not be used without termination. See e.g. "The Art of Electronics" Paul Horowitz, Winfield Hill, 1989, Cambridge University Press, ISBN: 0521370957.

Table 1.
Recommended Application Circuit Components

Component	Recommended Value	Vishay Part Number
C1	$4.7 \mu\text{F}$, 16 V	293D 475X9 016B
C2	$0.1 \mu\text{F}$, Ceramic	VJ 1206 Y 104 J XXMT
R1	5 V supply voltage: 5.6Ω s. text 0.25 W (recommended using two 2.8Ω , 0.125 W resistors in series). 3.3 V supply voltage: 2.0Ω s. text 0.25 W	e.g. 2 x CRCW-1206-2R0-F-RT1 for 3.3 V supply voltage
R2	47Ω , 0.125 W	CRCW-1206-47R0-F-RT1

I/O and Software

In the description, already different I/Os are mentioned. Different combinations are tested and the function verified with the special drivers available from the I/O suppliers. In special cases refer to the I/O manual, the Vishay application notes, or contact directly Vishay Sales, Marketing or Application.

Programming

Pulse duration Switching

After Power-on the TFBS5614 is in the default short Rxd pulse duration mode.

Some ENDECs are not able to decode short pulses as valid SIR pulses. Therefore an additional mode with extended pulse duration (same as in standard SIR transceivers) is added in TFBS5614. TFBS5614 is set to the "short output pulse" as default after power on, and after recovering from the shutdown mode (SD being active longer than 1.5 ms).

To switch the transceivers from the short Rxd pulse duration mode to the long pulse duration mode and vice versa, follow the procedure described below.

Setting to the ENDEC compatibility mode with an Rxd pulse duration of 2 μs

1. Set SD input to logic "HIGH".
2. Set Txd input to logic "HIGH". Wait $t_s \geq 200$ ns.
3. Set SD to logic "LOW" (this negative edge latches state of Txd, which determines speed setting).
4. After waiting $t_h \geq 200$ ns.

After that Txd is enabled as normal Txd input and the Rxd output is set for the longer Rxd - pulse duration mode.

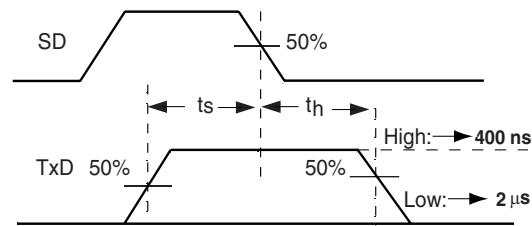
Setting back to the default mode with a 400 ns pulse duration

1. Set SD input to logic "HIGH".
2. Set Txd input to logic "LOW". Wait $t_s \geq 200$ ns.
3. Set SD to logic "LOW" (this negative edge latches state of Txd, which determines speed setting).

4. After waiting $t_h \geq 200$ ns Txd can be set to logic "LOW". The hold time of Txd is limited by the maximum allowed pulse length.

After that Txd is now enabled as normal Txd input and the Rxd output is set for the short Rxd - pulse duration mode.

The timing of the pulse duration changing procedure is quite uncritical. However, the whole change must not take more than 600 μs. See in the spec. "Shutdown Active Time Window for Programming"



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Figure 2. Timing Diagram for changing the output pulse duration

Simplified Method

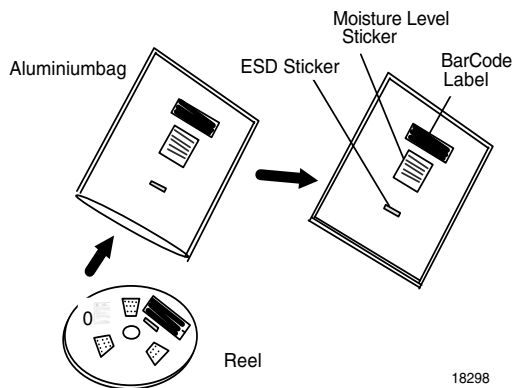
Setting the device to the long pulse duration is simply applying a short active (less than 600 μs) pulse to SD. In any case a short SD pulse will force the device to leave the default mode and go the compatibility mode. Vice versa applying a 1.5 ms (minimum) pulse at SD will cause the device to go back to the default mode by activating a power-on-reset and setting the device to the default short pulse mode. This simplified method takes more time but may be easier to handle.

Table 2.
Truth table

Inputs			Outputs		Remark
SD	Txd	Optical input Irradiance mW/m ²	Rxd	Transmitter	Operation
high < 600 μs	x	x	weakly pulled (500 kΩ) to V _{CC1}	0	Time window for pulse duration setting
high > 1.5 ms	x	x	weakly pulled (500 kΩ) to V _{CC1}	0	Shutdown
low	high	x	high inactive	I _e	Transmitting
low	high > 80 μs	x	high inactive	0	Protection is active
low	low	< 4	high inactive	0	Ignoring low signals below the IrDA defined threshold for noise immunity
low	low	> Min. Detection Threshold Irradiance < Max. Detection Threshold Irradiance	low (active)	0	Response to an IrDA compliant optical input signal
low	low	> Max. Detection Threshold Irradiance	undefined	0	Overload conditions can cause unexpected outputs

Dry packing

The reel is packed in a moisture proof aluminum bag to protect the device from absorbing moisture during transportation and storage.



Recommended Method of Storage

Dry box storage is recommended as soon as the dry bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

CAUTION
This bag contains
MOISTURE - SENSITIVE DEVICES

LEVEL

4

1. Shelf life in sealed bag 12 months at <40°C and < 90% relative humidity (RH)
2. After this bag is opened devices that will be subjected to infrared reflow, vapor-phase reflow, or equivalent processing (peak package body temp. 220°C) must be:
 - 2a. Mounted within 72 hours at factory condition of < 30°C/60%RH or
 - 2b. Stored at <20% RH.
3. Devices require baking before mounting if:
Humidity Indicator Card is >20% when read at 23°C + 5°C or
2a or 2b is not met.
4. If baking is required, devices may be baked for:

192 hours at 40°C + 5°C/-0°C and <5%RH (dry air/nitrogen)	or
96 hours at 60±5°C and <5%RH	For all device containers
24 hours at 125±5°C	Not suitable for reels or tubes

Bag Seal Date: _____ (If blank, see bar code label)

Note: LEVEL defined by EIA JEDEC Standard JESD22-A113

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Acc. to JEDEC Standard JESD22-A113

Recommended Solder Profile

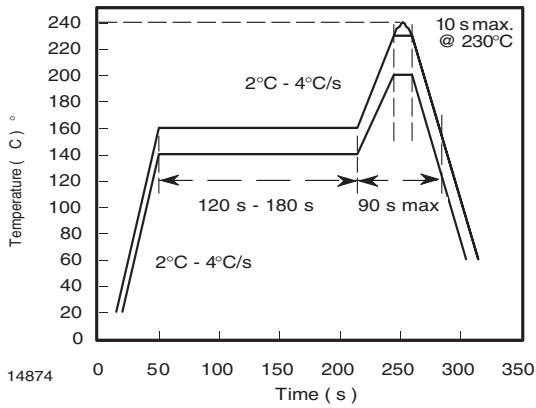


Figure 3. Recommended Solder Profile

Current Derating Diagram

Figure 4 shows the maximum operating temperature when the device is operated without external current limiting resistor. A minimum resistor of 2 Ohms is recommended from the anode of the IRED to V_{CC} . For other conditions see package drawing.

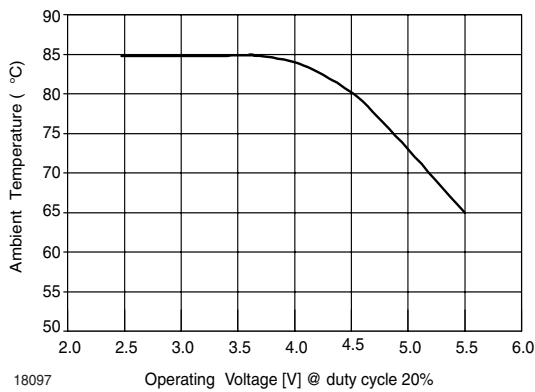


Figure 4. Temperature Derating Diagram

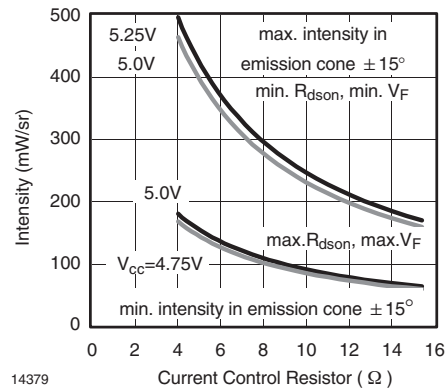


Figure 5. Intensity I_e vs. Current Control Resistor R2, 5 V Applications

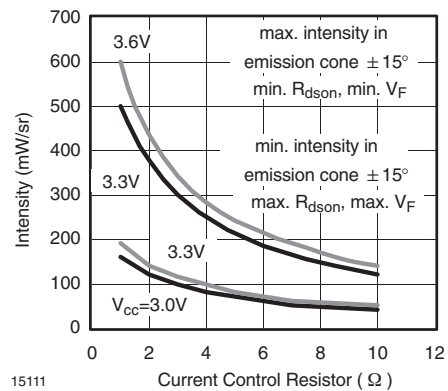
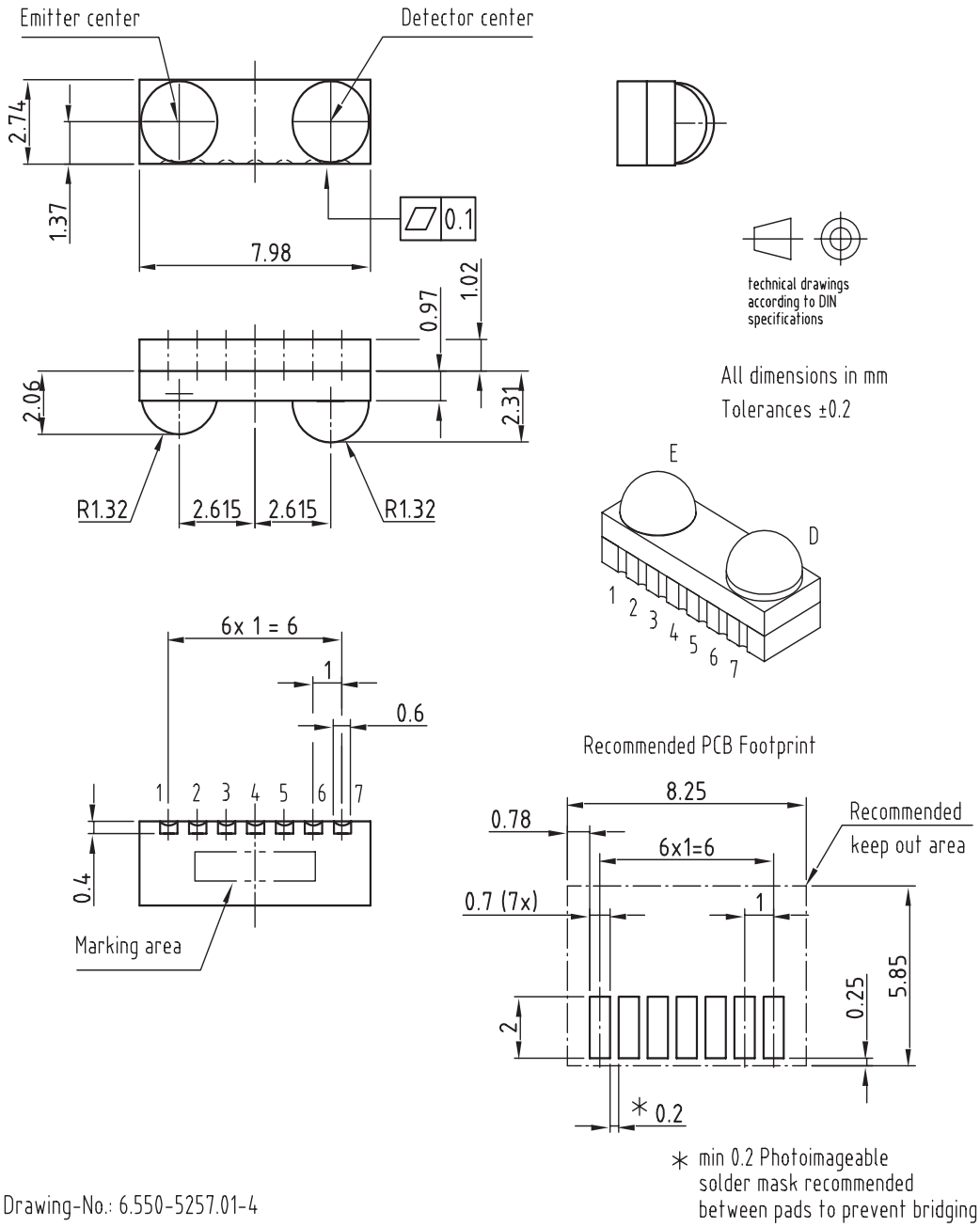


Figure 6. Intensity I_e vs. Current Control Resistor R1, 3 V Applications

Package Dimensions in mm

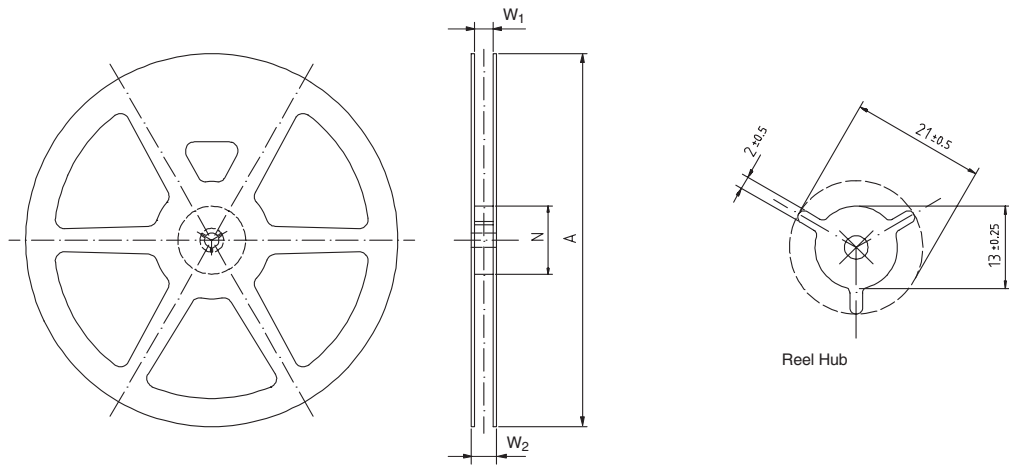


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Issue: 1; 24.06.03

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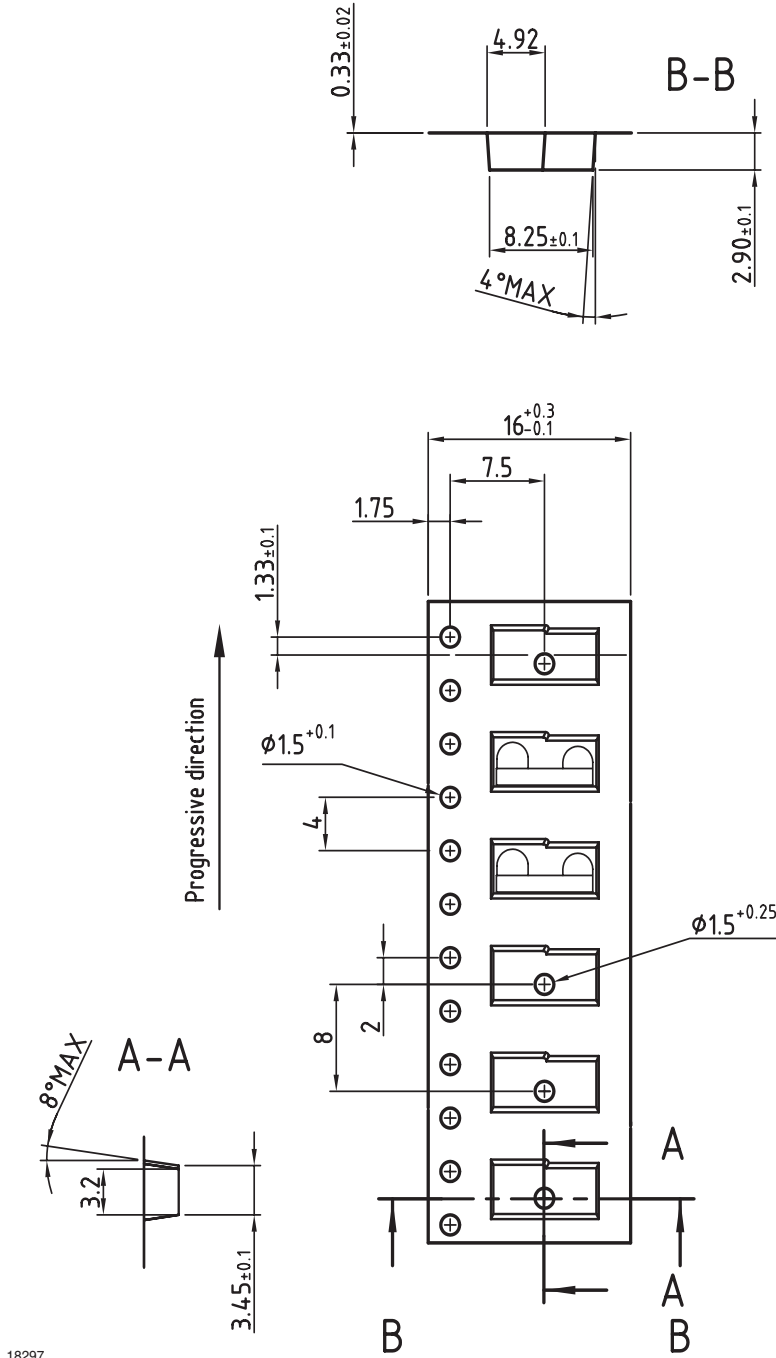
Reel Dimensions



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Tape Width	A max.	N	W_1 min.	W_2 max.	W_3 min.	W_3 max.
mm	mm	mm	mm	mm	mm	mm
16	330	50	16.4	22.4	15.9	19.4

Tape Dimensions in mm



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Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design
and may do so without further notice.**

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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