
HB526A264EN Series

1,048,576-word × 64-bit × 2-bank (Non Parity) Synchronous
Dynamic RAM Module

HITACHI

Under development

Description

The HB526A264EN belongs to 8 Byte DIMM (Dual In-line Memory Module) family, and has been developed as an optimized main memory solution for 8 Byte processor applications.

The HB526A264EN is a 1M × 64 × 2 bank synchronous dynamic RAM module, mounted 8 pieces of 16-Mbit SDRAM (HM5216805TT) sealed in TSOP package and 1 piece of serial EEPROM (24C02) for Presence Detect (PD). An outline of the HB526A264EN is 168-pin socket type package (dual lead out). Therefore, the HB526A264EN makes high density mounting possible without surface mount technology.

The HB526A264EN provides common data inputs and outputs. Decoupling capacitors are mounted beside each TSOP on the module board.

Features

- 168-pin socket type package (Dual lead out)
 - Lead pitch: 1.27 mm
- 3.3 V (±0.3 V) supply.
- Interface: LVTTL
- Clock frequency: 100 MHz/83 MHz/66 MHz
- Data bus width: × 64 (Non Parity) bit
- Single pulsed $\overline{\text{RAS}}$
- 4 Banks can operate simultaneously and independently
- Burst read/write operation and burst read/single write operation capability
- Programmable burst length: 1/2/4/8/Full page
- Programmable burst sequential: Sequential/interleave

Note: This document contains information on a product under development. Hitachi reserves the right to change or discontinue the product without notice.

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- Full page burst length capability
 - Sequential burst
 - burst stop capability
- Programmable $\overline{\text{CAS}}$ latency: 1/2/3
- 4,096 refresh cycles: 64 ms
- 2 variations of refresh
 - Auto refresh
 - Self refresh

Ordering Information

Type No.	Frequency	Package	Contact pad
HB526A264EN-10	100 MHz	168-pin dual lead out socket type	Gold
HB526A264EN-12	83 MHz		
HB526A264EN-15	66 MHz		

Pin Arrangement

