CS-8441

85mA Dual H-Bridge Odometer Driver with Divide by Select

Description

The CS-8441 is a Stepper Motor Driver that implements an H-Bridge design in order to drive two coils in an eight step sequence per revolution in the divide by 1 mode; 16 step sequence in the divide by 2 mode. The H-Bridge is capable of delivering 85mA to the load.

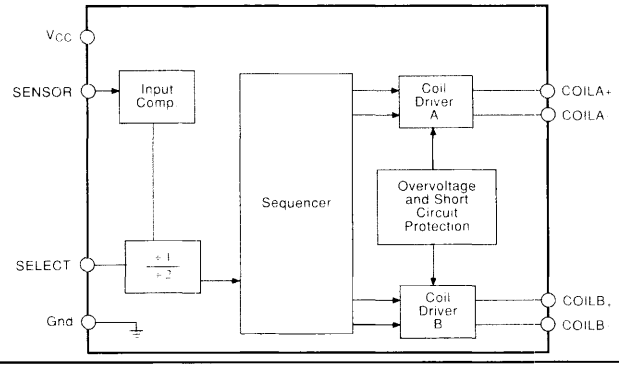
The sequencer insures that the odometer is monotonic. This sequencer is configured such that simultaneous conduction does not occur. Before each successive output sequence the part is taken through a state where both outputs are turned off individually. This tends to minimize the inductive kick back energy that the part must absorb. On chip clamp diodes are across each output to protect the part from the kick back energy that it must absorb.

Additional part protection is provided by two functions. The first being "short circuit protection". This function will protect the part in the case of a shorted or partially shorted load. The second protection function is the "overvoltage function". This function monitors the level of the supply voltage. In transient conditions such as load dump, the part will shut down, protecting itself.

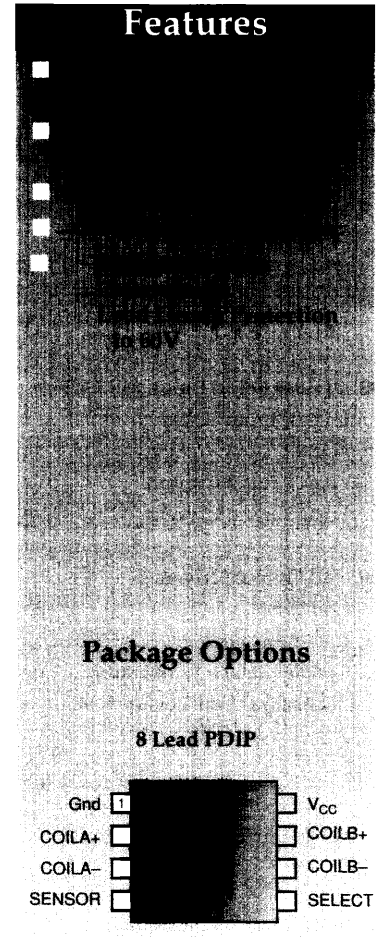
Absolute Maximum Ratings

Supply Voltage (V_{CC}) (continuous) -40°C to +105°C ..... -0.5 to 24V
(100ms pulse transient) -40°C to +105°C ..... -0.5 to 60V
Input Voltage (V_{IN}) ..... -0.3 to V_{CC} +0.3V
Operating Voltage ..... -40°C to 105°C
Storage Temperature Range (T_{STG}) ..... -65°C to 150°C
Junction Temperature Range ..... -40°C to 150°C
ESD (Human Body Model) 2kV
Lead Temperature Soldering
Wave Solder(through hole styles only) ..... 10 sec. max, 260°C peak

Block Diagram



Features



Package Options

8 Lead PDIP

Cherry Semiconductor

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■ Supply, V_{CC}					
Supply Voltage Range	-40°C ≤T_A≤105°C	6.5		15.5	VDC
	-40°C≤T_A≤25°C	6.5		24.0	VDC
	Transient Pulse, 100ms			35.0	VDC
Overvoltage Shutdown		16		23	V
■ Speed Sensor Input, SENSOR					
Input Frequency Range			0.2	1.0	kHz
Hysteresis		300	500		mVDC
Input Voltage Range		0		V_{CC}	VDC
Input Clamp Current	I Clamp at V_{IN} = 0 VDC		-0.4	-5.0	mA
■ Divider Select Input, SELECT					
Logic 0 Input Voltage				100	mVDC
Logic 0 Input Current	0V≤V_{IN}≤100mV		-1	-100	µA
■ Coil Output Drivers					
Coil Load	+25°C	198	210	222	Ω
Coil Resistance Temperature Coefficient				0.35	%/°C
De-Energized Coil Leakage Current				±100	µA
■ Short Circuit Protection					
Short Circuit Threshold I Coil A + I Coil B			275	400	mA

* Voltage across the coils shall be measured at the specific voltages, but shall also be within linearly interpolated limits.

8L PDIP		
1	Gnd	Ground connection.
3	COILA-	Output stage, when active, this pin supplies current to COIL A.
5	SELECT	Selects divide by 1 or divide by 2 mode.
7	COILB+	Output stage, when active, this pin supplies current to COIL B.

Sensor Input

SENSOR is a PNP comparator input which accepts a sine wave input or a square wave input. This input is protected from excursions above V_{CC} as well as any below ground, as long as the current is limited to 1.5mA. It has an active clamp set to zero volts to prevent negative input voltages from disrupting normal operation. The sensor input can withstand $150V_{DC}$ as long as the input current is limited to 1.5mA max using a series resistor of 100kΩ.

Coil Driver Outputs

Simultaneously energizing the source and sink on either leg is not permitted. i.e. Q1 & Q2 or Q3 & Q4 cannot be energized simultaneously.

Circuit function is not affected by inductive transients due to coil loads as specified in Transition States section.

The transition states occur as indicated in Table 1 without any intermediate states permitted.

Table 1: Transition States

Output State Table		
State	Coil A	Coil B
0	+	+
1	OFF	+
2	-	+
3	-	OFF
4	-	-
5	OFF	-
6	+	-
7	+	OFF

The polarity definition for the coil driver outputs is as follows:

Polarity	Connect Coil +	Connect Coil -
Positive (+)	V_{CC}	Gnd
Negative (-)	Gnd	V_{CC}

Divider Select Input

The speed sensor input frequency is divided by one or divided by two by connecting the divider select input, (Pin 5) as follows:

Logic 0 = divide by 2

Logic 1 = divide by 1

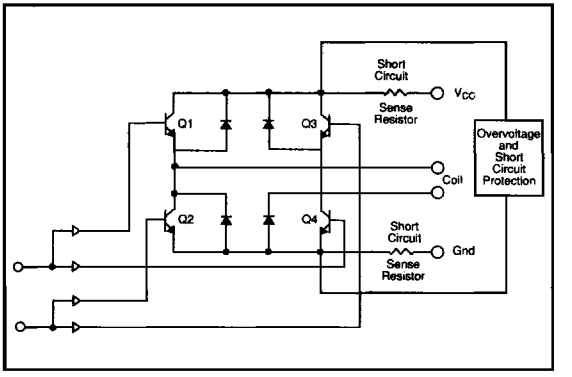


Figure 1: Coil Driver Output

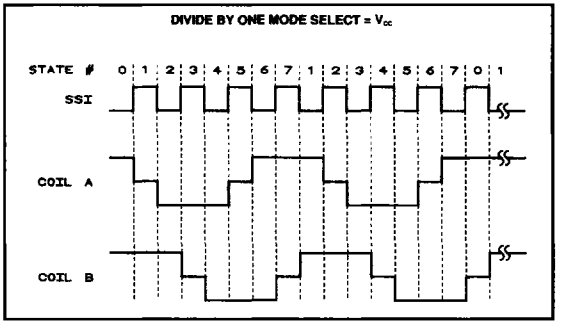


Figure 2: Divide by 1 SELECT Mode

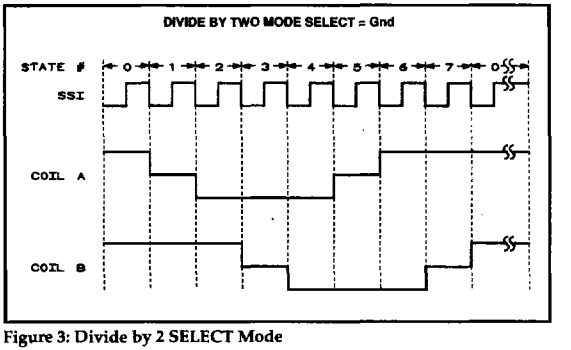
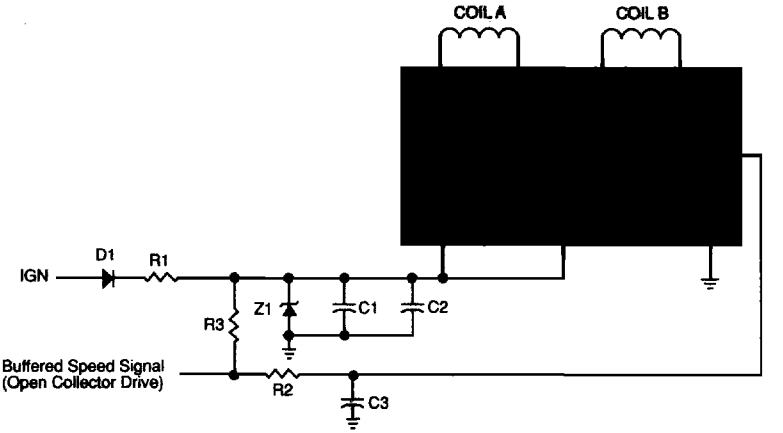


Figure 3: Divide by 2 SELECT Mode

C1 - 10µF
C2 - 0.1µF
C3 - 0.01µF
R1 - 3.9Ω, 500mW
R2 - 100kΩ
R3 - 15kΩ
D1 - 1A, 600PIV
Z1 - 50V, 500mW
COIL A - 210±12Ω, 80mH
COIL B - 210±12Ω, 80mH



Package Specification

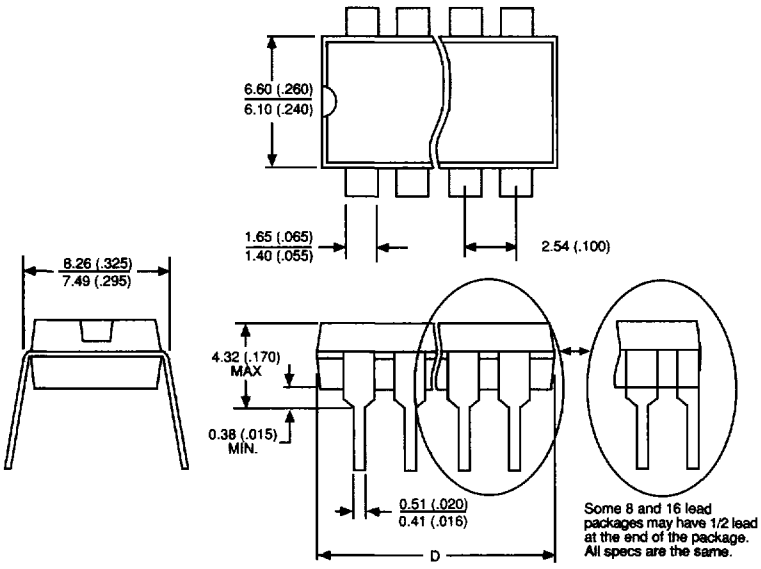
PACKAGE DIMENSIONS in mm (INCHES)

Lead Count	D			
	Metric		English	
	Max	Min	Max	Min
8L PDIP	9.40	9.14	.370	.360

PACKAGE THERMAL DATA

Thermal Data		8 Lead PDIP	
$R_{\Theta JC}$	typ	52	°C/W
$R_{\Theta JA}$	typ	100	°C/W

8 Lead PDIP



Ordering Information

Part Number	Description
CS-8441N8	8 Lead PDIP