

PRELIMINARY
 Notice: This is not a final specification. Some
 parametric limits are subject to change.

MITSUBISHI HIGH SPEED CMOS M74HC377P/FP/DWP

OCTAL D-TYPE FLIP-FLOP WITH COMMON CLOCK AND ENABLE

DESCRIPTION

The M74HC377 is a semiconductor integrated circuit consisting of eight positive-edge triggered D-type flip flops with common clock and enable inputs.

FEATURES

- High-speed: (clock frequency) 40MHz typ.
 ($C_L=15\text{pF}$, $V_{CC}=5\text{V}$)
- Low power dissipation: $20\mu\text{W}/\text{package}$, max
 ($V_{CC}=5\text{V}$, $T_a=25^\circ\text{C}$, quiescent state)
- High noise margin: 30% of V_{CC} , min ($V_{CC}=4.5\text{V}$, 6V)
- Capable of driving 10 74LSTTL loads
- Wide operating voltage range: $V_{CC}=2\sim 6\text{V}$
- Wide operating temperature range: $T_a=-40\sim +85^\circ\text{C}$

APPLICATION

General purpose, for use in industrial and consumer digital equipment.

FUNCTIONAL DESCRIPTION

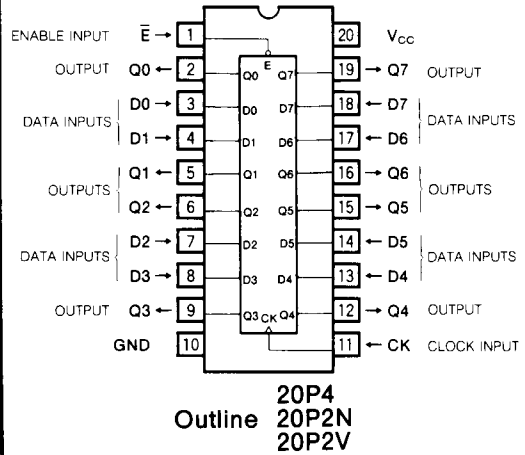
Use of silicon gate technology allows the M74HC377 to maintain the low power dissipation and high noise margin characteristics of the standard CMOS logic 4000B series while giving high-speed performance equivalent to the 74LS377.

The M74HC377 contains eight D-type flip flops with common clock and enable inputs.

When CK changes from low-level and high-level, the signals just previously input at D appears at outputs Q in accordance with function table given.

When \bar{E} is high, the output does not change, irrespective of other inputs. Changing \bar{E} from high to low, or from low to high does not cause misoperation.

PIN CONFIGURATION (TOP VIEW)



FUNCTION TABLE (Note 1)

Inputs			Output
\bar{E}	CK	D	Q
H	X	X	Q^0
L	↑	H	H
L	↑	L	L
X	L	X	Q^0

Note 1 : ↑ : Change from low to high
 Q^0 : Output state Q before input conditions are set
 X : Irrelevant

MITSUBISHI HIGH SPEED CMOS
PACKAGE OUTLINES

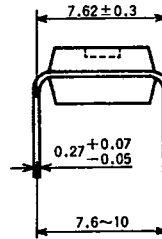
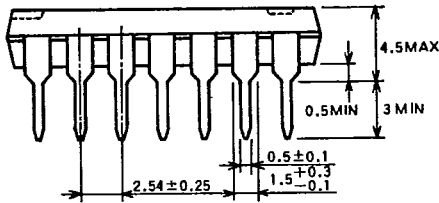
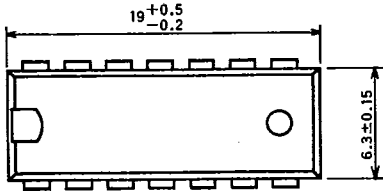
6249827 MITSUBISHI (DGTL LOGIC)

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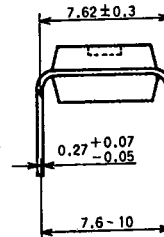
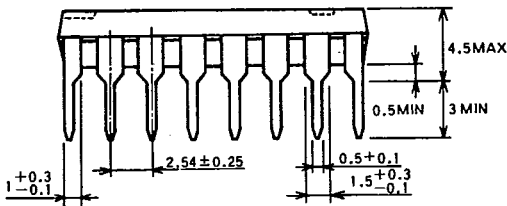
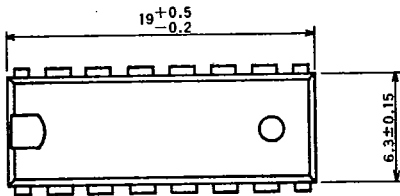
TYPE 14P4 14-PIN MOLDED PLASTIC DIP

Dimension in mm



TYPE 16P4 16-PIN MOLDED PLASTIC DIP

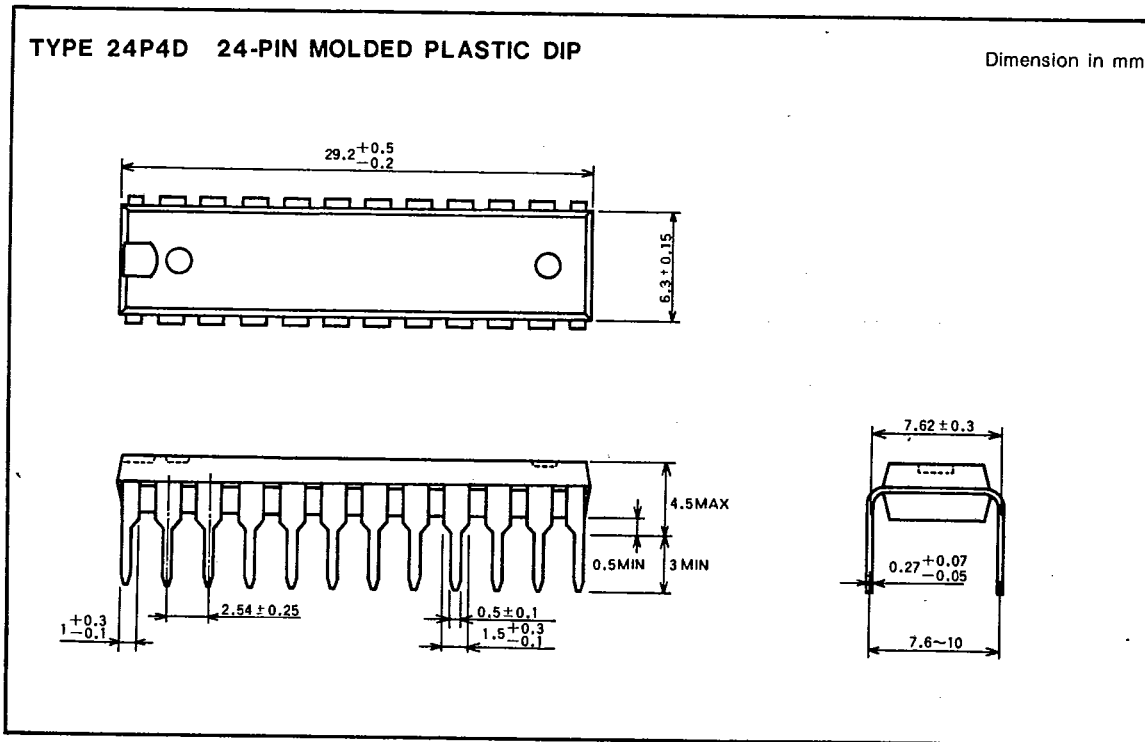
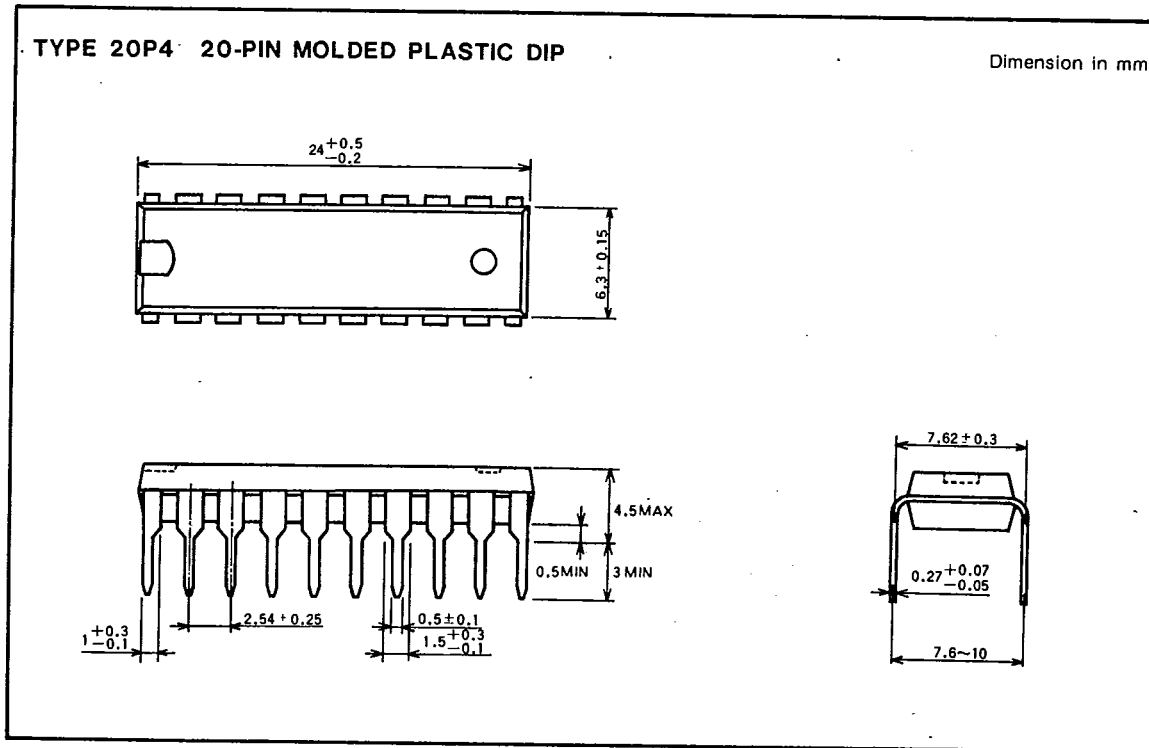
Dimension in mm



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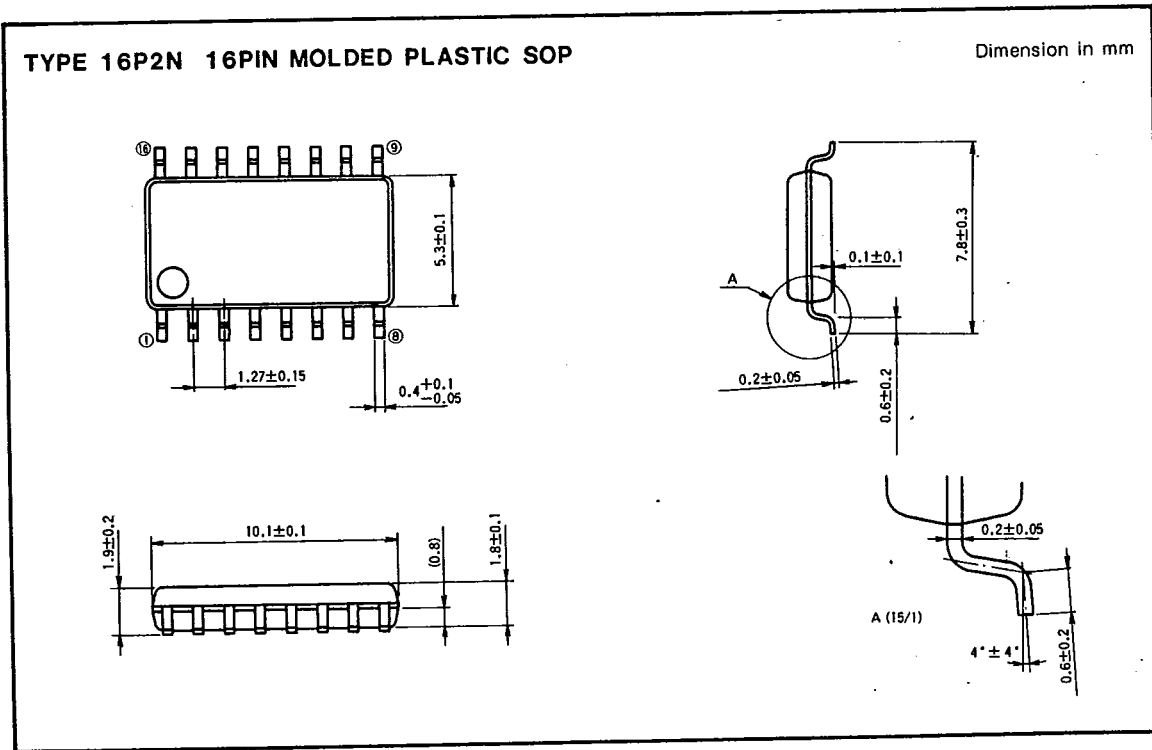
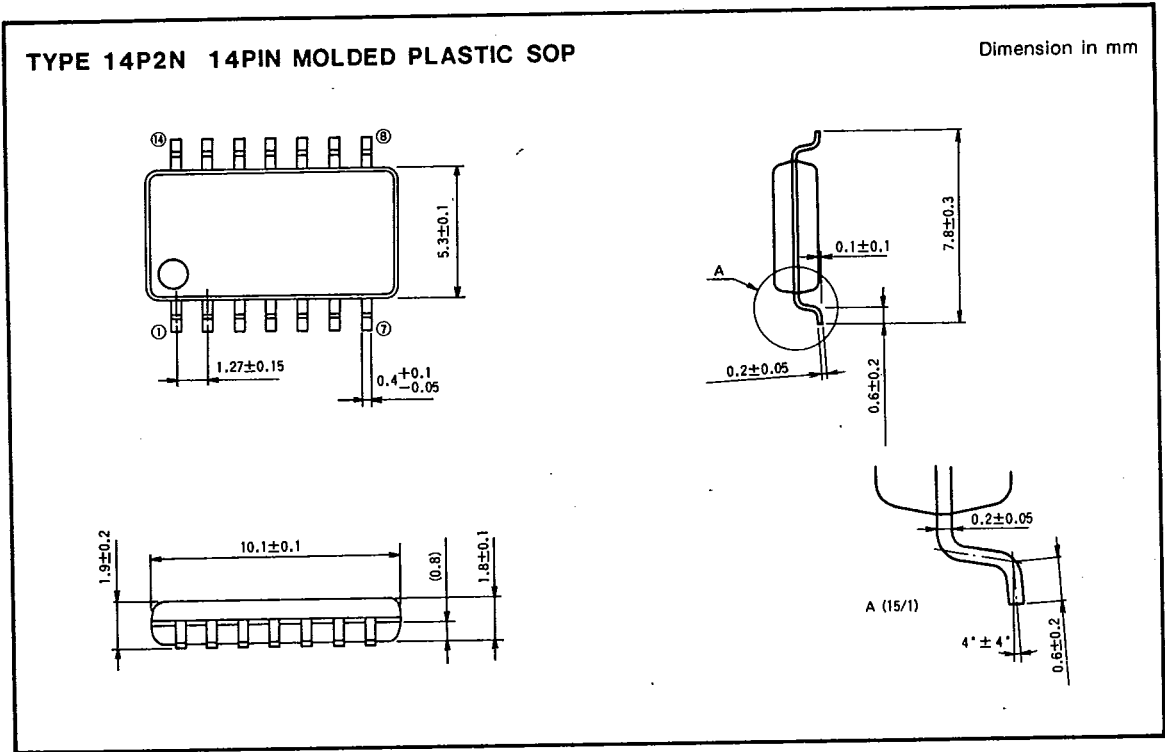


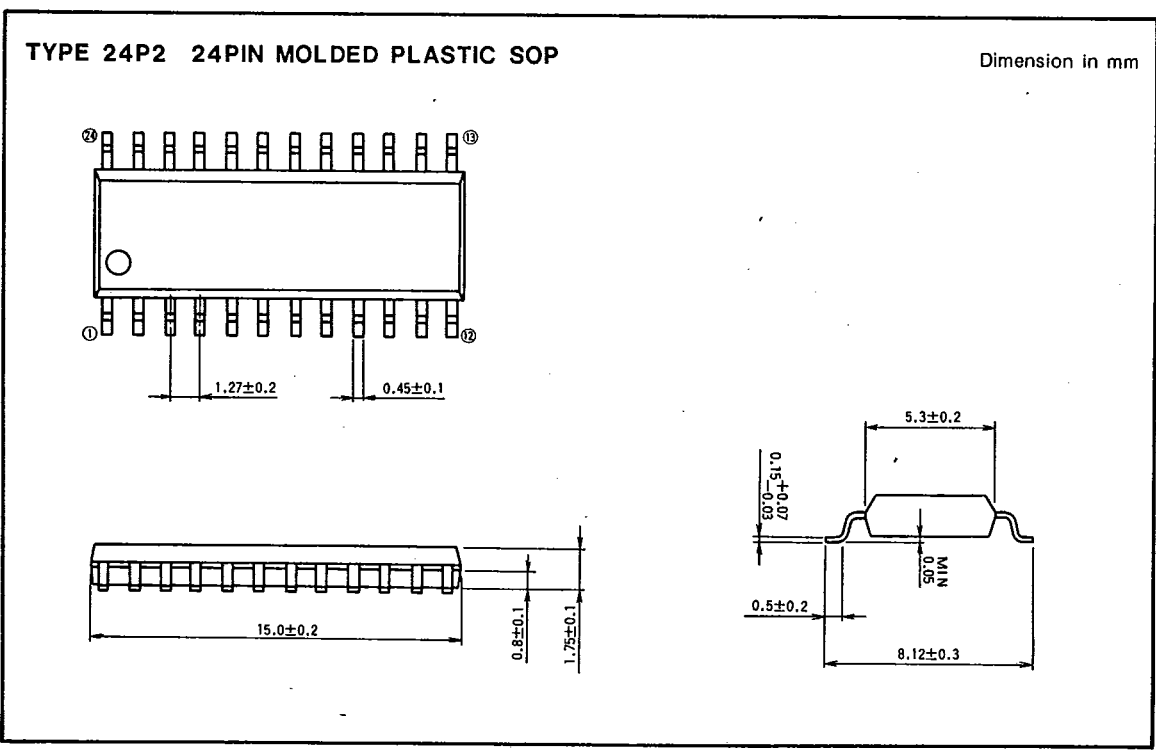
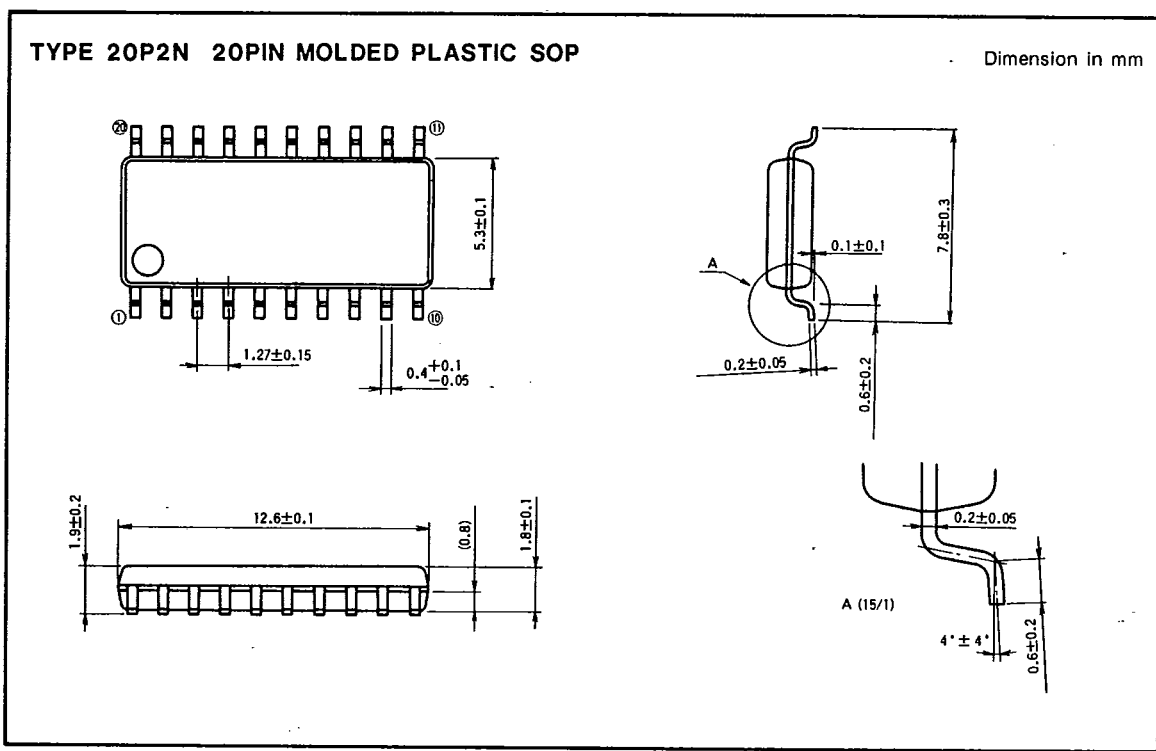
MITSUBISHI ELECTRIC CO. TOKYO, JAPAN

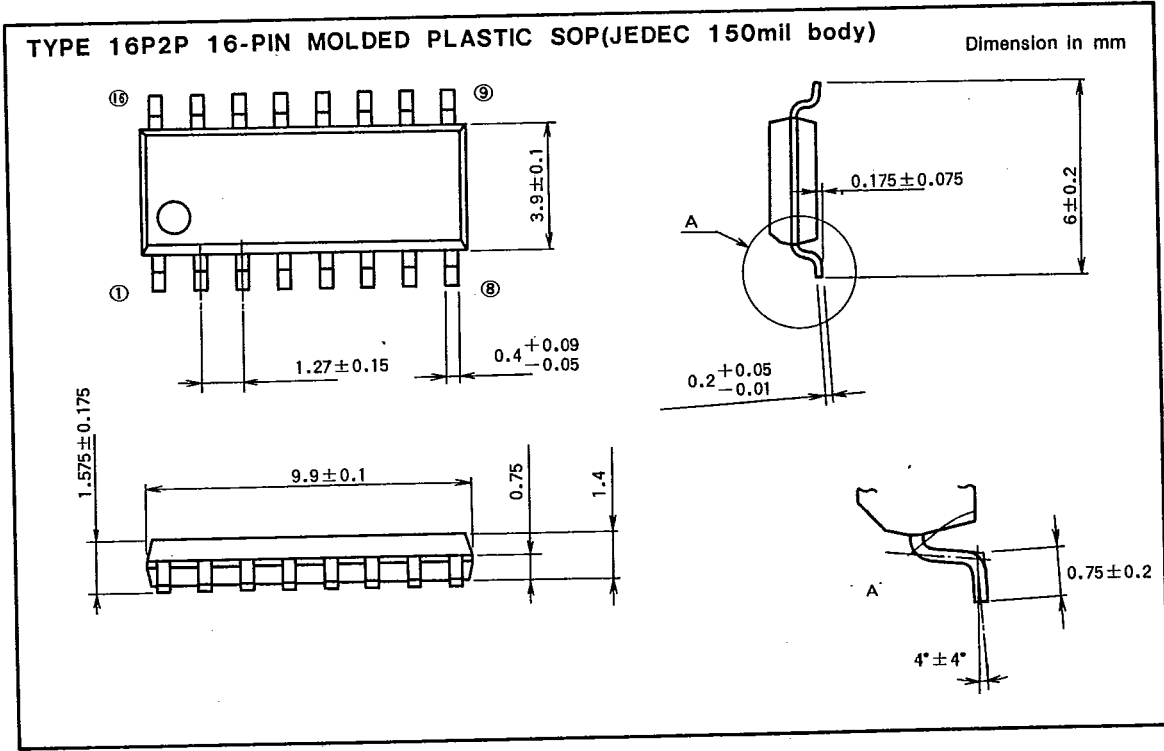
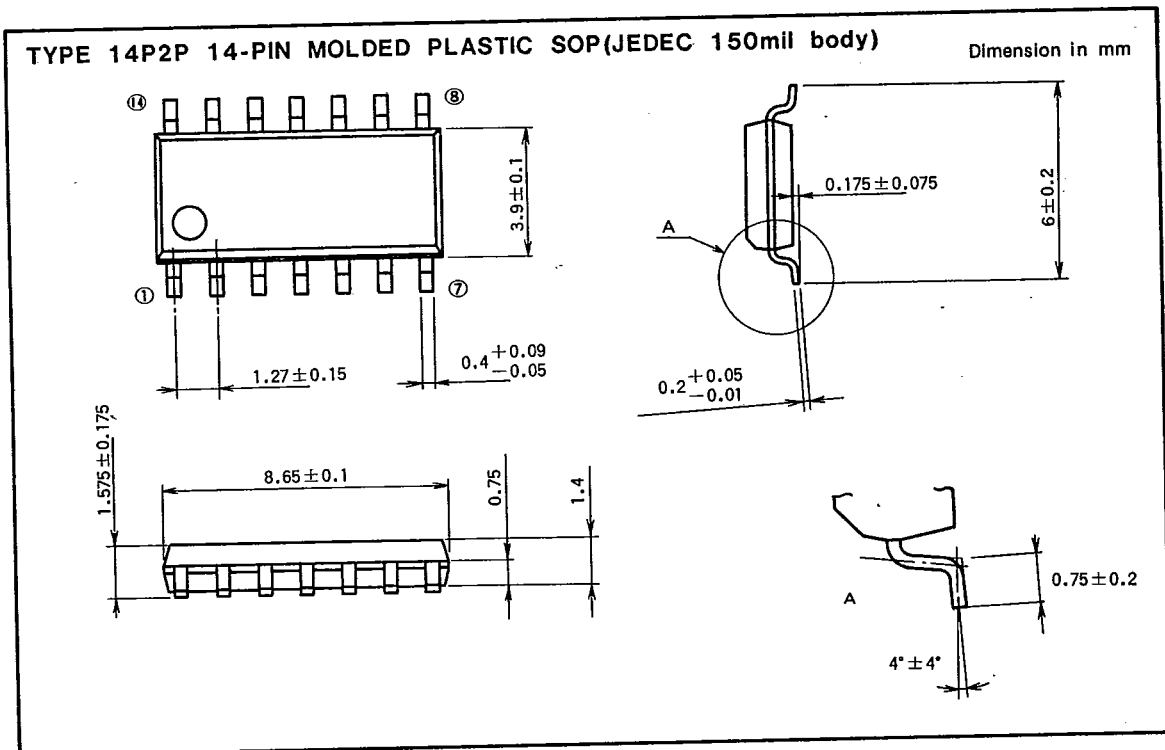
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MITSUBISHI HIGH SPEED CMOS
PACKAGE OUTLINES

6249827 MITSUBISHI (DGTL LOGIC)

91D 12854 D T-90-20

