



CXOQ/CXOQHG OSCILLATOR

16 kHz to 100 MHz
Surface Mount Quartz Crystal Oscillator

DESCRIPTION

Ultra-miniature, high performance quartz crystal oscillator designed and manufactured for high-reliability applications.

FEATURES

- 2.5 x 2.0 mm hermetically sealed ceramic package
- High shock resistance (HG version) up to 75,000 g
- Low acceleration sensitivity
- Low power consumption
- Fast start-up time
- Full military testing available
- IBIS model available
- Designed and manufactured in the USA

APPLICATIONS

Medical

- Test & Diagnostic Equipment
- Handheld Devices
- Patient Monitoring Devices

Defense and Aerospace

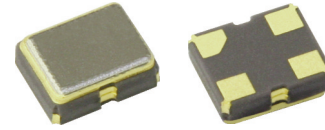
- Communications
- Navigation
- GPS

Industrial

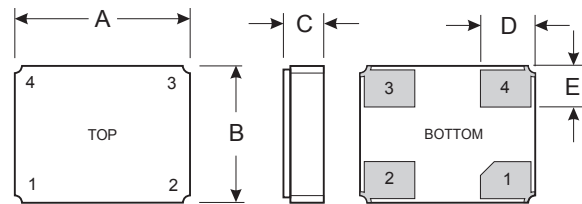
- Miniature Clock Requirements
- Handheld Instrumentation
- Transponder/Animal Migration

PACKAGING OPTIONS

- Tray Pack
- Tape and Reel (per EIA 481). See Tape and Reel datasheet 10109.



PACKAGE DIMENSIONS

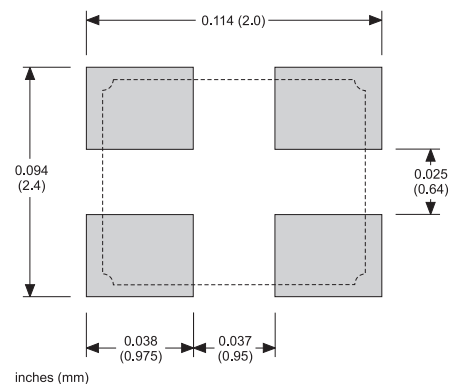


DIM	Termination	TYPICAL		MAXIMUM	
		inches	mm	inches	mm
A		0.098	2.50	0.102	2.60
B		0.079	2.00	0.083	2.10
C	SM1	0.035	0.89	0.039	1.00
	SM3/SM5	0.040	1.02	0.048	1.22
D		0.026	0.67	0.027	0.69
E		0.022	0.57	0.023	0.59

PIN CONNECTIONS

1. Output Enable/Disable (E) or no connection (N)
2. Ground (Connected to Lid)
3. Output
4. V_{DD}

SUGGESTED LAND PATTERN



10190 Rev C



SPECIFICATIONS

Specifications are typical at 25°C unless otherwise noted. Specifications are subject to change without notice. Tighter specifications available.

Frequency ¹	32.768 kHz		400 kHz to 100 MHz		
Supply Voltage	1.8V to 5.0V ± 10%		1.8V to 5.0V ± 10%		
Calibration Tolerance ²	±100 ppm to ±25 ppm				
Frequency-Temperature Stability ^{3,4}	±50 ppm to ±10 ppm (Commercial) ±100 ppm to ±20 ppm (Industrial) ±100 ppm to ±30 ppm (Military)				
Typical Supply Current (mA)	<u>1.8V</u> 0.115	<u>3.3V</u> 0.125	24 MHz 32 MHz 50 MHz	<u>1.8V</u> 1.5 2.0 3.0	<u>3.3V</u> 3.0 3.0 6.0
Output Load (CMOS) ⁵	15 pF				
Start-up Time (ms)	1 MAX		5 MAX		
Rise/Fall Time (ns) ⁶	14 MAX		6 MAX		
Duty Cycle	45% MIN 55% MAX				
Aging, First Year	3 ppm MAX				
Shock Survival	STD: 5,000 g, 0.3 ms, 1/2 sine HG: 75,000 g, 0.5 ms, 1/2 sine				
Vibration Survival ⁷	20 g, 10-2,000 Hz swept sine				
Operating Temperature Range ⁴	-10°C to +70°C (Commercial) -40°C to +85°C (Industrial) -55°C to +125°C (Military)				
Storage Temperature Range ⁴	-55°C to +125°C				
Max Process Temperature	260°C for 20 seconds				
Max Supply Voltage V _{DD} ⁸	-0.3V to 7.0V		-0.5V to 7.0V		
Moisture Sensitivity Level (MSL)	This product is hermetically sealed and is not moisture sensitive.				

1. Not all frequencies available at all voltages. Contact factory.
2. Tighter tolerances available.
3. Does not include calibration tolerance. Tighter tolerances available.
4. Broader temperature ranges available. Contact factory.
5. Higher CMOS loads and TTL loads available. Contact factory.
6. Maximum rise/fall time for 32.768 kHz @ 3.3V. Contact factory for rise/fall time at other voltages.
7. Per MIL-STD-202G, Method 204D, Condition D. Random vibration testing also available.
8. The supply voltage is -0.5V to 4.0V for some frequencies. Contact factory.

ENABLE/DISABLE OPTIONS (E/N)

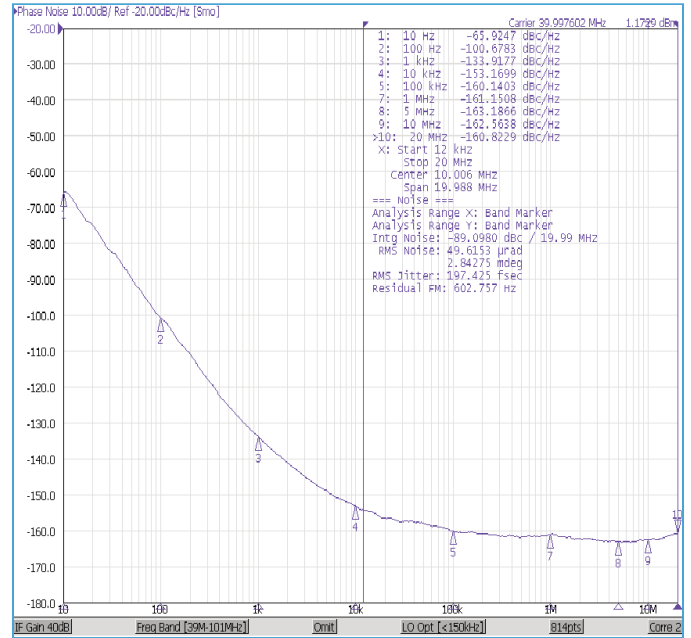
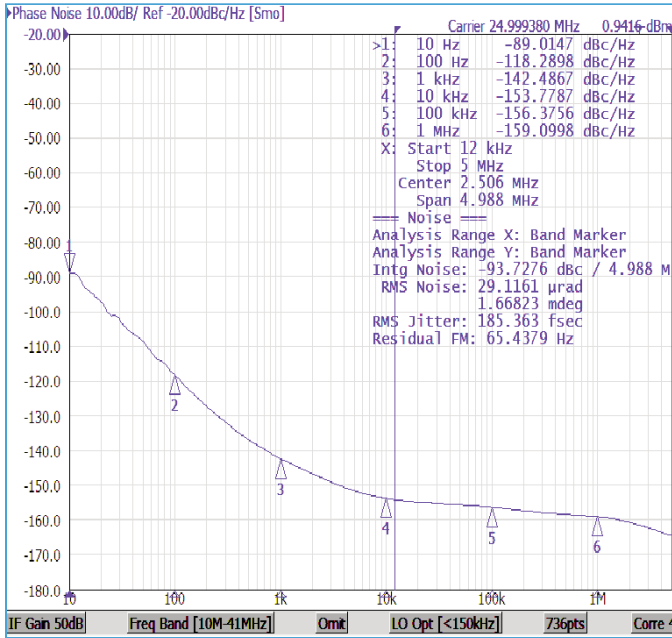
Statek offers two enable/disable options: E and N. The E-version has a Tri-State output and stops oscillating internally when the output is put into the high Z state. The N-version does not have PIN 1 connected internally and so has no enable/disable capability. The following table describes the Enable/Disable option E.

ENABLE/DISABLE OPTION E FUNCTION TABLE

	Enable (Pin 1 High*)	Disable (Pin 1 Low)
Output	Frequency Output	High Z State
Oscillator	Oscillates	Stops
Current	Normal	Very Low

*When PIN 1 is allowed to float, it is held high by an internal pull-up resistor.

PHASE NOISE AND JITTER PERFORMANCE AT 25 MHz AND 40 MHz



Typical Phase Noise (dBc/Hz) (3.3V)		
Frequency Offset [Hz]	Clock Frequency (MHz)	
	25 MHz	40 MHz
10 Hz	-89	-66
100 Hz	-118	-101
1 kHz	-142	-134
10 kHz	-154	-153
100 kHz	-156	-160
1 MHz	-159	-161
5 MHz	-163	-163

Integrated RMS Jitter (12 kHz to 20 MHz) ¹	
Frequency	VDD = 3.3V
25 MHz	200 femtoseconds
40 MHz	200 femtoseconds

1. Upper integration frequency point is clock frequency dependent.

Period Jitter (Typical) 10,000 cycles (3.3V)		
Frequency	RMS	Peak to Peak
25 MHz	1.2 picoseconds	9.5 picoseconds
40 MHz	1.3 picoseconds	10.0 picoseconds

HOW TO ORDER STATEK CXOQ/CXOQH OSCILLATORS

