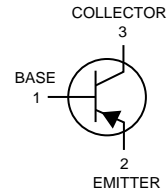


## General Purpose Transistor

### PNP Silicon

**MMBT3906WG**



#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	-40	V <sub>dc</sub>
Collector-Base Voltage	V <sub>CBO</sub>	-40	V <sub>dc</sub>
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	V <sub>dc</sub>
Collector Current-Continuous	I <sub>C</sub>	-200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max.	Unit
Total Device Dissipation FR-5 Board <sup>(1)</sup> T <sub>A</sub> =25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW / °C
Thermal Resistance Junction to Ambient	R <sub>θJA</sub>	556	°C / W
Total Device Dissipation Alumina Substrate, <sup>(2)</sup> T <sub>A</sub> =25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW / °C
Thermal Resistance Junction to Ambient	R <sub>θJA</sub>	417	°C / W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
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#### OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage <sup>(3)</sup> ( I <sub>C</sub> =1.0mAdc, I <sub>B</sub> =0 )	V <sub>(BR)CEO</sub>	-40	-	V <sub>dc</sub>
Collector-Base Breakdown Voltage ( I <sub>C</sub> = -10 uAdc, I <sub>E</sub> =0 )	V <sub>(BR)CBO</sub>	-40	-	V <sub>dc</sub>
Emitter-Base Breakdown Voltage ( I <sub>E</sub> = -10 uAdc, I <sub>C</sub> =0 )	V <sub>(BR)EBO</sub>	-5.0	-	V <sub>dc</sub>
Base Cutoff Current ( V <sub>CE</sub> = -30 Vdc, V <sub>EB</sub> = -3.0 Vdc )	I <sub>BL</sub>	-	-50	nAdc
Collector Cutoff Current ( V <sub>CE</sub> = -30 Vdc, V <sub>EB</sub> = -3.0 Vdc )	I <sub>CEX</sub>	-	-50	nAdc

**ELECTRICAL CHARACTERISTICS** ( $T_A=25^{\circ}\text{C}$  unless otherwise noted) (Continued)

Characteristic	Symbol	Min.	Max.	Unit
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**ON CHARACTERISTICS<sup>(3)</sup>**

DC Current Gain ( $I_C = -0.1 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ ) ( $I_C = -1.0 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ ) ( $I_C = -10 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ ) ( $I_C = -50 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ ) ( $I_C = -100 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ )	HFE	60 80 100 60 30	- - 300 - -	-
Collector-Emitter Saturation Voltage <sup>(3)</sup> ( $I_C = -10 \text{ mA}$ , $I_B = -1.0 \text{ mA}$ ) ( $I_C = -50 \text{ mA}$ , $I_B = -5.0 \text{ mA}$ )	$V_{CE(sat)}$	- -	-0.25 -0.4	Vdc
Base-Emitter Saturation Voltage <sup>(3)</sup> ( $I_C = -10 \text{ mA}$ , $I_B = -1.0 \text{ mA}$ ) ( $I_C = -50 \text{ mA}$ , $I_B = -5.0 \text{ mA}$ )	$V_{BE(sat)}$	-0.65 -	-0.85 -0.95	Vdc

**SMALL-SIGNAL CHARACTERISTIC**

Current-Gain-Bandwidth Product ( $I_C = -10 \text{ mA}$ , $V_{CE} = -20 \text{ V}$ , $f = 100 \text{ MHz}$ )	$f_T$	250	-	MHZ
Output Capacitance ( $V_{CB} = -5.0 \text{ V}$ , $I_E = 0$ , $f = 1.0 \text{ MHz}$ )	$C_{ob0}$	-	4.5	pF
Input Capacitance ( $V_{EB} = -0.5 \text{ V}$ , $I_C = 0$ , $f = 1.0 \text{ MHz}$ )	$C_{ib0}$	-	10	pF
Input Impedance ( $V_{CE} = -10 \text{ V}$ , $I_C = -1.0 \text{ mA}$ , $f = 1.0 \text{ kHz}$ )	$h_{ie}$	2.0	12	k ohms
Voltage Feedback Ratio ( $V_{CE} = -10 \text{ V}$ , $I_C = -1.0 \text{ mA}$ , $f = 1.0 \text{ kHz}$ )	$h_{re}$	0.1	10	$\times 10^{-4}$
Small-Signal Current Gain ( $V_{CE} = -10 \text{ V}$ , $I_C = -1.0 \text{ mA}$ , $f = 1.0 \text{ kHz}$ )	$h_{fe}$	100	400	-
Output Admittance ( $V_{CE} = -10 \text{ V}$ , $I_C = -1.0 \text{ mA}$ , $f = 1.0 \text{ kHz}$ )	$h_{oe}$	3.0	60	$\mu \text{ mhos}$
Noise Figure ( $V_{CE} = -5.0 \text{ V}$ , $I_C = -100 \mu \text{ A}$ , $R_S = 1.0 \text{ k ohm}$ , $f = 1.0 \text{ kHz}$ )	NF	-	4.0	dB

**SWITCHING CHARACTERISTICS**

Delay Time	( $V_{CC} = -3.0 \text{ V}$ , $V_{BE} = -0.5 \text{ V}$ , $I_C = -10 \text{ mA}$ , $I_{B1} = -1.0 \text{ mA}$ )	$t_d$	-	35	nS
Rise Time		$t_r$	-	35	
Storage Time	( $V_{CC} = -3.0 \text{ V}$ , $I_C = -10 \text{ mA}$ , $I_{B1} = I_{B2} = -1.0 \text{ mA}$ )	$t_s$	-	225	nS
Fall Time		$t_f$	-	75	

(1) FR-5=1.0 x 0.75 x 0.062in.

(2) Alumina=0.4 x 0.3 x 0.024in. 99.5% alumina.

(3) Pulse Test : Pulse Width  $\leq 300 \mu \text{S}$ , Duty Cycle  $\leq 2.0\%$ .

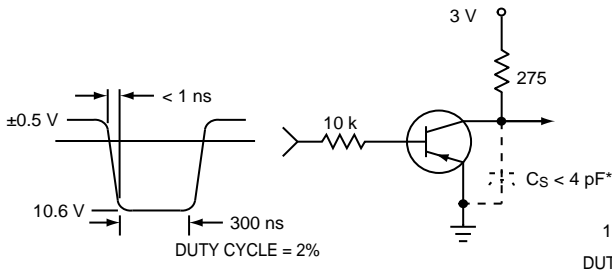


Figure 1. Delay and Rise Time Equivalent Test Circuit

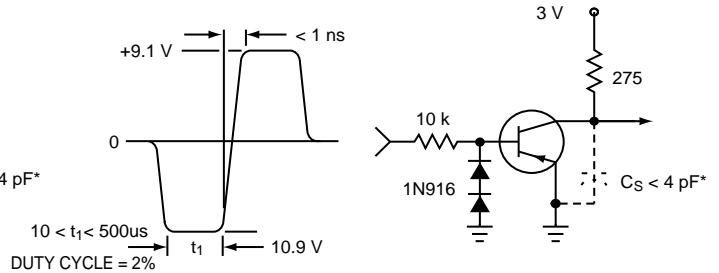


Figure 2. Storage and Fall Time Equivalent Test Circuit

\* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

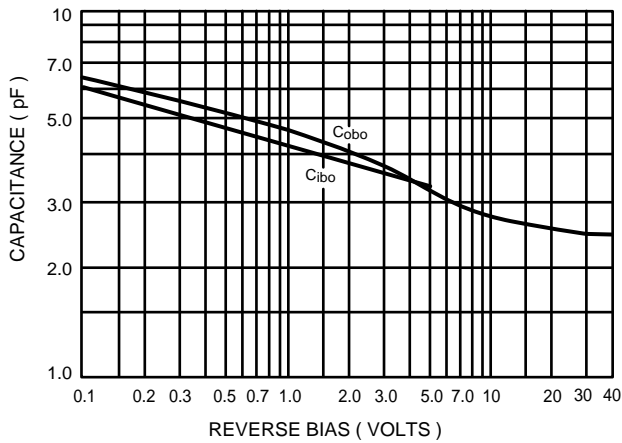


Figure 3. Capacitance

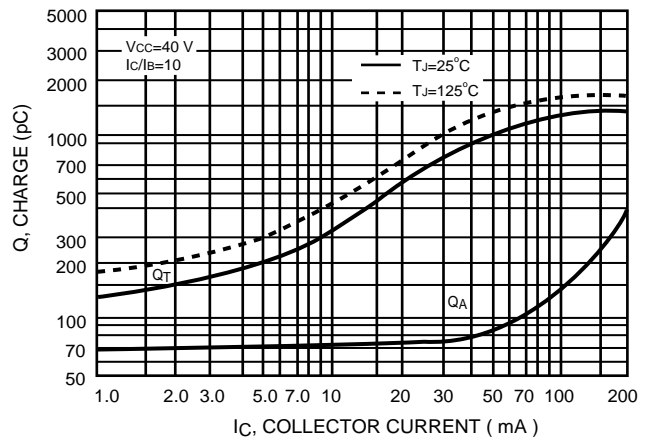


Figure 4. Charge Data

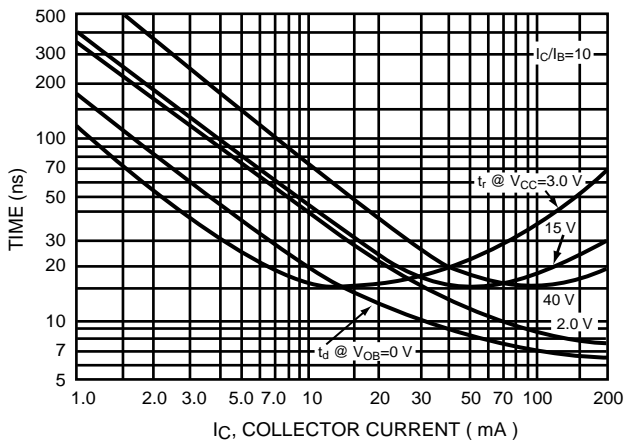


Figure 5. Turn-On Time

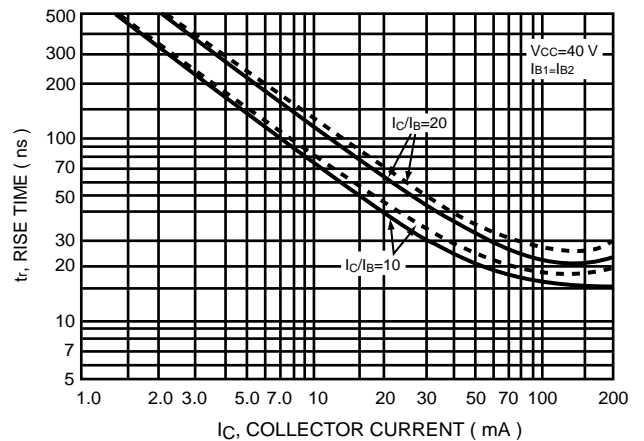


Figure 6. Fall Time

TYPICAL TRANSIENT CHARACTERISTICS  
NOISE FIGURE VARIATIONS

( $V_{CE} = -5.0V_{dc}$ ,  $T_A = 25^\circ C$ , Bandwidth=1.0Hz)

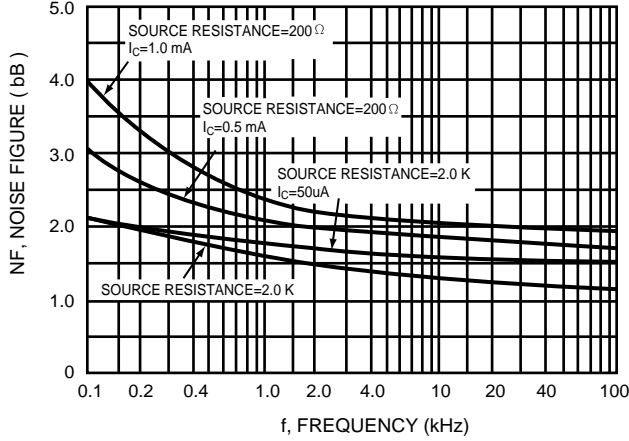


Figure 7.

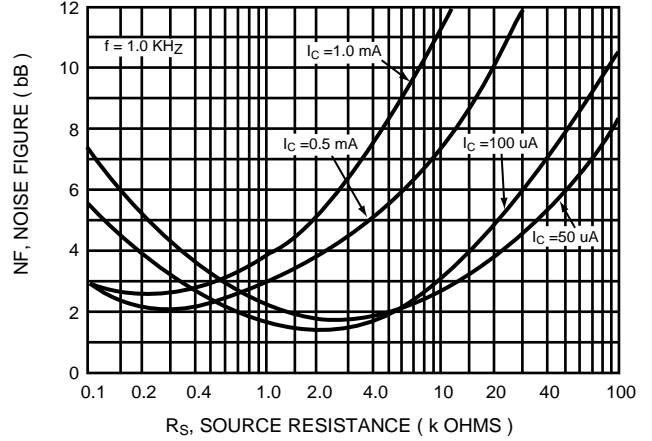


Figure 8.

h PARAMETERS

( $V_{CE} = -10V_{dc}$ ,  $f = 1.0 kHz$ ,  $T_A = 25^\circ C$ )

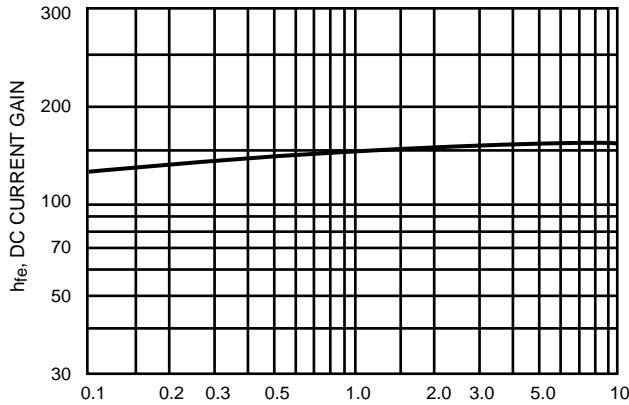


Figure 9. Current Gain

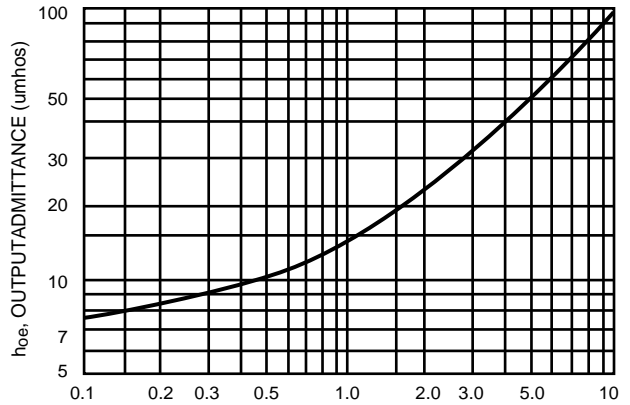


Figure 10. Output Admittance

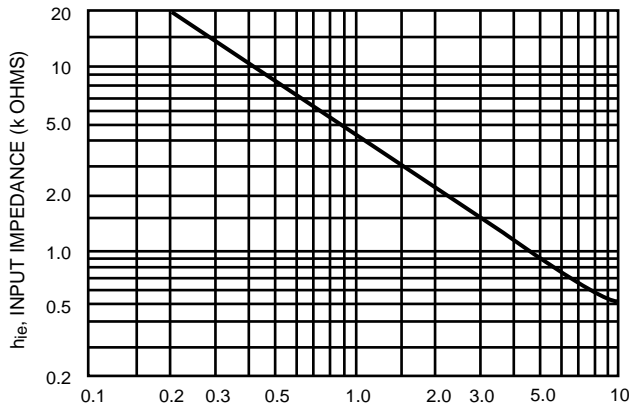


Figure 11. Input Impedance

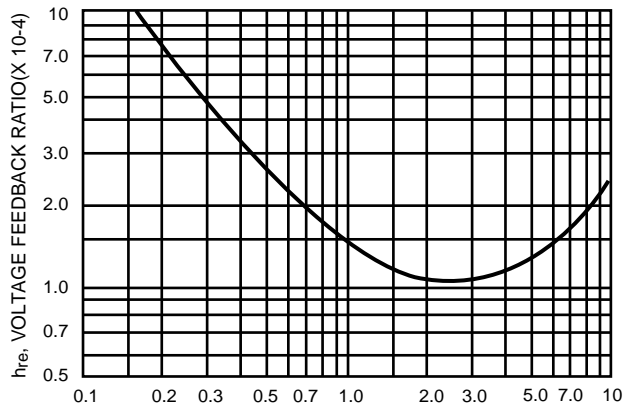


Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

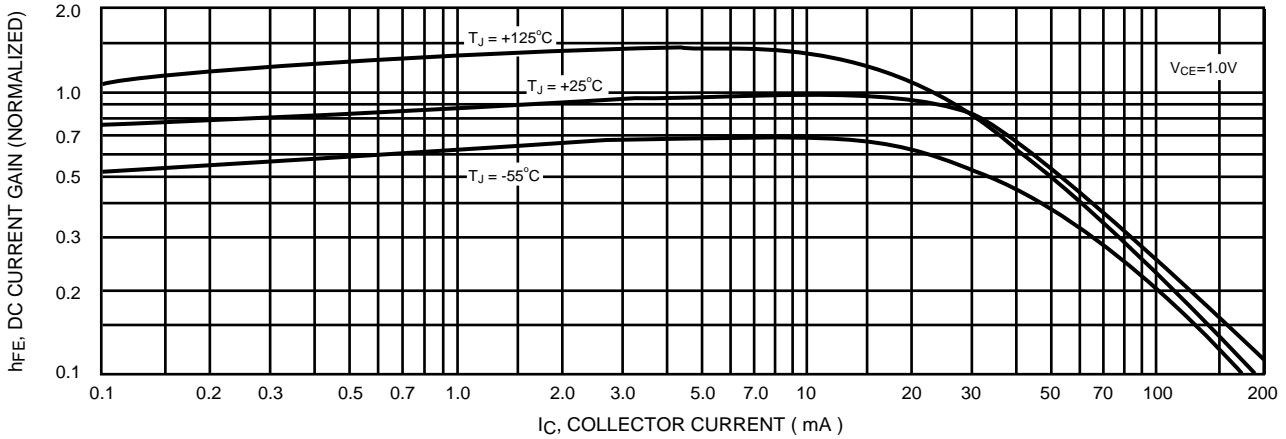


Figure 13. DC Current Gain

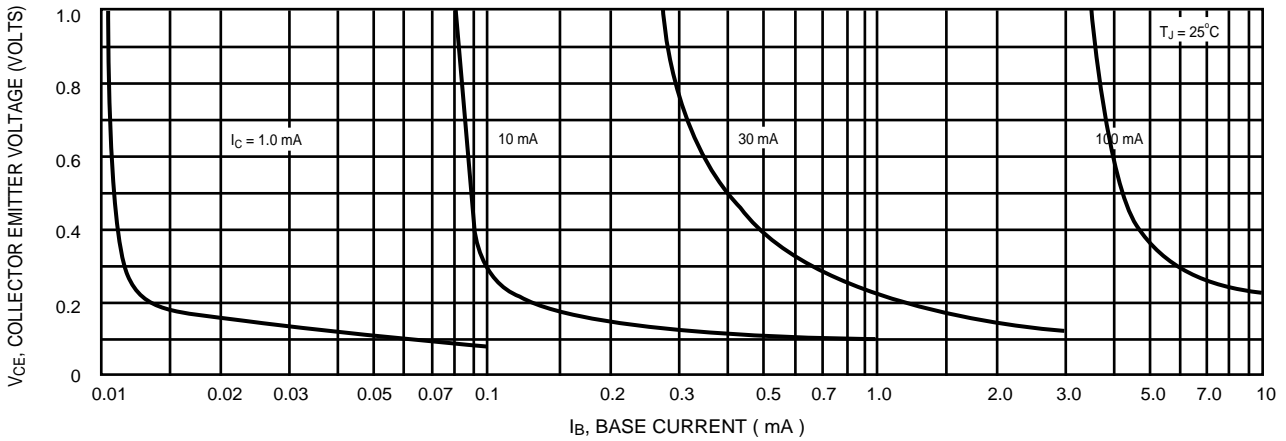


Figure 14. Collector Saturation Region

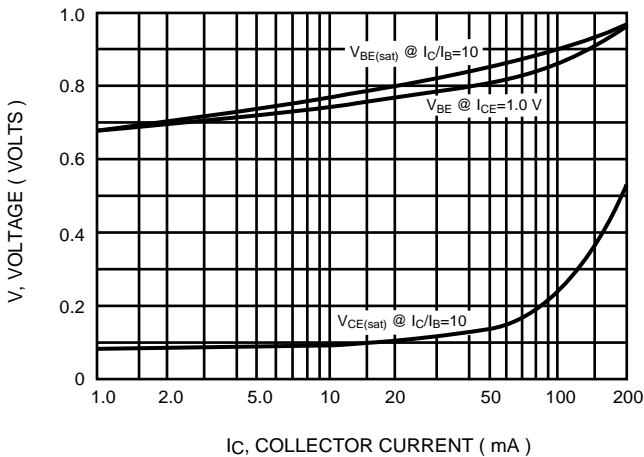


Figure 17. " ON " Voltage

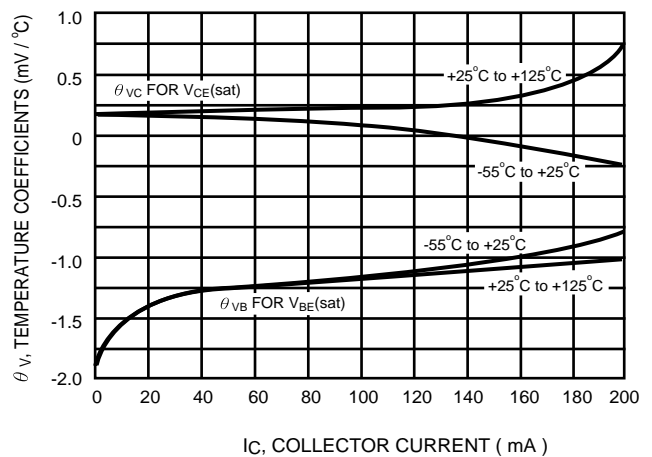


Figure 16. Temperature Coefficients