

# Bilateral Trigger Diacs

## HT Series

### General Description

Teccor's "HT" Series of bilateral trigger diacs offers a range of voltage characteristics from 27 to 70 volts.

The diac semiconductor is a full-wave or bidirectional thyristor. It is triggered from a blocking-to-conduction state for either polarity of applied voltage whenever the amplitude of applied voltage exceeds the breakover voltage rating of the diac.

The Teccor line of diacs features glass-passivated junctions to ensure long term device reliability and parameter stability. Teccor's glass offers a rugged, reliable barrier against junction contamination.

The diac specifications listed in this data sheet are for standard products. Special parameter selections such as close tolerance voltage symmetry are available. Please consult the factory for more information for custom design applications. Suffix RP signifies tape-and-reel packing. Example: HT32RP.

### Features

- Glass-chip passivation
- DO-35 trigger package
- Pre-tinned leads
- Wide voltage range selections

# Electrical Specifications

ELECTRICAL CHARACTERISTICS $T_C = 25^\circ\text{C}$						
Part Number	$V_{BO}$		$\Delta V_{BO}$	$V_{BB}$	$I_{BO}$	$I_{TRM}$
	Breakover Voltage (Forward and Reverse)		Breakover Voltage Symmetry $\Delta V_{BO} = [V_{BO} -  V_{BO} ]$	Dynamic Breakback Voltage (3) $ V_{BB} \pm  $	Peak Breakover Current at Breakover Voltage	Peak Pulse Current for 10μs 120 PPS $T_A \leq 40^\circ\text{C}$
	Volts	Volts	Volts	Volts	μAmps	Amps
	MIN	MAX	MAX	MIN	MAX	MAX
<b>HT-32</b>	<b>27</b>	<b>37</b>	<b>3 (1)</b>	<b>10 (2)</b>	<b>25</b>	<b>2.0</b>
<b>HT-32A / HT-5761</b>	<b>28</b>	<b>36</b>	<b>2 (1)</b>	<b>7 at 10mA (4)</b>	<b>25</b>	<b>2.0</b>
<b>HT-32B / HT-5761A</b>	<b>30</b>	<b>34</b>	<b>2 (1)</b>	<b>7 at 10mA (4)</b>	<b>25</b>	<b>2.0</b>
<b>HT-34B</b>	<b>32</b>	<b>36</b>	<b>2 (1)</b>	<b>10 (2)</b>	<b>25</b>	<b>2.0</b>
<b>HT-35</b>	<b>30</b>	<b>40</b>	<b>3 (1)</b>	<b>10 (2)</b>	<b>25</b>	<b>2.0</b>
<b>HT-36A / HT-5762</b>	<b>32</b>	<b>40</b>	<b>2 (1)</b>	<b>7 at 10mA (4)</b>	<b>25</b>	<b>2.0</b>
<b>HT-36B</b>	<b>34</b>	<b>38</b>	<b>2 (1)</b>	<b>10 (2)</b>	<b>25</b>	<b>2.0</b>
<b>HT-40</b>	<b>35</b>	<b>45</b>	<b>3 (1)</b>	<b>10 (2)</b>	<b>25</b>	<b>2.0</b>
<b>HT-60</b>	<b>56</b>	<b>70</b>	<b>4</b>	<b>20 (2)</b>	<b>25</b>	<b>1.5</b>

## General Notes

- Lead solder temperature is  $+230^\circ\text{C}$  max. for 10 seconds max.;  $\geq 1/16"$  (1.59mm) from case.
- See "Package Dimensions" section of this catalog.

## Electrical Specification Notes

- Breakover Voltage symmetry as close as 1.0V is available from factory on these products.
- See Figures 8.4 and 8.5 for Test Circuit and waveforms.
- Typical switching time is 900 nano-seconds measured at  $I_{PK}$  (see Figure 8.4) across a  $20\Omega$  resistor (see Figure 8.5). Switching time defined as rise time of  $I_{PK}$  between the 10% to 90% points.
- See Figure 8.7.

## Bilateral Trigger DIAC Specifications

- Maximum Ratings, Absolute-Maximum Values  
Maximum Trigger Firing Capacitance:  $0.1\mu\text{F}$   
Device Dissipation (at  $T_A = -40^\circ$  to  $+40^\circ\text{C}$ ):  $250\text{mW}$   
Derate Above  $+40^\circ\text{C}$ :  $3.6\text{mW}/^\circ\text{C}$
- Temperature Ranges  
Storage:  $-40^\circ\text{C}$  to  $+125^\circ\text{C}$   
Operating (Junction):  $-40^\circ\text{C}$  to  $+125^\circ\text{C}$
- Thermal Resistance  
Junction to Ambient ( $R_{\theta JA}$ ):  $278^\circ\text{C}/\text{W}$   
Junction to Lead ( $R_{\theta JL}$ ):  $100^\circ\text{C}/\text{W}$   
(based on maximum lead temperature of  $85^\circ\text{C}$  at  $\leq 250\text{mW}$ )

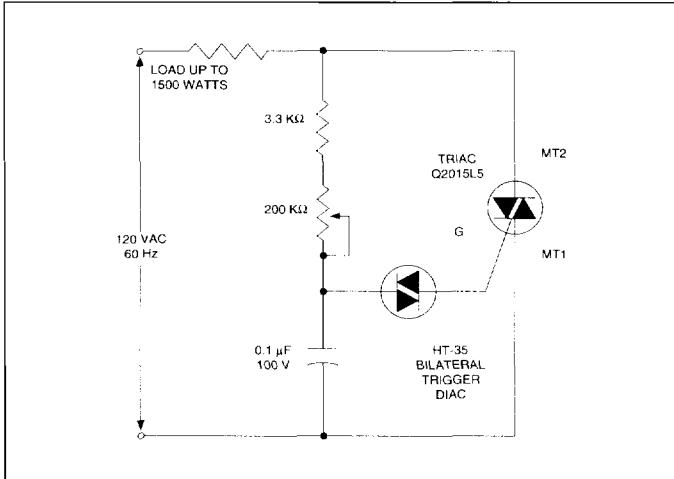


Figure 8.1 Typical Diac-Triac Full-Wave Phase Control Circuit using Lower Voltage Diacs

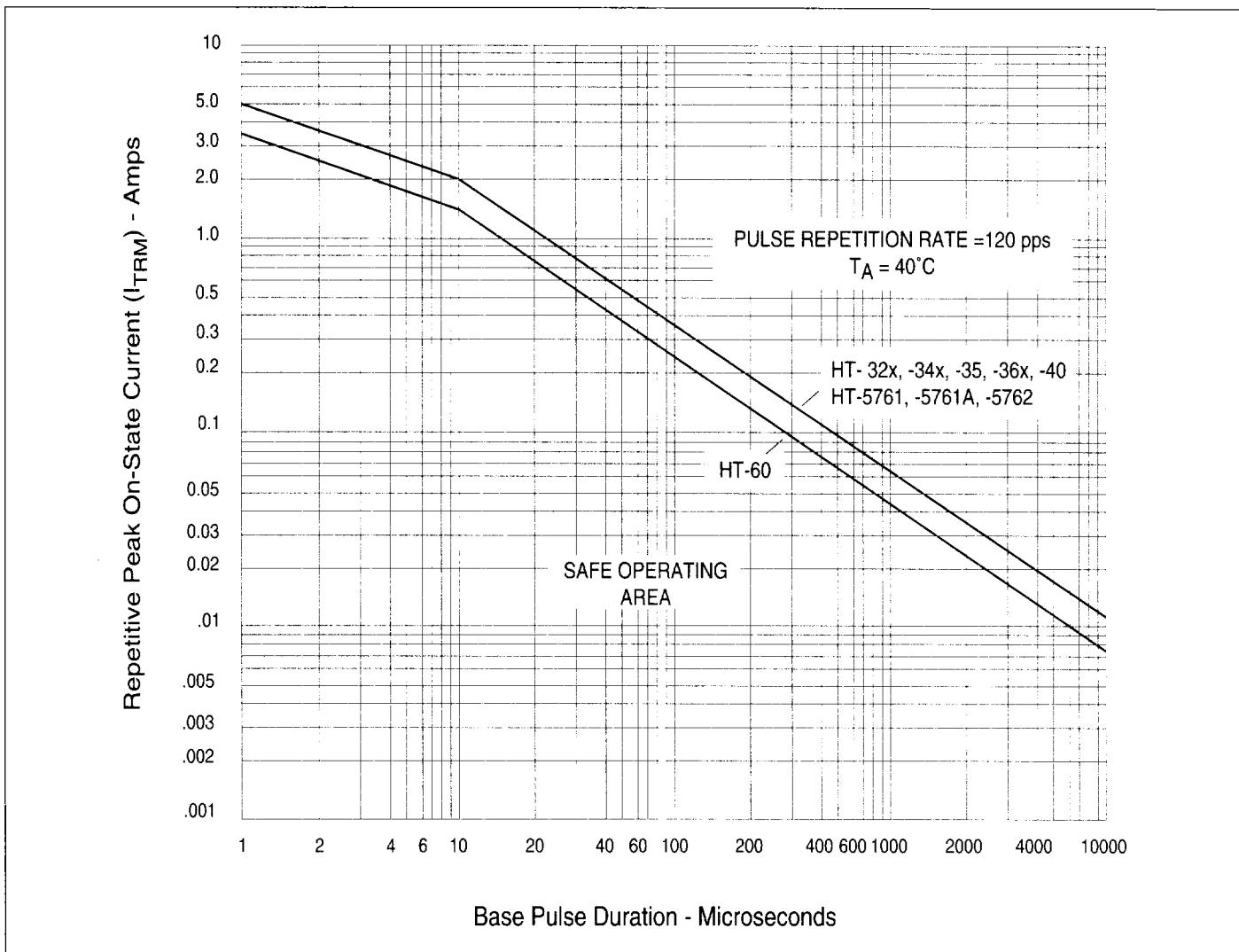


Figure 8.2 Repetitive Peak On-State Current vs Pulse Duration

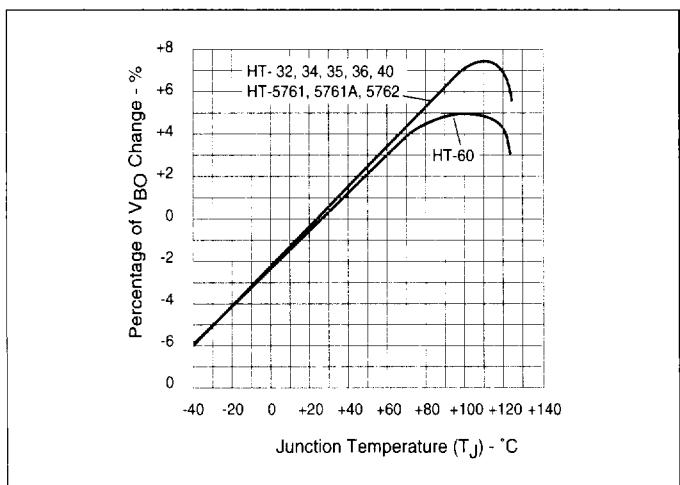


Figure 8.3 Normalized  $V_{BO}$  Change vs Junction Temperature

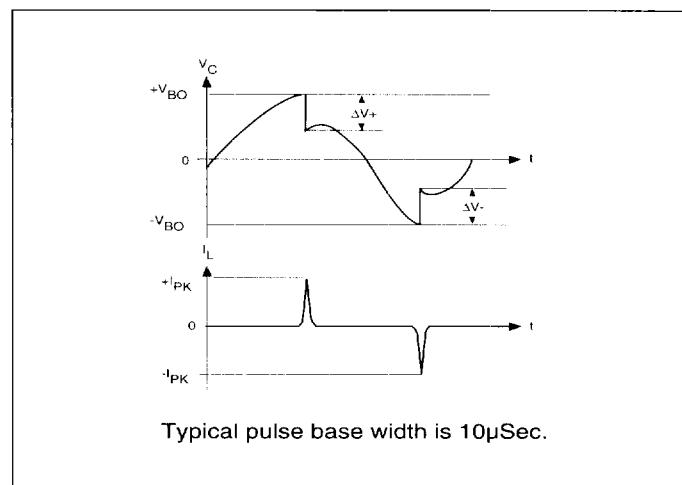


Figure 8.4 Test Circuit Waveforms (See Figure 8.5.)

# Electrical Specifications

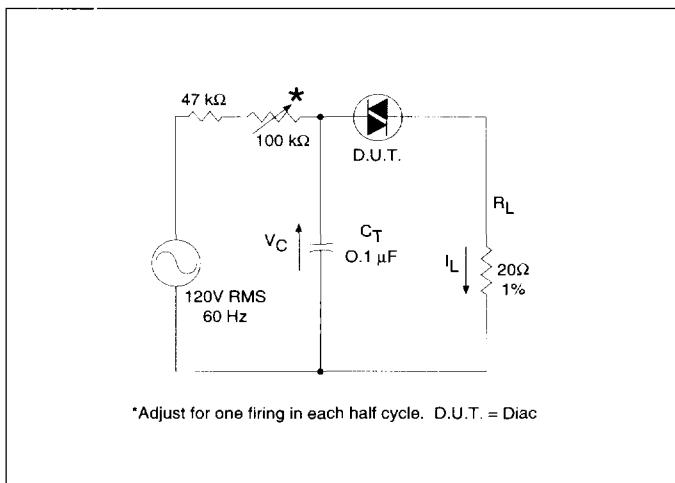


Figure 8.5 Circuit Used to Measure Diac Characteristics  
(See Figure 8.4.)

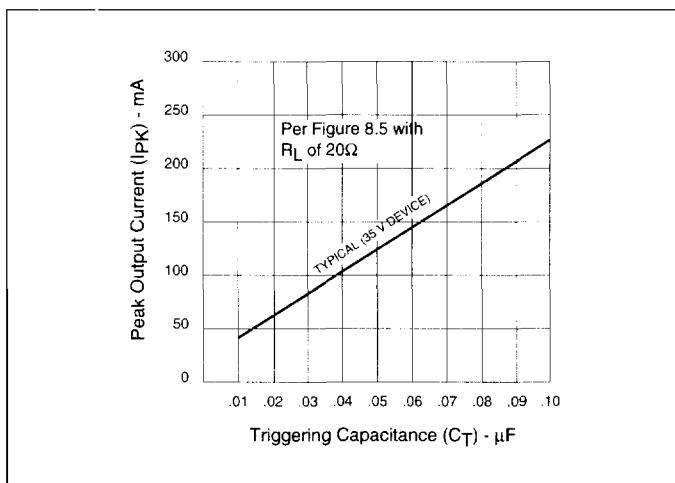


Figure 8.6 Peak Output Current vs Triggering Capacitance

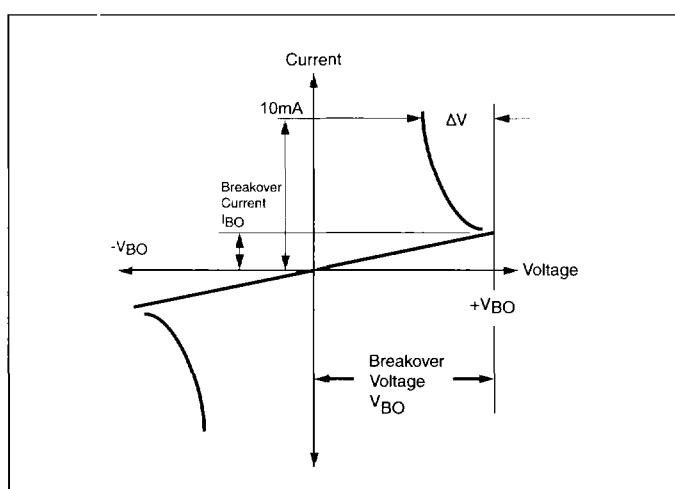


Figure 8.7 V-I Characteristics