

Product Preview
Dual Differential XOR/XNOR

The MC100LVEL08 is a dual differential XOR/XNOR gate. The MC100EL08 is pin and functionally equivalent to the MC100LVEL08 but is specified for operation at the standard 100K ECL voltage supply. The V_{BB} pin is provided for single-ended input applications. When used the V_{BB} pin should be bypassed to ground via a $0.01\mu\text{F}$ capacitor.

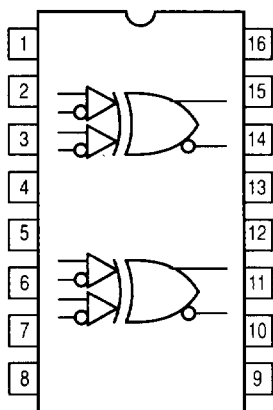
The differential inputs have special circuitry which ensures device stability under open input conditions. When both differential inputs are left open the D input will pull down to V_{EE} and the \bar{D} input will bias around $V_{CC}/2$. This situation will then force a LOW on the Q output.

- 16-Lead SOIC Package
- 400ps Propagation Delays
- Supports Both Standard and Low Voltage 100K ECL

MC100LVEL08
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Logic Diagram: 16-Lead SOIC
(Top View, Pinout TBD)



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