

Description

The μ PD7059K and μ PC7069K are subscriber line interface circuits (SLICs) for electronic exchanges. They provide battery feed to the subscriber line (B), supervision (S), and a two-wire/four-wire hybrid (H). Battery feed is constant-resistance in the μ PC7059K and constant-current in the μ PC7069K.

Features

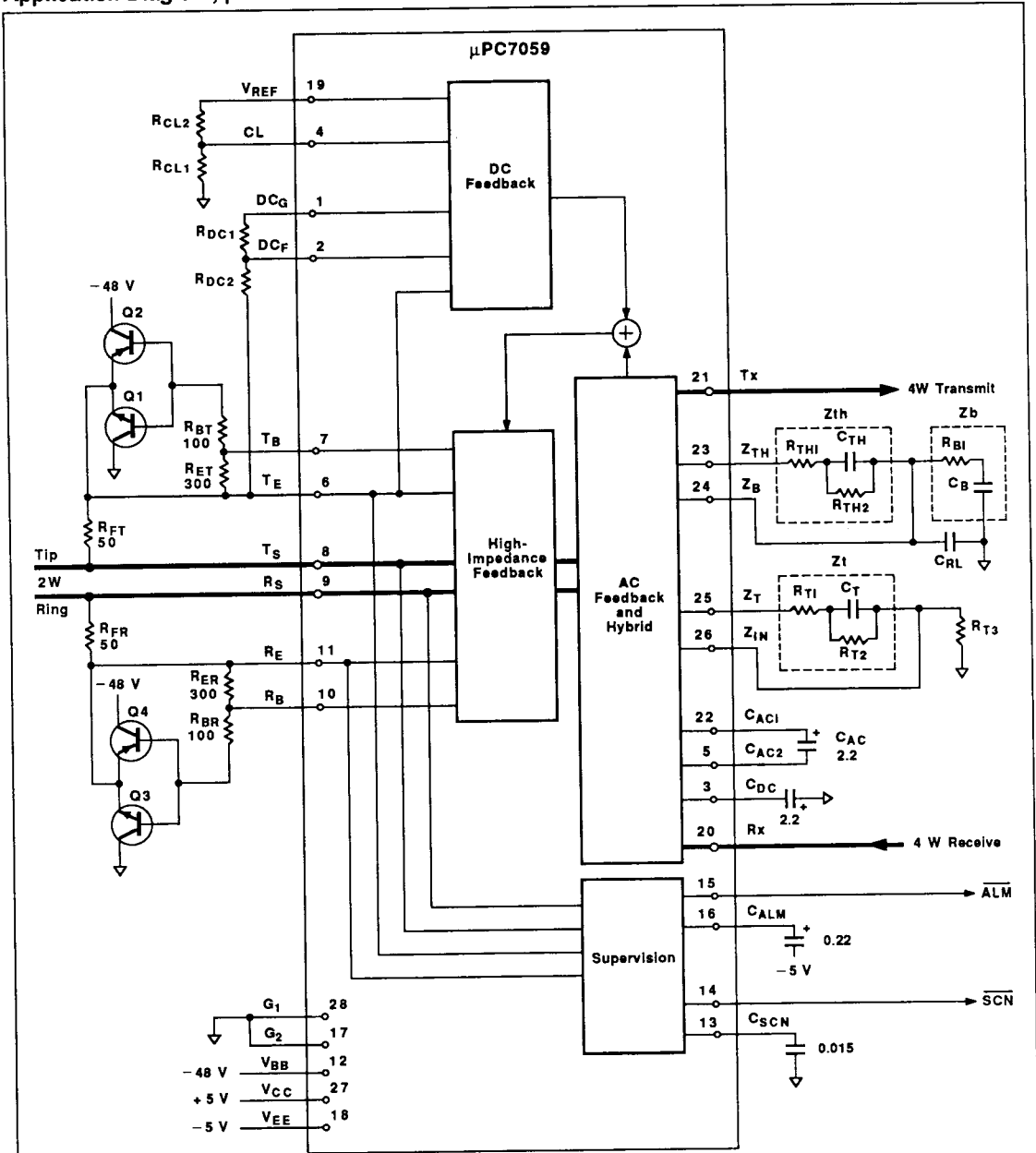
- Battery feed (-48 V) to the subscriber line
 - Constant-resistance, external setting (7059K)
 - Constant-current, external setting (7069K)
- Supervision
 - Loop detection
 - Ground-key detection
 - Circuit protection
- Hybrid
 - Terminating impedance (external setting)
 - Balancing network impedance (external setting)
- Two-wire specification
 - Longitudinal balance: 56 dB minimum (63 dB obtainable by trimming resistor R_F)
 - Longitudinal induction: 23 dBrc maximum
 - Return loss: 25 dB minimum

- Complies with CCITT recommendation Q.517
- Power consumption: 150 mW (on-hook)
- Power sources: -48 V office battery, -5 V, and $+5$ V
- Process: 70-volt bipolar with thin-film resistances
- Package: 28-pin ceramic leadless chip carrier (LCC)

Ordering Information

Part Number	Battery Feed	Office Battery	Package
μ PC7059K	Constant resistance	-48 volts	28-pin ceramic LCC
μ PC7069K	Constant current		

Application Diagram, μPC7059K



Note: Values are in ohms and microfarads.

63YL-5180B

Application Diagram, μPC7069K

