

14-PIN 5-TAP DIGITAL DELAY LINE

STANDARD AND AUTO INSERTABLE PACKAGE

5TS14DL/5TS14DP SERIES

FEATURES

- Compatible with TTL circuits
- 5 equally-spaced delay taps
- Available in surface mount (5TS14SG)
- Other delays, pin layouts and tolerances available upon request.

ELECTRICAL CHARACTERISTICS

Supply Voltage Vcc:	5.0±0.25Vdc
Rise Time:	4ns
Operating Temperature:	0°C to +70°C
Logic 1 Input Voltage:	2.0V min.
Logic 1 Input Current:	50µA max.
Logic 0 Input Voltage:	0.8V max.
Logic 0 Input Current:	-2.0mA max.
Logic 1 Output Voltage:	2.5V min.
Logic 0 Output Voltage:	0.5V max.

All measurements made at Vcc=5.0V, 25°C

DRIVE CAPABILITIES

Logic 0 Output:	10/tap max.
Logic 1 Output:	20/tap max.

TEST CONDITIONS

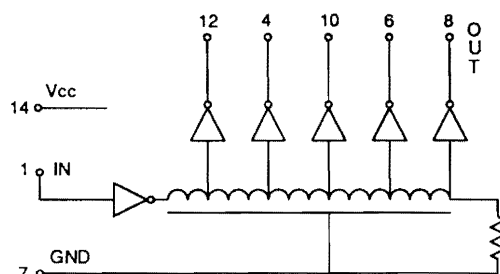
Pulse Voltage:	3.2V
Rise Time:	2.0ns
Supply Voltage:	5.0Vdc
Supply Current:	75mA max.
Pulse Width:	1.5 times of total delay
Duty Cycle:	50% max.

ELECTRICAL SPECIFICATIONS

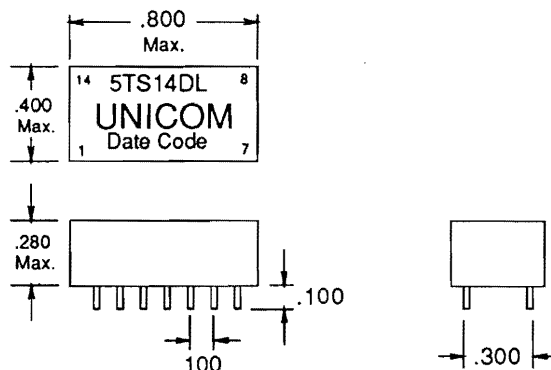
PART NO. (STD)	PART NO. (DIP)	TOTAL DELAY(NS)	TAP DELAY(NS)
5TS14DL-20	5TS14DP-20	20	4
5TS14DL-25	5TS14DP-25	25	5
5TS14DL-30	5TS14DP-30	30	6
5TS14DL-40	5TS14DP-40	40	8
5TS14DL-50	5TS14DP-50	50	10
5TS14DL-75	5TS14DP-75	75	15
5TS14DL-100	5TS14DP-100	100	20
5TS14DL-125	5TS14DP-125	125	25
5TS14DL-150	5TS14DP-150	150	30
5TS14DL-175	5TS14DP-175	175	35
5TS14DL-200	5TS14DP-200	200	40
5TS14DL-250	5TS14DP-250	250	50
5TS14DL-300	5TS14DP-300	300	60
5TS14DL-400	5TS14DP-400	400	80
5TS14DL-500	5TS14DP-500	500	100

(1) Delays measured at 1.5 Volts, positive edge
 (2) Rise time measured from 0.75V to 2.4V
 (3) Delay tolerances at ±5% or ±2ns whichever is greater

SCHEMATIC



STANDARD PACKAGE



DIP PACKAGE

