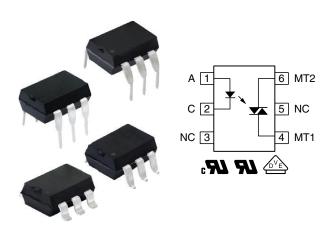


## **Optocoupler, Phototriac Output, Non-Zero Crossing**



### **LINKS TO ADDITIONAL RESOURCES**

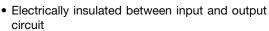


#### **DESCRIPTION**

The BRT12 and BRT13 product family consists of AC optocouplers non-zero voltage detectors consisting of two electrically insulated lateral power ICs which integrate a thyristor system, a photo detector and noise suppression at the output and an IR GaAs diode input.

#### **FEATURES**

- I<sub>TRMS</sub> = 300 mA
- High static dV/dt < 10 000 V/µs





Microcomputer compatible - very low trigger current

RoHS COMPLIANT

- Trigger current
  - (I<sub>FT</sub> < 1.2 mA) BRT12**F**
- (I<sub>FT</sub> < 2 mA) BRT12**H**, BRT13**H**
- (I<sub>FT</sub> < 3 mA) BRT13**M**
- · Non-zero voltage detectors high input sensitivity
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- · Industrial controls
- · Office equipment
- Consumer appliances

### **AGENCY APPROVALS**

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1

ORDERING INFORMATION					
B R T 1 # PART NUMBER		0 # # T  KAGE OPTION TAPE AND REEL	7.62 mm Option 9  Option 7 Option 9  > 0.7 mm		
AGENCY CERTIFIED / PACKAGE	V <sub>DRM</sub> (V)				
AGENCT CENTIFIED / FACRAGE	≤ 600		≤ 800		
UL	I <sub>FT</sub> = 1.2 mA	I <sub>FT</sub> = 2 mA	I <sub>FT</sub> = 2 mA		
DIP-6	BRT12F	BRT12H	BRT13H		
DIP-6, 400 mil, option 6	-	BRT12H-X006	BRT13H-X006		
SMD-6, option 7	BRT12F-X007T	BRT12H-X007T	BRT13H-X007T <sup>(1)</sup>		
SMD-6, option 9	-	BRT12H-X009T	BRT13H-X009T		
UL, VDE	I <sub>FT</sub> = 1.2 mA	I <sub>FT</sub> = 2 mA	I <sub>FT</sub> = 2 mA		
DIP-6	=	BRT12H-X001	-		
DIP-6, 400 mil, option 6	=	BRT12H-X016	-		
SMD-6, option 7	-	-	BRT13H-X017T		

#### Note

(1) Also available in tube, do not put T on the end



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
INPUT						
Reverse voltage			$V_R$	6	V	
Forward current			I <sub>F</sub>	20	mA	
Surge forward current			I <sub>FSM</sub>	1.5	Α	
Power dissipation	t ≤ 10 μs		P <sub>diss</sub>	30	mW	
OUTPUT						
Repetitive peak off-state voltage		BRT12	$V_{DRM}$	600	V	
nepetitive peak oil-state voltage		BRT13	$V_{DRM}$	800	V	
RMS on-state current			I <sub>TRMS</sub>	300	mA	
Single cycle surge current	50 Hz		I <sub>TSM</sub>	3	Α	
Power dissipation			P <sub>diss</sub>	600	mW	
COUPLER						
Maximum power dissipation			P <sub>tot</sub>	630	mW	
Reference voltage in accordance with VDE 0110 b			V <sub>ref</sub>	500	V <sub>RMS</sub>	
Reference voltage in accordance with VDE 0110 b (insulation group C)			V <sub>ref</sub>	600	V <sub>DC</sub>	
Storage temperature range			T <sub>stg</sub>	-40 to +150	°C	
Ambient temperature range			T <sub>amb</sub>	-40 to +100	°C	

#### **Notes**

<sup>(1)</sup> Test AC voltage in accordance with DIN 57883, June 1980

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NPUT							
Forward voltage	I <sub>F</sub> = 10 mA		$V_{F}$	-	1.1	1.35	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	-	-	10	μΑ
Thermal resistance, junction to ambient <sup>(1)</sup>			R <sub>thJA</sub>	-	-	750	°C/W
OUTPUT							
Peak off-state voltage	I <sub>D(RMS)</sub> = 100 μA	BRT12	V	-	600	-	μΑ
Peak on-state voltage		BRT13	$V_{DM}$	-	800	-	μΑ
Off-state current	$T_C = 80  ^{\circ}C,  V_{DRM}$		I <sub>D</sub>	-	0.5	100	μΑ
On-state voltage	I <sub>T</sub> = 300 mA		V <sub>T</sub>	-	-	2.3	V
Pulse current	$t_p \le 5 \ \mu s, \ f = 100 \ Hz, \ dI_{tp}/dt \le 8 \ A/\mu s$		I <sub>tp</sub>	-	-	2	Α
	$V_D = 0.67 V_{DRM}, T_j = 25  ^{\circ}C$		dV/dt <sub>cr</sub>	10	-	-	kV/μs
Critical rate of rise of off-state voltage	V <sub>D</sub> = 0.67 V <sub>DRM</sub> , T <sub>j</sub> = 80 °C		dV/dt <sub>cr</sub>	5	-	-	kV/μs
Critical rate of rise of voltage at current commutation	$V_D = 0.67 V_{DRM}, T_j = 25 ^{\circ}\text{C},$ $dI/dt_{crq} \le 15 \text{A/ms}$		dV/dt <sub>crq</sub>	10	-	-	kV/μs
	$\begin{split} V_D = 0.67 \ V_{DRM}, T_j = 80 \ ^{\circ}C, \\ dI/dt_{crq} \leq 15 \ A/ms \end{split}$		dV/dt <sub>crq</sub>	5	-	-	kV/μs
Critical rate of rise of on-state at current			dl/dt <sub>cr</sub>	8	-	-	A/µs
Holding current	V <sub>D</sub> = 10 V		I <sub>H</sub>	-	80	500	μΑ
Thermal resistance, junction to ambient			R <sub>thJA</sub>	-	_	125	°C/W

<sup>•</sup> Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER							
Trigger current	$V_D = 10 \text{ V}, \text{ F - versions}$		I <sub>FT</sub>	1	-	1.2	mA
	$V_D = 10 \text{ V}, \text{ H} - \text{versions}$		I <sub>FT</sub>	0.4	-	2	mA
	V <sub>D</sub> = 10 V, M - versions		I <sub>FT</sub>	0.8	-	3	mA
Trigger current temperature gradient			$\Delta I_{FT}/\Delta T_{j}$	-	7	14	μΑ/°C
Capacitance (input to output)	$f = 1 \text{ MHz}, V_R = 0 \text{ V}$		C <sub>IO</sub>	1	-	2	pF

#### **Notes**

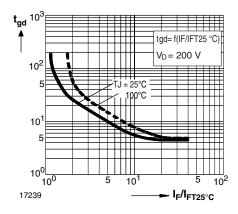
- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
  evaluation. Typical values are for information only and are not part of the testing requirements.
- (1) Static air, SITAC soldered in PCB or base plate

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>			
Maximum transient isolation voltage		V <sub>IOTM</sub>	10 000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V <sub>peak</sub>			
la dation projetance	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Output safety power		P <sub>SO</sub>	400	mW			
Input safety current		I <sub>SI</sub>	275	mA			
Safety temperature		T <sub>S</sub>	175	°C			
Creepage distance	DIP-6; SMD-6, option 7;		≥ 7	mm			
Clearance distance	SMD-6, option 9		≥ 7	mm			
Creepage distance	DID 6 option 6		≥ 8	mm			
Clearance distance	DIP-6, option 6		≥8	mm			
Insulation thickness		DTI	≥ 0.4	mm			

#### Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)





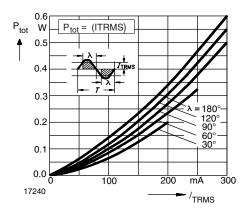
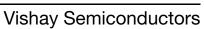


Fig. 2 - Power Dissipation 60 Hz to 60 Hz Line Operation



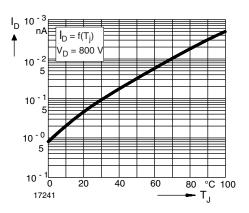


Fig. 3 - Typical Off-State Current

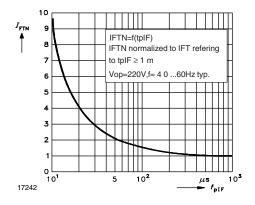


Fig. 4 - Pulse Trigger Current

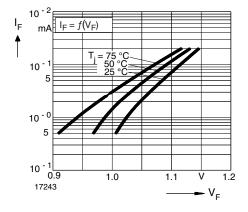


Fig. 5 - Typical Input Characteristics

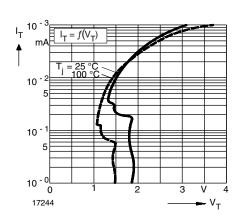


Fig. 6 - Typical Output Characteristics

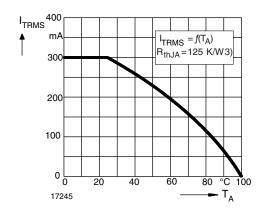


Fig. 7 - Current Reduction

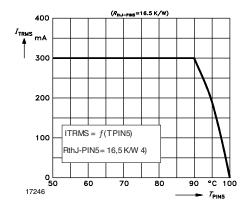
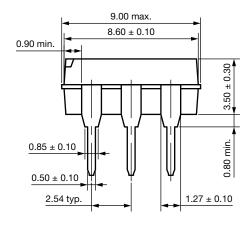


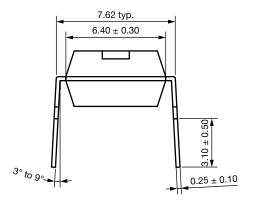
Fig. 8 - Current Reduction

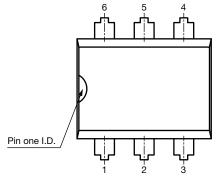


### **PACKAGE DIMENSIONS** in millimeters

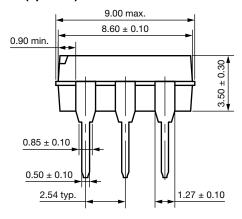
### DIP-6

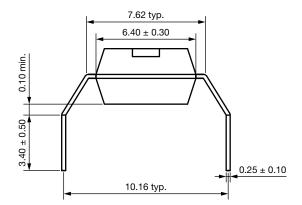


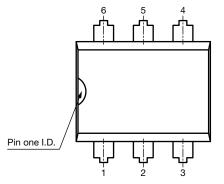




### **DIP-6, 400 mil (option 6)**

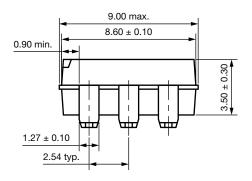


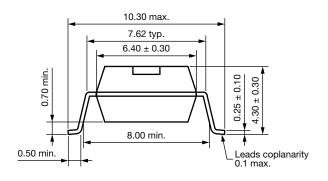


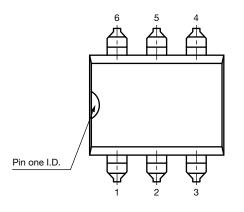


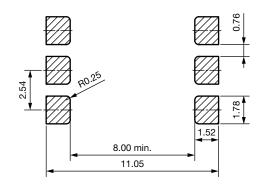


### SMD-6 (option 7)

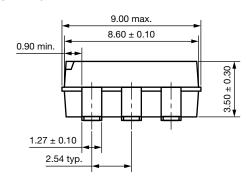


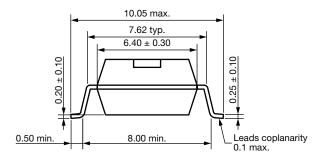


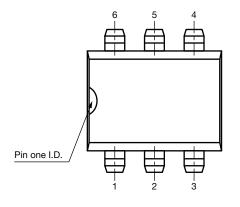


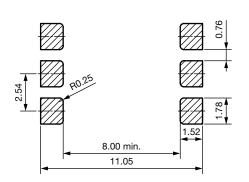


## SMD-6 (option 9)











## **PACKAGE MARKING** (example)



#### **Notes**

- XXXX = LMC (lot marking code)
- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

#### **SOLDER PROFILES**

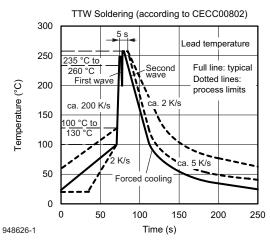


Fig. 9 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

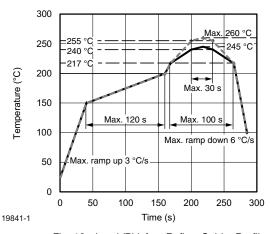


Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb} < 30$  °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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