

Digital Circuits

9LS/54LS/74LS Low Power Schottky (Cont.)

| Type Number | Description | Prop Delay ¹ (ns) or Max. Op. Freq. (MHz) ² | Pwr Diss. (mW) | Available Packages | | | | | |
|-------------------|--|--|----------------------|--------------------|---|--------|---|--------|---|
| | | | | 14 Pin | | 16 Pin | | 24 Pin | |
| | | | | J | W | J | W | J | W |
| 9LS/54LS/74LS190 | BCD decade counter, mode control | 25 MHz | 90 | | | X | X | | |
| 9LS/54LS/74LS191 | 4-bit binary counter, mode control | 25 MHz | 90 | | | X | X | | |
| 9LS/54LS/74LS192 | BCD decade counter, up/down | 30 MHz | 85 | | | X | X | | |
| 9LS/54LS/74LS193 | 4-bit binary counter, up/down | 30 MHz | 85 | | | X | X | | |
| 9LS/54LS/74LS194A | 4-bit bidirectional universal shift register | 30 MHz | 75 | | | X | X | | |
| 9LS/54LS/74LS195A | 4-bit parallel access shift register | 30 MHz | 70 | | | X | X | | |
| 9LS/54LS/74LS196 | 4-bit presettable decade counter | 35 MHz | 60 | X | X | | | | |
| 9LS/54LS/74LS197 | 4-bit presettable binary counter | 35 MHz | 60 | X | X | | | | |
| 9LS/54LS/74LS221 | Dual one-shot | 40 | 95* | | | X | X | | |
| 9LS/54LS/74LS251 | LS151 with tri-state outputs | 25 | 35 | | | X | X | | |
| 9LS/54LS/74LS253 | LS153 with tri-state outputs | 15 | 35 | | | X | X | | |
| 9LS/54LS/74LS255 | LS155 with tri-state outputs | 25 | 35 | | | X | X | | |
| 9LS/54LS/74LS257 | LS157 with tri-state outputs | 18 | 50 | | | X | X | | |
| 9LS/54LS/74LS258 | LS158 with tri-state outputs | 15 | 35 | | | X | X | | |
| 9LS/54LS/74LS261 | 2 x 4 parallel binary multiplexer | 35 | 110 | | | X | X | | |
| 9LS/54LS/74LS266 | Quad 2-input exclusive NOR open collectors | 20 | 40 | X | X | | | | |
| 9LS/54LS/74LS279 | Quad latch | 27 | 12 | | | X | X | | |
| 9LS/54LS/74LS283 | 4-bit full adder, fast carry | 18 | 96 | | | X | X | | |
| 9LS/54LS/74LS295A | LS95B with tri-state outputs | 30 MHz | 70 | X | X | | | | |
| 9LS/54LS/74LS298 | Quad 2 multiplexer with output register | 16 | 65 | | | X | X | | |
| 9LS/54LS/74LS365 | Hex buffer (tri-state, common enable) | 15 | 68 | | | X | X | | |
| 9LS/54LS/74LS366 | Hex inverter (tri-state, common enable) | 15 | 60 | | | X | X | | |
| 9LS/54LS/74LS367 | Hex buffer (tri-state, 4 x 2 bit) | 15 | 68 | | | X | X | | |
| 9LS/54LS/74LS368 | Hex inverter (tri-state, 4 x 2 bit) | 15 | 60 | | | X | X | | |
| 9LS/54LS/74LS375 | Quad latch (rotated LS75) | 12 | 32 | | | X | X | | |
| 9LS/54LS/74LS386 | Quad 2-input exclusive OR gate | 12 | 30 | X | X | | | | |
| 9LS/54LS/74LS395 | 4-bit shift register (tri-state) | 35 MHz | 75 | | | X | X | | |
| 9LS/54LS/74LS670 | 4 x 4 register file (tri-state) | 40 | 150 | | | X | X | | |

1. Maximum at 25°C

2. Guaranteed minimum at 25°C

Beam Lead Low Power Schottky Devices

| Type | Description | Die Size (Mils) | Layout | | Mech. Outline Dwg. |
|-----------|---|--------------------|-----------------|-------------|--------------------------|
| | | | No. of Beams | EIA Std. | |
| 54LS00BL | Quad 2-input NAND gate | 45 x 45 | 16 | X | 9 |
| 54LS03BL | Quad 2-input NAND gate, open collector outputs | 45 x 45 | 16 | X | 9 |
| 54LS04BL | Hex inverter | 45 x 45 | 16 | X | 9 |
| 54LS05BL | Hex inverter, open collector outputs | 45 x 45 | 16 | X | 9 |
| 54LS10BL | Triple 3-input NAND gate | 45 x 45 | 16 | X | 9 |
| 54LS11BL | Triple 3-input AND gate | 45 x 45 | 16 | X | 9 |
| 54LS15BL | Triple 3-input AND gate, open collector outputs | 45 x 45 | 16 | X | 9 |
| 54LS153BL | Dual 4-to-1 line multiplexer | 55 x 55 | 20 | X | 12 |
| 54LS253BL | Dual 4-to-1 line multiplexer, tri-state output | 55 x 55 | 20 | X | 12 |