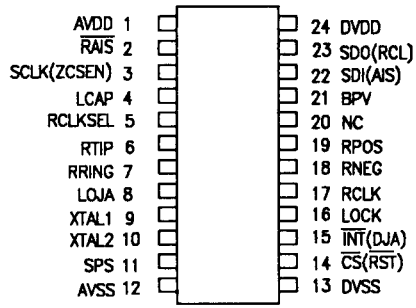
Dallas Semiconductor
Receive Line Interface with Jitter Attenuation

PRODUCT PREVIEW
DS2189
Available Spring 1989

FEATURES
- Line interface for T1 (1.544 MHz) and CEPT (2.048 MHz)
- Extracts clock and data from twisted pair of coax
- Meets requirements of PUB 62411, TR170, and CCITT recommendations
- Greater than 300UI of jitter tolerance at 10 Hz
- Jitter attenuation starting at 6 Hz
- Unique instantaneous jitter reporting through the serial port
- Decodes AMI, B8ZS, and HDB3 signals
- Designed for short loop applications such as terminal equipment to DSX-1
- Receiver sensitivity of 100 mv peak
- Simple serial interface port used for configuration and to report alarms and error events
- "Hardware" mode requires no host processor but still allows access to most features
- Compatible with the DS2180A T1 and DS2181 CEPT Transceivers
- Companion to the DS2186 Transmit Line Interface
- Single 5V supply, low power CMOS technology
- Available in 24-pin "skinny" DIP and 24-pin SOIC

PIN CONNECTIONS (tentative)



( ) = Hardware mode