

74ABT16260

12-Bit to 24-Bit Multiplexed D-Type Latches with TRI-STATE® OUTPUTS

General Description

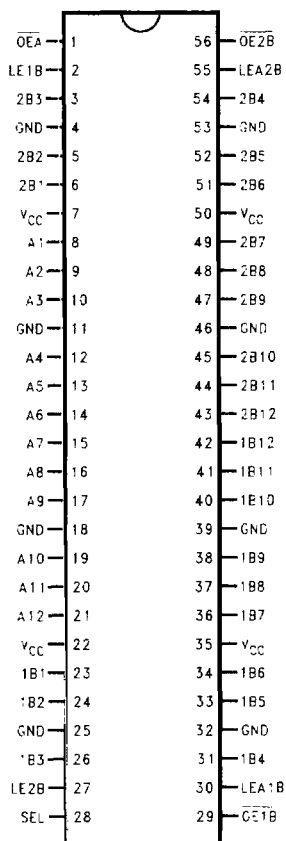
The 74ABT16260 is a 12-bit to 24-bit multiplexed D-type latch with TRI-STATE outputs. Its intended use is to multiplex two separate data paths onto one, or demultiplex from a single data path. Applications include multiplexing and or demultiplexing of address and data info in microprocessors, memory interleaving and bus interface.

Features

- Guaranteed latch-up protection
- Non-destructive hot insertion capability
- High impedance glitch free bus loading during entire power up and power down cycle
- Flow through architecture optimizes PCB layout

Connection Diagram

Pin Assignment for SSOP



Function Tables

B to A ($\overline{OE}B = H$)

Inputs						Outputs
1B	2B	SEL	LE1B	LE2B	$\overline{OE}A$	A_n
H	X	H	H	X	L	H
L	X	H	H	X	L	L
X	X	H	L	X	L	A_n
X	H	L	X	H	L	H
X	L	L	X	H	L	L
X	X	L	X	L	L	A_n
X	X	X	X	X	H	Z

A to B ($\overline{OE}A = H$)

Inputs					Outputs	
A	LEA1B	LEA2B	$\overline{OE}1B$	$\overline{OE}2B$	1Bn	2Bn
H	H	H	L	L	H	H
L	H	H	L	L	L	L
H	H	L	L	L	H	2Bn
L	H	L	L	L	L	2Bn
H	L	H	L	L	1Bn	H
L	L	H	L	L	1Bn	L
X	L	L	L	L	1Bn	2Bn
X	X	X	H	H	Z	Z
X	X	X	L	H	Active	Z
X	X	X	H	L	Active	Active
X	X	X	L	L	Active	Active

TL/F/12101-1

