



High Speed Quad PNP Array

T-43-25

Preliminary Product Announcement

CA2015 / CA2015A

FEATURES

- Quad High Speed PNP's
- High Frequency $f_t > 350\text{MHz}$
- $B_V > 40V$
- Monolith Structure with Dielectric Isolation
- Excellent Thermal Characteristics
- High H_{fe} - 150 minimum
- Tight V_{be} matching - 1mV
- Tight H_{fe} Matching - 5%
- Each transistor similar to 2N3906
- Pin Compatible with TPQ3906 and MPQ3906

APPLICATIONS

- Current Sources
- Current Mirrors
- Log Amplifiers
- Multipliers

DESCRIPTION

The CA2015 series are quad monolithic vertical PNP transistor arrays offering excellent matching characteristics and high speed performance. This product group is built on Calogic's Dielectrically Isolated process allowing total transistor isolation from one transistor to the next. The design and construction results in low collector to substrate capacitance achieving high speed performance and very low crosswalk while maintaining complete D.C. isolation. Substrate biasing is not required for normal operation, however for optimum high speed performance the substrate should be grounded. Excellent parameter matching and tracking over temperature is the result of a monolithic structure.

ORDERING INFORMATION

Pin	Package	Temperature Range
CA2015CP	14 Pin Plastic Dip	25 to 85°C
XCA2015	Sorted Chips in Carriers	25 to 85°C
CA2015ACP	14 Pin Plastic Dip	25 to 85°C
XCA2015A	Sorted Chips in Carriers	25 to 85°C

CONNECTION DIAGRAM

