

FAST Products

FEATURES

- DRAM signal timing generator
- Automatic refresh circuitry
- Dual Ported arbitration
- Selectable row address hold and RAS precharge times
- Supports Page and Nibble Mode accesses
- On-chip column address counter
- Multiple CAS outputs with CAS enables for byte addressing
- Controls 4-MBit DRAMs
- Intelligent burst-mode refresh after page mode access cycles

PRODUCT DESCRIPTION

The Signetics 4-MBit Intelligent DRAM Controller is a 4-MBit dual-ported version of the 'F1763 Intelligent DRAM Controller. It contains automatic signal timing, address multiplexing, refresh control, and access and refresh arbitration. Additional features include multiple CAS outputs, selectable row address hold and RAS precharge times, page mode support and on-chip column address counter for all major modes of burst access to the DRAMs.

Preliminary Specification

TYPE	TYPICAL f_{MAX}	TYPICAL SUPPLY CURRENT (TOTAL)
74F4763	150MHz	175mA

ORDERING INFORMATION

PACKAGES	COMMERCIAL RANGE $V_{CC} = 5V \pm 10\%$; $T_A = 0^\circ C$ to $+70^\circ C$
64-Pin Plastic DIP	N74F4763N
68-Pin Plastic PLCC	N74F4763A

BLOCK DIAGRAM

