

FAST 74F808, 74F1808 AND DRIVERS

FAST Products

FEATURES

- High capacitive drive capability
- Choice of configuration
Corner V_{CC} and GND-- 'F808
Center V_{CC} and GND-- 'F1808
- Typical propagation delay of 2.6ns

74F808-Hex 2-Input AND Driver 74F1808-Hex 2-Input AND Driver *Preliminary Specification*

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F808	2.6ns	11mA
74F1808	2.6ns	11mA

ORDERING INFORMATION

PACKAGES	COMMERCIAL RANGE $V_{CC} = 5V \pm 10\%$; $T_A = 0^\circ C$ to $+70^\circ C$
20-Pin Plastic DIP	N74F808N, N74F1808N
20-Pin Plastic SOL	N74F808D, N74F1808D

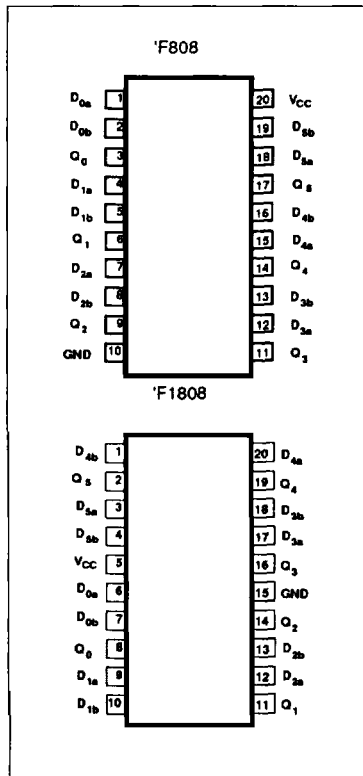
INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

PINS	DESCRIPTION	74F(U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
$D_{na} - D_{nb}$	Data inputs	1.0/0.033	20 μ A/20 μ A
$Q_0 - Q_5$	Data outputs	2400/80	48mA/48mA

NOTE:

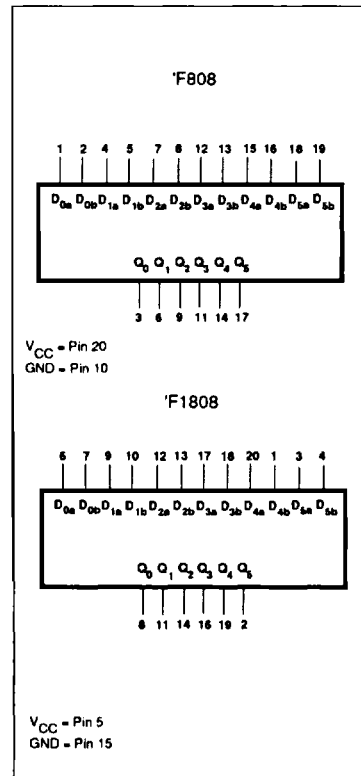
One (1.0) FAST Unit Load is defined as: 20 μ A in the High state and 0.6mA in the Low state.

PIN CONFIGURATION



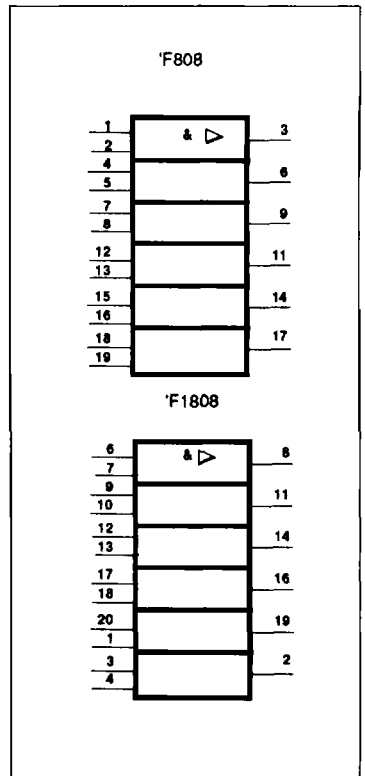
March 7, 1989

LOGIC SYMBOL



6-774

LOGIC SYMBOL (IEEE/IEC)



853-

AND Drivers

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FUNCTION TABLE

INPUTS		OUTPUT
D _{na}	D _{nb}	Q _n
L	X	L
X	L	L
H	H	H

H = High voltage level
 L = Low voltage level
 X = Don't care

ABSOLUTE MAXIMUM RATINGS (Operation beyond the limits set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	-0.5 to +7.0	V
V _{IN}	Input voltage	-0.5 to +7.0	V
I _{IN}	Input current	-30 to +5	mA
V _{OUT}	Voltage applied to output in High output state	-0.5 to +V _{CC}	V
I _{OUT}	Current applied to output in Low output state	96	mA
T _A	Operating free-air temperature range	0 to +70	°C
T _{STG}	Storage temperature	-65 to +150	°C

RECOMMENDED OPERATION CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		Min	Nom	Max	
V _{CC}	Supply voltage	4.5	5.0	5.5	V
V _{IH}	High-level input voltage	2.0			V
V _{IL}	Low-level input voltage			0.8	V
I _{IK}	Input clamp current			-18	mA
I _{OH}	High-level output current			-48	mA
I _{OL}	Low-level output current			48	mA
T _A	Operating free-air temperature range	0		70	°C

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DC ELECTRICAL CHARACTERISTICS (Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER	TEST CONDITIONS ¹	LIMITS			UNIT	
			Min	Typ ²	Max		
V _{OH}	High-level output voltage	V _{CC} = MIN, I _L = MAX	±10%V _{CC}	2.0		V	
		V _{IH} = MIN, I _{OH} = MAX	±5%V _{CC}	2.0		V	
V _{OL}	Low-level output voltage	V _{CC} = MIN, I _L = MAX	±10%V _{CC}	0.38	0.55	V	
		V _{IH} = MIN, I _{OL} = MAX	±5%V _{CC}	0.38	0.55	V	
V _{IK}	Input clamp voltage	V _{CC} = MIN, I _I = I _{IK}		-0.73	-1.2	V	
I _I	Input current at maximum input voltage	V _{CC} = MAX, V _I = 7.0V			100	μA	
I _{IH}	High-level input current	V _{CC} = MAX, V _I = 2.7V			20	μA	
I _{IL}	Low-level input current	V _{CC} = MAX, V _I = 0.5V			-20	μA	
I _{OS}	Short circuit output current ³	V _{CC} = MAX, V _O = 2.25V		-60	-160	mA	
I _{CC}	Supply current (total)	I _{CCH} I _{CCL}	V _{CC} = MAX	V _{IN} = GND	6.5	11	mA
					V _{IN} = 4.5V	19	32

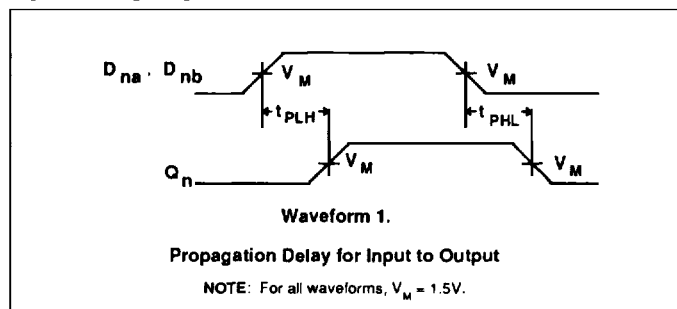
NOTES:

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at V_{CC} = 5V, T_A = 25°C.
- The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITION	LIMITS						UNIT
			T _A = +25°C V _{CC} = 5V C _L = 50pF R _L = 500Ω			T _A = 0°C to +70°C V _{CC} = 5V ±10% C _L = 50pF R _L = 500Ω			
			Min	Typ	Max	Min	Max		
t _{PLH} t _{PHL}	Propagation delay D _{na} , D _{nb} to Q _n	Waveform 1	1.0	2.5	4.5	1.0	5.0	5.0	ns

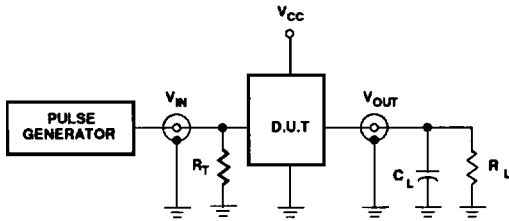
AC WAVEFORMS



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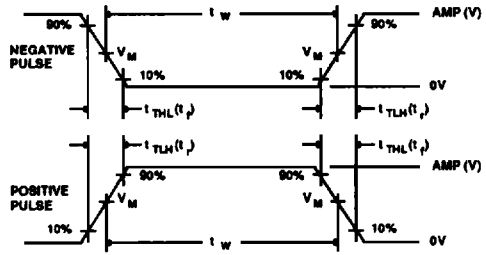
TEST CIRCUIT AND WAVEFORMS



Test Circuit For Totem-Pole Outputs

DEFINITIONS

- R_L = Load resistor; see AC CHARACTERISTICS for value.
- C_L = Load capacitance includes jig and probe capacitance; see AC CHARACTERISTICS for value.
- R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.



$V_M = 1.5V$

Input Pulse Definition

FAMILY	INPUT PULSE REQUIREMENTS				
	Amplitude	Rep. Rate	t_W	t_{TLH}	t_{THL}
74F	3.0V	1MHz	500ns	2.5ns	2.5ns