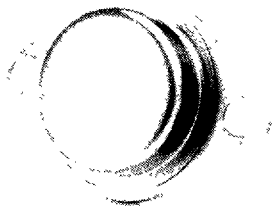


# 160 WATT (20 AMP CONTINUOUS, 40 AMP PEAK)

2N6283, 2N6284,  
2N6286, 2N6287



## FEATURES

- Electrical specifications guaranteed at 25°C
- Guaranteed minimum DC current gain at full rated current
- Hermetically sealed

## DESCRIPTION

The 2N6283, 2N6284 devices are three-terminal NPN Darlington Power Transistors. The 2N6286, 2N6287 devices are PNP Darlington Power Transistors. These devices are monolithic epitaxial base structures with built-in base to emitter shunt resistors. The devices are CVD glass passivated to increase reliability and provide reduced high-temperature reverse leakage current. Internal diode protection (D1) of the Darlington configuration is built into the structure to limit the device power dissipation during negative overshoot.

## \*MAXIMUM RATINGS

PARAMETER	SYMBOL	MAXIMUM	UNITS
Collector Emitter Voltage 2N6283, 2N6286 2N6284, 2N6287	$V_{CE0}$	80 100	Vdc
Collector Base Voltage 2N6283, 2N6286 2N6284, 2N6287	$V_{CBO}$	80 100	Vdc
Emitter Base Voltage	$V_{EBO}$	5	Vdc
Collector Current Continuous Peak	$I_C$	20 40	Adc
Base Current	$I_B$	0.5	Adc
Thermal Resistance	$\theta_{JC}$	1.09	°C/Watt
Total Internal Power Dissipation @ $T_C = 25^\circ\text{C}$ <sup>1)</sup>	$P_D$	160	Watts
Operating Junction and Storage Temperature	$T_J$ $T_{STG}$	-65 to +200	°C

<sup>1)</sup> Indicates JEDEC Registered Data

<sup>1)</sup> For operation above  $T_C = 25^\circ\text{C}$ , derate @ 0.915 W/°C

## DEVICE SELECTION GUIDE

DEVICE	VOLTAGE RATING	POLARITY
2N6283	80V	NPN
2N6284	100V	NPN
2N6286	80V	PNP
2N6287	100V	PNP

These Darlington devices are hermetically sealed copper/steel TO-3 packages providing high reliability and low thermal resistance.

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## \*ELECTRICAL CHARACTERISTICS

All parameters are guaranteed at  $T_C = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Minimum	Maximum	Units
<b>ON CHARACTERISTICS</b>					
Collector Emitter Saturation Voltage <sup>1</sup>	$V_{CE(sat)}$	$I_C = 10 \text{ Adc}, I_B = 40 \text{ mAdc}$ $I_C = 20 \text{ Adc}; I_B = 100 \text{ mAdc}$		2.0 3.0	Vdc
Base Emitter Turn-on Voltage <sup>1</sup>	$V_{BE(on)}$	$I_C = 10 \text{ Adc}, V_{CE} = 3 \text{ Vdc}$		2.8	Vdc
Base Emitter Saturation Voltage <sup>1</sup>	$V_{BE(sat)}$	$I_C = 20 \text{ Adc}, I_B = 200 \text{ mAdc}$		4.0	Vdc
DC Current Gain <sup>1</sup>	$h_{FE}$	$I_C = 10 \text{ Adc}; V_{CE} = 3 \text{ Vdc}$ $I_C = 20 \text{ Adc}, V_{CE} = 3 \text{ Vdc}$	750 100	18,000 <sup>2</sup> 18,000 <sup>2</sup>	
<b>OFF CHARACTERISTICS</b>					
Collector Emitter Sustaining Voltage <sup>1</sup> 2N6283, 2N6286 2N6284, 2N6287	$V_{CEO(sus)}$	$I_{CE} = 100 \text{ mAdc}, I_B = 0\text{A}$	80 100		Vdc
Emitter Cutoff Current	$I_{EBO}$	$V_{EB} = 5 \text{ Vdc}, I_C = 0\text{A}$		2.0	mAdc
Collector Cutoff Current 2N6283, 2N6286 2N6284, 2N6287	$I_{CEO}$	$V_{CE} = 40 \text{ Vdc}; I_B = 0\text{A}$ $V_{CE} = 50 \text{ Vdc}; I_B = 0\text{A}$		1.0 1.0	mAdc
Collector Cutoff Current	$I_{CEX}$	$V_{CE} = \text{Rated}; V_{BE(off)} = 1.5 \text{ Vdc}$ $V_{CE} = \text{Rated}; V_{BE(off)} = 1.5 \text{ Vdc},$ $T_C = 150^\circ\text{C}$		0.5 5.0	mAdc
<b>DYNAMIC CHARACTERISTICS</b>					
Output Capacitance	$C_{ob}$	$V_{CB} = 10 \text{ Vdc}; I_E = 0 \text{ Adc}$ $f = 0.1 \text{ MHz}$		400	pF
Small Signal Current Gain	$h_{fe}$	$I_C = 10 \text{ Adc}; V_{CE} = 3 \text{ Vdc}$ $f = 1 \text{ kHz}$	300		
Common Emitter Short Circuit Forward Transfer Ratio	$h_{fe}$	$I_C = 10 \text{ Adc}, V_{CE} = 3 \text{ Vdc}$ $f = 1 \text{ MHz}$	4		

<sup>1</sup>Indicates JEDEC Registered Data

(1) Pulse tested with pulse width  $\sim 300 \mu\text{s}$  and duty cycle  $\sim 2.0\%$

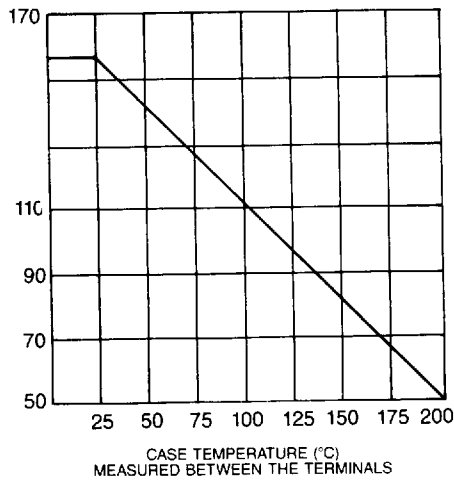
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# 160 WATT (20 AMP CONTINUOUS, 40 AMP PEAK)

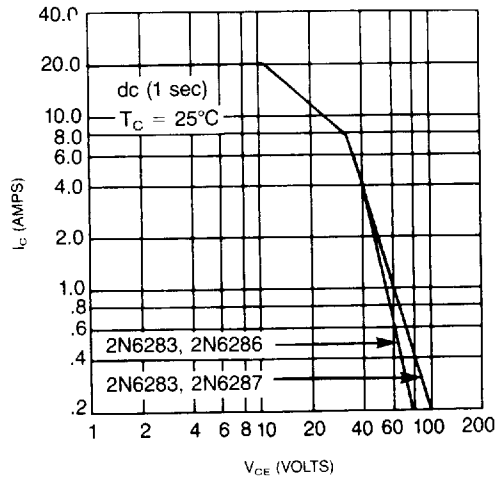
2N6283, 2N6284,  
2N6286, 2N6287

## OPERATIONAL DATA

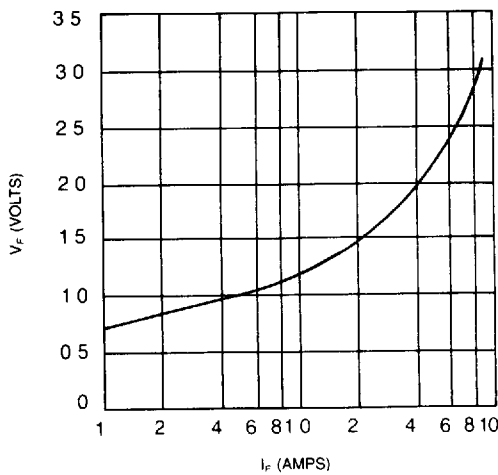
**POWER DERATING**



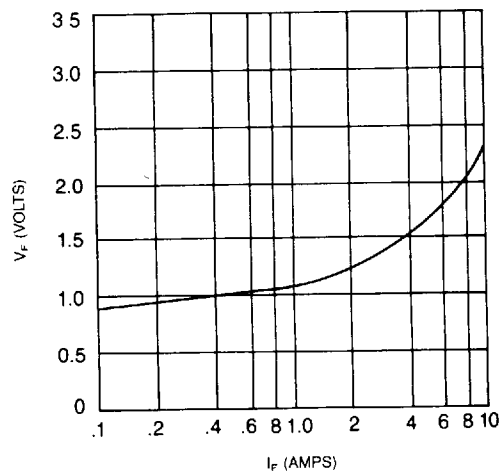
**SAFE OPERATING AREA**



**FORWARD VOLTAGE OF D1 (2N6283, 2N6284)**



**FORWARD VOLTAGE OF D1 (2N6286, 2N6287)**



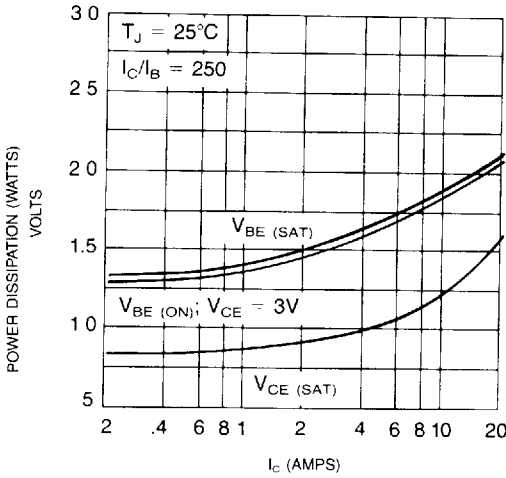
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2N6283, 2N6284,  
2N6286, 2N6287

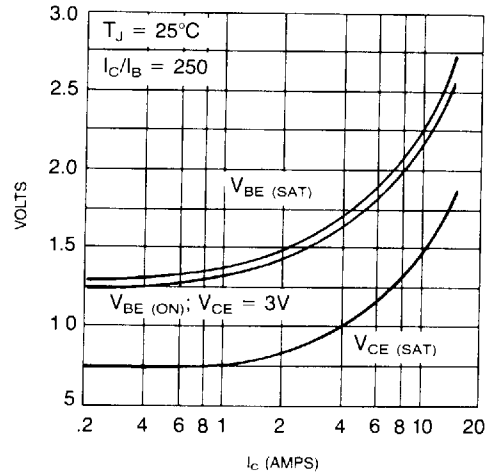
# 160 WATT (20 AMP CONTINUOUS, 40 AMP PEAK)

## OPERATIONAL DATA

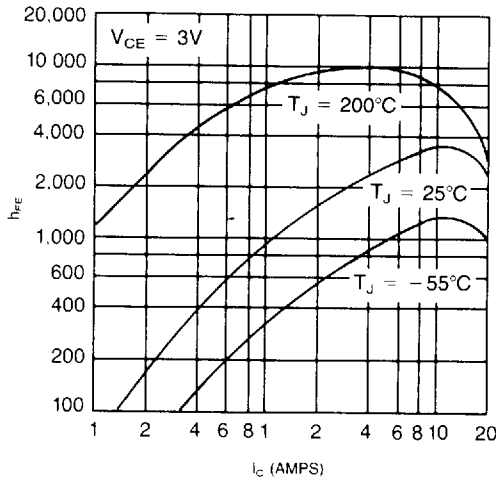
**ON VOLTAGE VS  
COLLECTOR CURRENT  
(2N6283, 2N6284)**



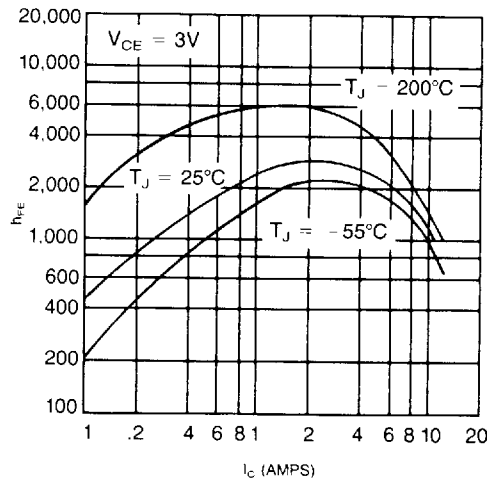
**ON VOLTAGE VS  
COLLECTOR CURRENT  
(2N6286, 2N6287)**



**DC COLLECTOR CURRENT GAIN  
VS COLLECTOR CURRENT  
(2N6283, 2N6284)**



**DC COLLECTOR CURRENT GAIN  
VS COLLECTOR CURRENT  
(2N6286, 2N6287)**

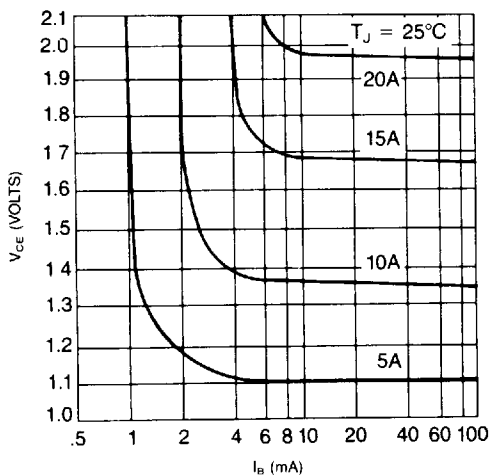


# 160 WATT (20 AMP CONTINUOUS, 40 AMP PEAK)

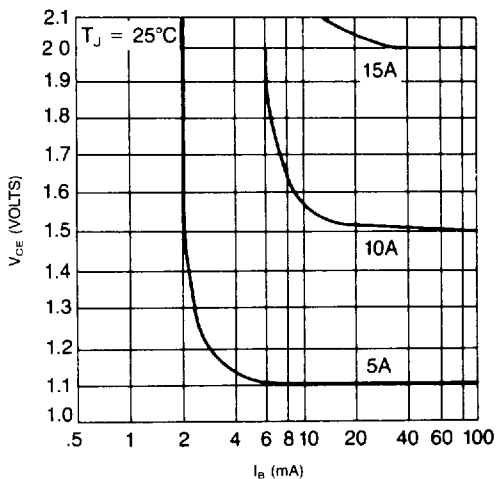
2N6283, 2N6284,  
2N6286, 2N6287

## OPERATIONAL DATA

**COLLECTOR SATURATION REGION (2N6283, 2N6284)**



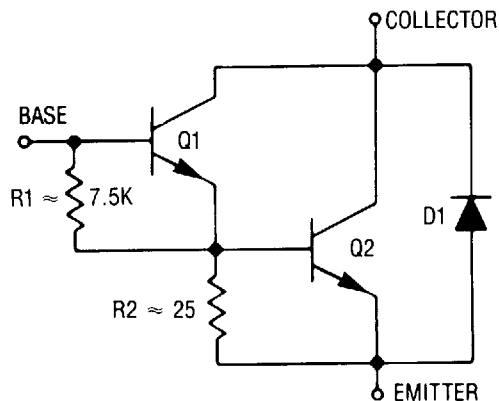
**COLLECTOR SATURATION REGION (2N6286, 2N6287)**



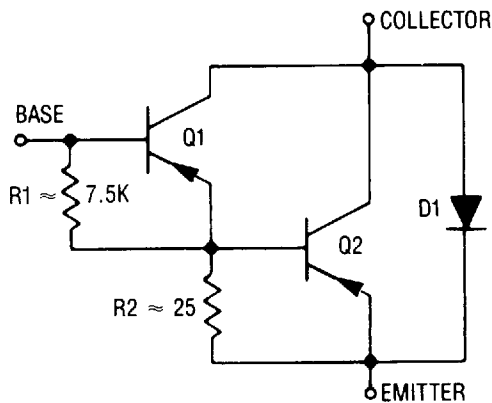
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## BLOCK DIAGRAMS

**NPN**



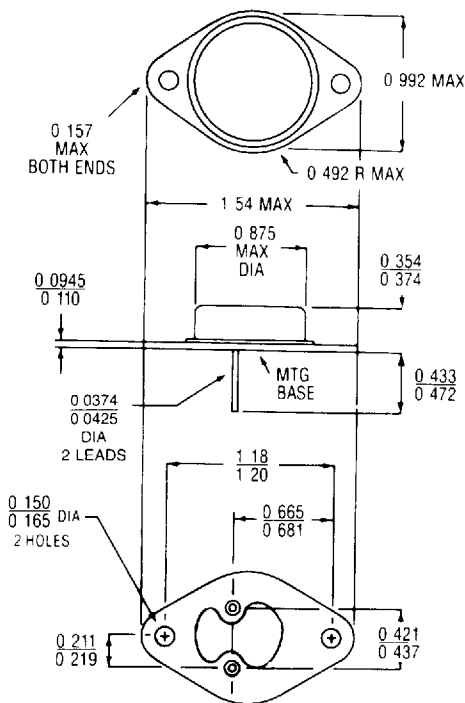
**PNP**



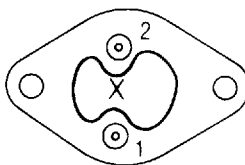
2N6283, 2N6284,  
2N6286, 2N6287

# 160 WATT (20 AMP CONTINUOUS, 40 AMP PEAK)

## DEVICE OUTLINE



Bottom View



- 1 — Base
- 2 — Emitter
- Case Is Collector

NOTE. Case temperature measured at point X  
All dimensions are in inches.