OKI electronic components

KGL6030

Ultra high-speed logic IC Exclusive EXOR/EXNOR gate

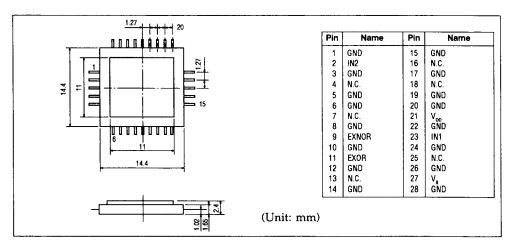
GENERAL DESCRIPTION

The KGL6000 family are the ultra high-speed GaAs devices. With our unique MCFF (memory cell type flip-flop) technology, ultra high-speed operation of 5 GHz or more has been realized. Built-in DCFL interface buffer circuit allows for easier connection between family ICs. Ideal for optical communication systems and high-speed measuring instruments.

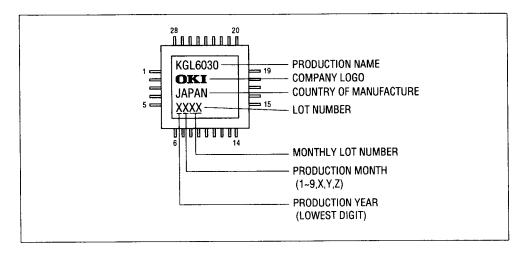
FEATURES

- Ultra high-speed logic IC family
 This logic IC family operates at 5 GHz or more by using a unique memory cell type flip-flop (MCFF) technology. Ideal for optical communication systems and high-performance measuring instruments.
- Ultra high-speed operation: 5 Gbps (min. NRZ)
- Single power supply voltage: 2 V (V_{DD}, V_B)
- Output level is variable by V_B
- High-speed rise and fall time: 60 ps (typical)
- Low-current consumption: 100 mA (typical, V_B)
- I/O terminals for 50 Ω connection
- · Complementary output
- 28-pin flat package

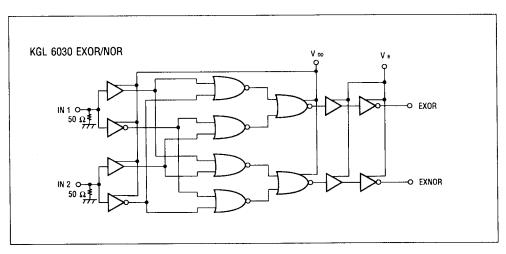
OUTLINE DIMENSIONS



MARKING



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Unit	Min.	Max. 3	
Power supply voltage	V _{DD} , V _B	٧	0		
Input terminal applied voltage	_	٧	-1.0	1.5	
Output terminal applied voltage	-	V	-1.0	3.0	
Storage temperature	Tstg	°C	-55	125	
Case temperature under bias	_	°C	-45	125	

ELECTRICAL CHARACTERISTICS

item	Symbol	Condition	Unit	Min.	Тур.
Max. data rate	f max	_	Gbps	5	_
Input voltage High	V _{iH}	_	V		0.8
Input voltage Low	V,L	_	V	-	0.1
Output voltage High	V _{oH}	Level is variable by V _B	V	_	0.9
Output voltage Low	V _{oL}	Level is variable by V _B	V	_	0.1
Power source current at V _{DD}	I _{DD}	Power supply for logic circuit	mA	_	90
Power source current at V _B	I _B	Power supply for output buffer	mA		100