

# 25 MHz Low Cost Fiber Optic Receiver

## Technical Data

### Description

The HFBR-24X4 fiber optic receiver is designed to operate with the Hewlett-Packard HFBR-14XX fiber optic transmitters and 50/125  $\mu\text{m}$ , 62.5/125  $\mu\text{m}$ , and 100/140  $\mu\text{m}$  fiber optic cable. Consistent coupling into the receiver is assured by the lensed optical system (Figure 1). Response does not vary with fiber size.

The receiver output is an analog signal that can be optimized for a variety of distance/data rate requirements. Low-cost external components can be used to convert the analog output to logic compatible signal levels for various data formats and data rates up to 35 MBaud. This distance/data rate tradeoff results in increased optical power budget at lower data rates which can be used for additional distance or splices.

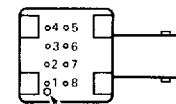
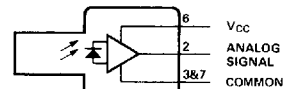
The HFBR-24X4 receiver contains a PIN photodiode and

low noise transimpedance pre-amplifier integrated circuit with an inverting output (see note 3). The HFBR-24X4 receives an optical signal and converts it to an analog voltage. The output is a buffered emitter-follower. Because the signal amplitude from the HFBR-24X4 receiver is much larger than from a simple PIN photodiode, it is less susceptible to EMI, especially at high signal rates. A receiver dynamic range of 15 dB over temperature is achievable (assuming  $10^{-9}$  BER). For very noisy environments, the conductive port option is recommended.

The frequency response is typically dc to 25 MHz. Although the HFBR-24X4 is an analog receiver, it is easily made compatible with digital systems. Please refer to Application Bulletin 73 for simple and inexpensive circuits that operate up to 35 MBd.

### HFBR-24X4 Series

#### Housed Product



BOTTOM VIEW — PIN 1 INDICATOR

PIN	FUNCTION
1†	N C
2	SIGNAL
3*	COMMON
4†	N C
5†	N C
6	V <sub>cc</sub> (5 V)
7*	COMMON
8†	N C

\*PINS 3 AND 7 ARE ELECTRICALLY CONNECTED TO HEADER

†PINS 1, 4, 5, AND 8 ARE ELECTRICALLY CONNECTED

#### Unhoused Product

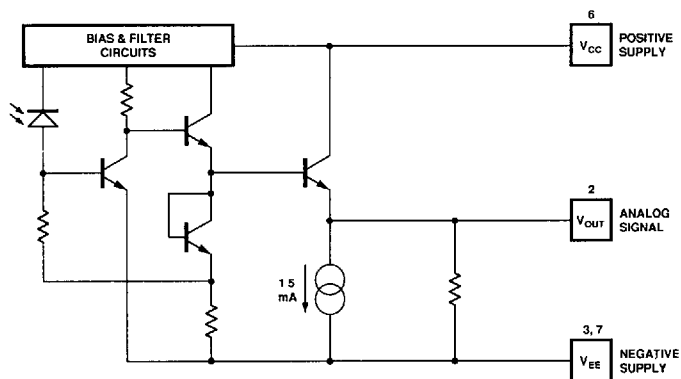


BOTTOM VIEW

PIN	FUNCTION
1	SIGNAL
2	COMMON
3	V <sub>cc</sub> (5 V)
4	COMMON

**CAUTION:** The small junction sizes inherent to the design of this component increases the component's susceptibility to damage from electrostatic discharge (ESD). It is advised that normal static precautions be taken in handling and assembly of this component to prevent damage and/or degradation which may be induced by ESD.

### Simplified Schematic Diagram



### Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	$T_S$	-55	+85	$^{\circ}\text{C}$	
Operating Temperature	$T_A$	-40	+85	$^{\circ}\text{C}$	
Lead Soldering Cycle	Temp.		+260	$^{\circ}\text{C}$	Note 1
	Time		10	sec	
Signal Pin Voltage	$V_{\text{SIGNAL}}$	-0.5	1	V	
Supply Voltage	$V_{CC}$	-0.5	7.0	V	

**Electrical/Optical Characteristics**  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ;  $4.75\text{ V} \leq V_{\text{CC}} \leq 5.25\text{ V}$ ;  $R_{\text{LOAD}} = 511\ \Omega$ ;  
Fiber sizes with core diameter  $\leq 100\ \mu\text{m}$ , and N.A.  $\leq 0.35$  unless otherwise specified.

Parameter	Symbol	Min.	Typ. <sup>(6)</sup>	Max.	Units	Conditions	Reference
Responsivity	$R_p$	5.1	7	10.9	mV/ $\mu\text{W}$	$T_A = 25^{\circ}\text{C}$ @ 820 nm	Figure 14
		4.6		12.3	mV/ $\mu\text{W}$		
RMS Output Noise Voltage	$V_{\text{NO}}$		0.30	0.36	mV	$T_A = 25^{\circ}\text{C}$ , $P_R = 0\ \mu\text{W}$	Figure 15
				0.43	mV		
Equivalent Optical Noise Input Power (RMS)	$P_N$		-43.7	-40.3	dBm		
			0.042	0.094	$\mu\text{W}$		
Peak Input Power	$P_R$			-12.6	dBm	$T_A = 25^{\circ}\text{C}$	Note 2
				55	$\mu\text{W}$		
				-14	dBm		
				40	$\mu\text{W}$		
Output Impedance	$Z_o$		20		$\Omega$	Test Frequency = 20 MHz	
DC Output Voltage	$V_{\text{odc}}$		0.7		V	$P_R = 0\ \mu\text{W}$	Note 3
Power Supply Current	$I_{\text{CC}}$		3.4	6.0	mA	$R_{\text{LOAD}} = \infty$	
Equivalent N.A.	NA		0.35				
Equivalent Diameter	$D_R$		250		$\mu\text{m}$		Note 4

**Dynamic Characteristics** -40°C to +85°C; 4.75 V ≤ V<sub>CC</sub> ≤ 5.25 V; R<sub>LOAD</sub> = 511 Ω, C<sub>LOAD</sub> = 13 pF unless otherwise specified.

Parameter	Symbol	Min.	Typ. <sup>[6]</sup>	Max.	Units	Conditions	Reference
Rise/Fall Time, 10% to 90%	t <sub>r</sub> , t <sub>f</sub>		14	19.5	ns	T <sub>A</sub> = 25°C P <sub>R</sub> = 10 μW Peak	Note 6
				26	ns		
Pulse Width Distortion	t <sub>phl</sub> - t <sub>ph</sub>			2	ns	P <sub>R</sub> = 40 μW Peak	
Overshoot			10		%	T <sub>A</sub> = 25°C	Note 7
Bandwidth (Electrical)	BW <sub>e</sub>		25		MHz	-3 dB Electrical	
Power Supply Rejection Ratio (Referred to Output)	PSRR		50		dB	at 1 MHz	Figure 16 Note 8
Bandwidth - Rise Time Product			0.35		Hz · s		

**Notes:**

- 2.0 mm from where leads enter case.
- If P<sub>R</sub> > 40 μW, then pulse width distortion may increase. At P<sub>in</sub> = 80 μW and T<sub>A</sub> = 85°C, some units have exhibited as much as 100 ns pulse width distortion.
- V<sub>OUT</sub> = V<sub>ODC</sub> - (R<sub>p</sub> × P<sub>R</sub>).
- D<sub>e</sub> is the effective diameter of the detector image on the plane of the fiber face. The numerical value is the product of the actual detector diameter and the lens magnification.
- Typical specifications are for operation at T<sub>A</sub> = 25°C and V<sub>CC</sub> = 5.0 V.
- Input optical signal is assumed to have 10% - 90% rise and fall times of less than 6 ns.
- Percent overshoot is defined as:  $\left( \frac{V_{PK} - V_{100\%}}{V_{100\%}} \right) \times 100\%$ .
- Output referred P.S.R.R. is defined as  $20 \log \left( \frac{V_{POWER SUPPLY RIPPLE}}{V_{OUT RIPPLE}} \right)$ .

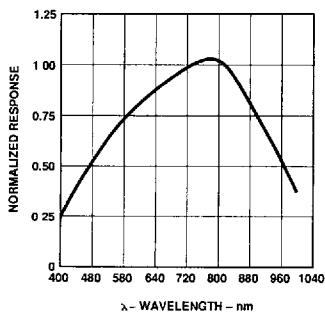


Figure 14. Receiver Spectral Response Normalized to 820 nm.

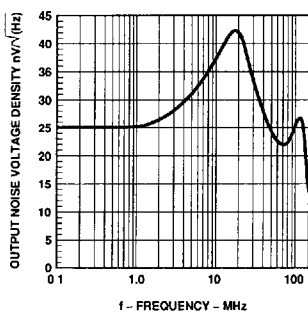


Figure 15. Receiver Noise Spectral Density.

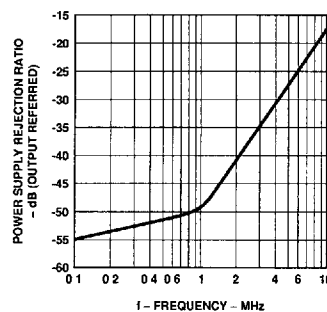
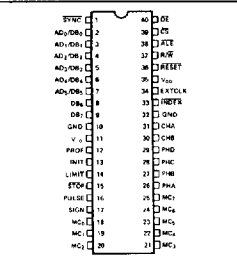
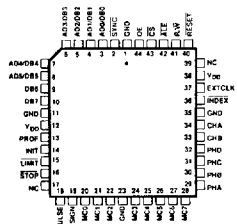
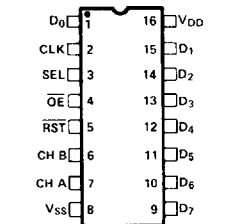
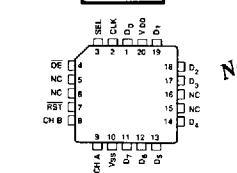
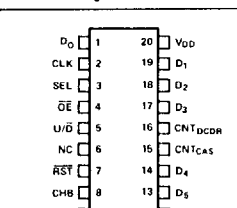
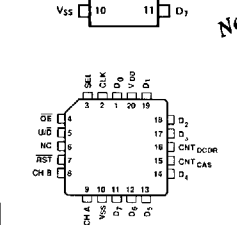





Figure 16. Receiver Power Supply Rejection vs. Frequency.

Motion Control ICS - HCTL-XXXX Series

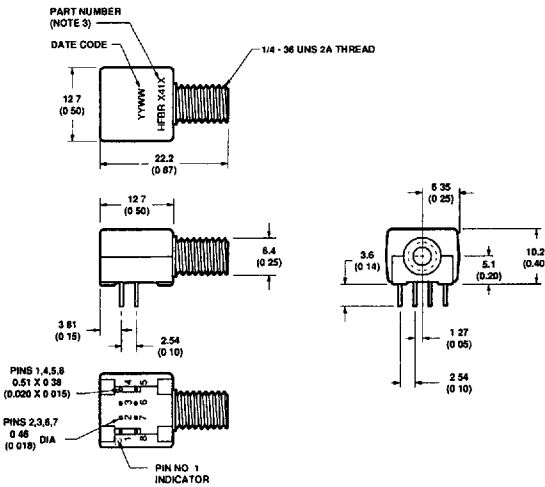
Package Outline Drawing	Part No.	Package	Description	Page No.
	HCTL-1100	PDIP	CMOS General Purpose Motion Control IC	1-104
	HCTL-1100 OPT PLC	PLCC	CMOS General Purpose Motion Control IC	1
	HCTL-2000	PDIP	CMOS Quadrature Decoder/Counter IC, 12-bit Counter	1-86
	HCTL-2016	PDIP	CMOS Quadrature Decoder/Counter IC, 16-bit Counter	
	<b>New</b> HCTL-2016 OPT PLC	PLCC	CMOS Quadrature Decoder/Counter IC, 16-bit Counter	1-102
	HCTL-2020	PDIP	CMOS Quadrature Decoder/Counter IC, 16-bit Counter, Quadrature Decoder Output Signals, Cascade Output Signals	1-86
	<b>New</b> HCTL-2020 OPT PLC	PLCC	CMOS Quadrature Decoder/Counter IC, 16-bit Counter, Quadrature Decoder Output Signals, Cascade Output Signals	1-102

## Accessories for Encoders and Encoder Modules

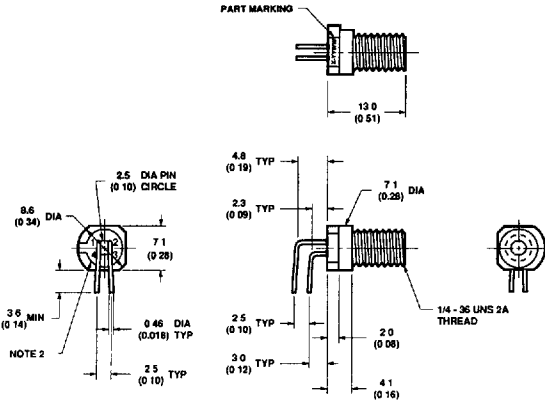
Package Outline Drawing	Part No.	Description	Page No.
	HEDS-8902	4-wire connector with 15.5 cm (6.1 in.) flying leads. Locks into HEDS-5500 and HEDS-5600 2 channel encoders. Also fits HEDS-9000, HEDS-9100, and HEDS-9200 2 channel encoder modules.	1-61 1-22 1-28
	HEDS-8903	5-wire connector with 15.5 cm (6.1 in.) flying leads. Locks into HEDS-5540 and HEDS-5640 three channel encoders. Also fits HEDS-9040 and HEDS-9140 three channel encoder modules.	1-61 1-32
	HEDS-8905	Alignment Tool for HEDS-9140	1-32
	HEDS-8906	Alignment Tool for HEDS-9040	1-32
	HEDS-8901	Gap Setting shown for film codewheels	1-51
	HEDS-8932	Gap Setting shown for glass codewheels	1-51
	HEDS-8910 OPT 0 □□	Alignment Tool for HEDS-5540/5545 and HEDS-5640/5645. Order in appropriate shaft size.	1-61

### Mechanical Dimensions HFBR-0400 SMA Series

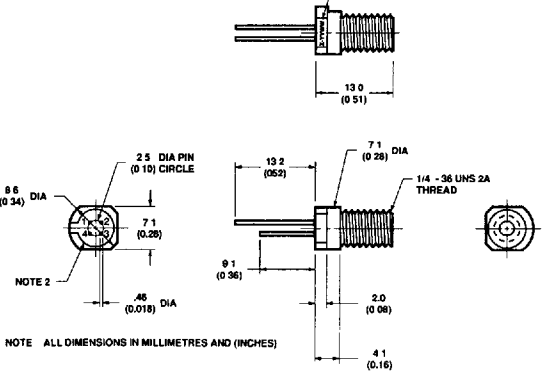
#### HFBR-X40X



#### HFBR-X43X



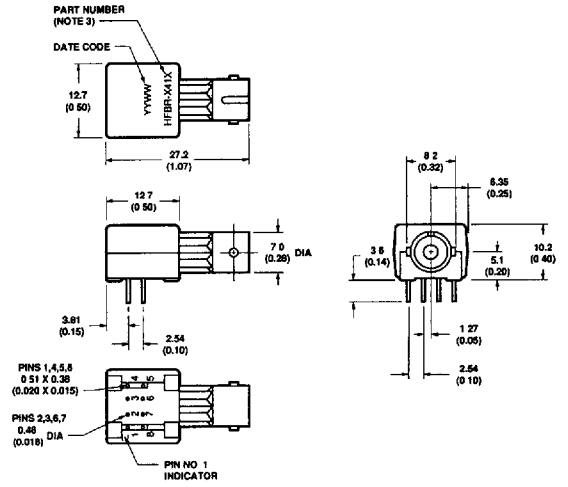
#### HFBR-X45X



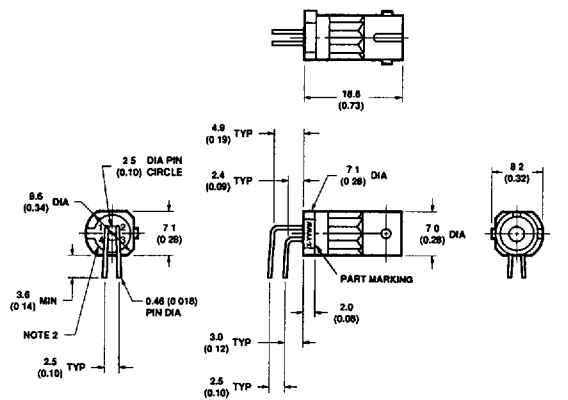
NOTE ALL DIMENSIONS IN MILLIMETRES AND (INCHES)

### Mechanical Dimensions HFBR-0400 ST Series

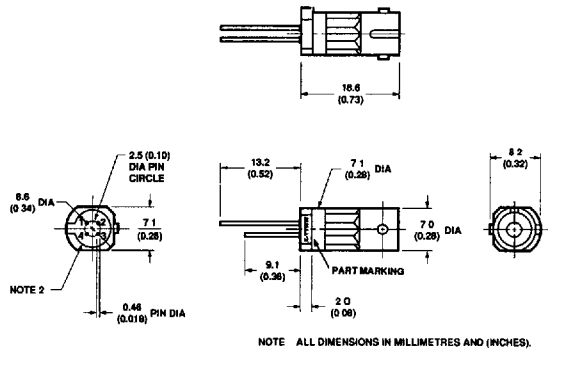
#### HFBR-X41X



#### HFBR-X44X



#### HFBR-X46X

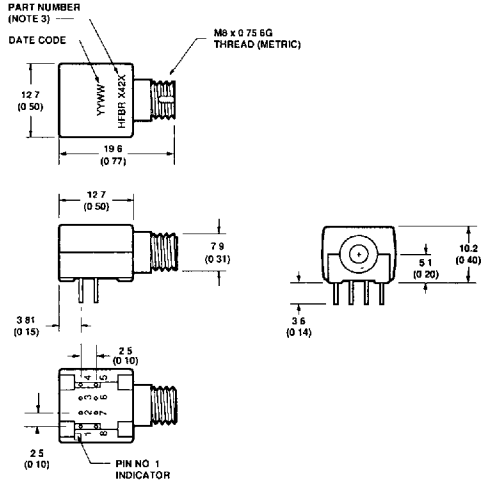
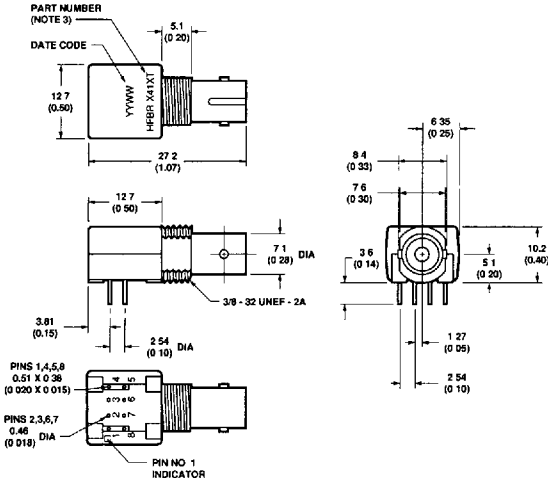


NOTE ALL DIMENSIONS IN MILLIMETRES AND (INCHES)

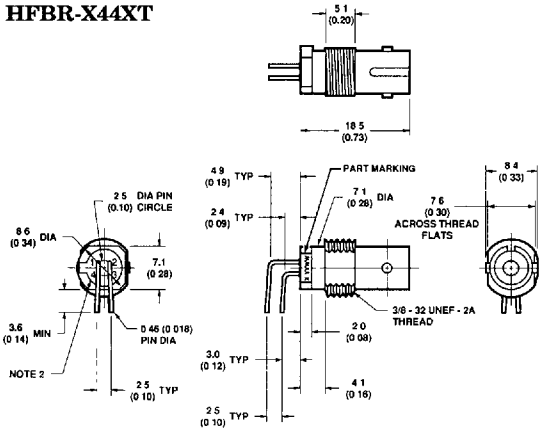
**Mechanical Dimensions  
HFBR-0400T Threaded ST Series**

**Mechanical Dimensions  
HFBR-0400FC Series**

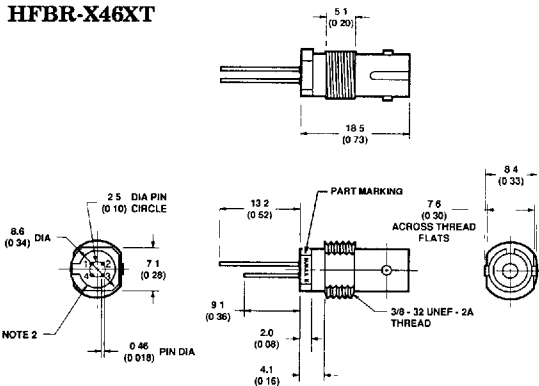
**HFBR-X41XT**



**HFBR-X44XT**

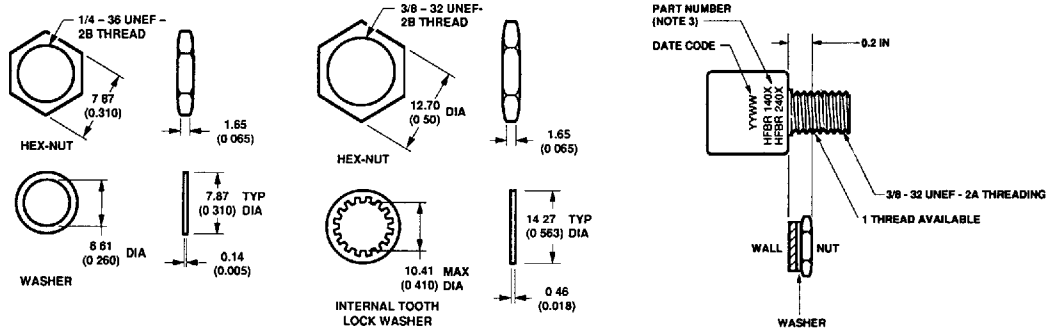


**HFBR-X46XT**



## Panel Mounting Hardware

### HFBR-4401: for SMA Ports    HFBR-4411: for ST Ports



(Each HFBR-4401 and HFBR-4411 kit consists of 100 nuts and 100 washers.)

## Port Cap Hardware

HFBR-4402: SMA Port Caps

HFBR-4412: ST Port Caps

(Each HFBR-4402 and HFBR-4412 consists of 500 port caps)

## Recommended Chemicals for Cleaning/Degreasing HFBR-0400 Products

- Alcohols (methyl, isopropyl, isobutyl)
- Aliphatics (hexane, heptane)
- Other (soap solution, naphtha)

(Do not use partially halogenated hydrocarbons (such as 1,1,1 trichloroethane), ketones (such as MEK), acetone, chloroform, ethyl acetate, methylene dichloride, phenol, methylene chloride, or N-methylpyrrolidone. Also, HP does not recommend the use of cleaners that use halogenated hydrocarbons because of their potential environmental harm.)

### Notes:

1. All dimensions are in millimetres and (inches).
2. Unhoused products are distinguished by the combination of the first character in the part marking and the part marking color. YYWW represents the date code

Base Part Number	Part Marking	Part Marking Color
HFBR-14X2	2-YYWW	Red
HFBR-14X4	4-YYWW	Red
HFBR-24X2	2-YYWW	White
HFBR-24X4	4-YYWW	White
HFBR-24X6	6-YYWW	White

This marking scheme does not distinguish between different connector styles.

3. The ports are shaded as shown below.



Transmitters



Receivers



Conductive Port Receivers