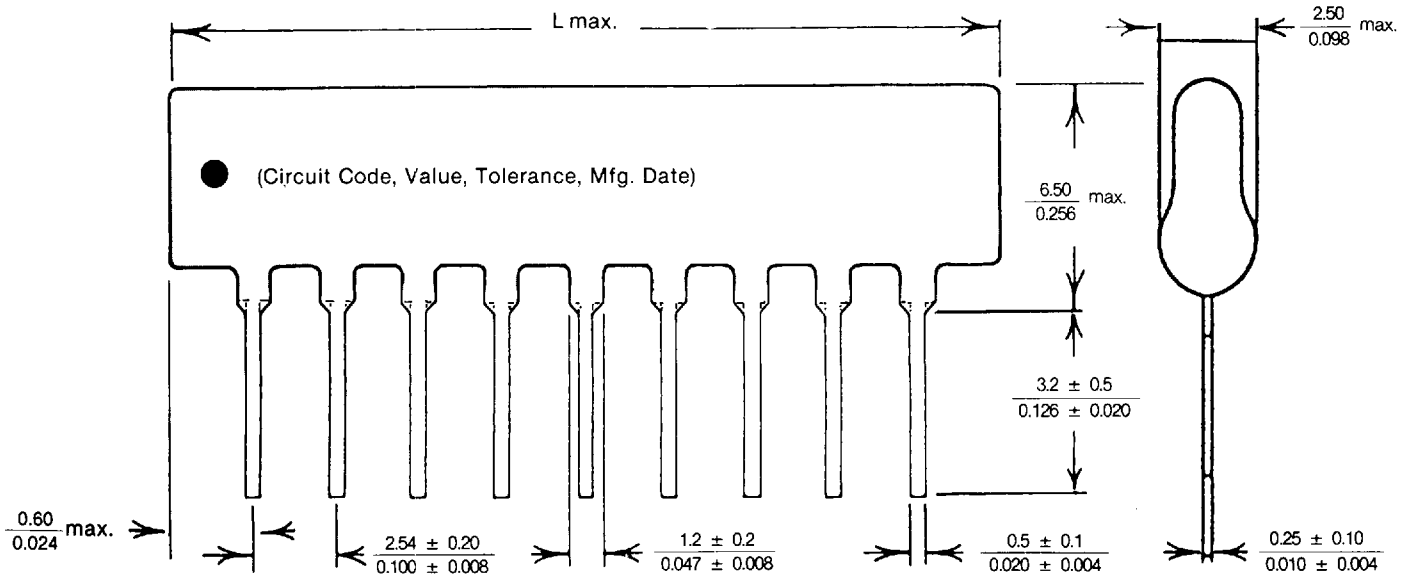




THICK FILM SIP RESISTOR MEDIUM PROFILE NETWORK



No. of Pins (P)	Body Length (L max.)
3 ~ 18	$2.56 \times P + 1.2$

RATING

RESISTANCE RANGE	R circuit	Others	OPERATING TEMP. RANGE	L circuit	Others
	100 Ω ~ 10K Ω	56 Ω ~ 2.2M Ω		-55°C ~ +125°C	-55°C ~ +155°C
T.C.R.	± 200ppm/°C		MAX. WORKING VOLTAGE	L circuit	Others
				20V	200V
RESISTANCE TOLERANCE	F	G	RATING AMBIENT TEMP.	+70°C	
	(±1%)	(±2%)		J	(±5%)
WATTAGE/ELEMENT	S circuit	Others			
	250mW	125mW	L circuit		
			20mW		

CIRCUIT CONSTRUCTIONS

CIRCUIT SYMBOL	B	S	C	D
NUMBER OF PINS	3 ~ 16	4 ~ 16	5 ~ 13	5 ~ 14
CIRCUIT CONSTRUCTION				
	$R_1 = R_2 = \dots = R_n$	$R_1 = R_2 = \dots = R_n$	$R_1 = R_2 = \dots = R_n$	$R_1 = R_2 = \dots = R_n$
A	T	E	R	L
4 ~ 14	5 ~ 13	5 ~ 13	4 ~ 18	6 ~ 12
$R_1 = R_2 = \dots = R_n$	$R_1 = R_2 \text{ OR } R_1 \neq R_2$	$R_1 = R_2 \text{ OR } R_1 \neq R_2$	$R_1 = R_2 \text{ OR } R_1 \neq R_2$	

CHARACTERISTICS

REQUIREMENTS	CHARACTERISTICS	TEST METHOD
		MIL-STD-202E
Operating Temp. Range	- 55°C ~ + 125°C	
Temp. Coefficient	±200ppm/°C	304 (Operating Temp. Range)
Short Time Overload	±(0.5% + 0.05Ω)	(MIL R 83401B 3.14)
Insulation Resistance	Over 10 ⁴ MΩ	302A
Dielectric Withstanding V.	±(0.5% + 0.05Ω)	301 D.C250V 60sec
Terminal Strength	Pull Test	±(0.5% + 0.05Ω)
	Bend Test	3 cycles and over
Resistance to Soldering Heat	±(0.5% + 0.05Ω)	211A, A 2lb
Solderability	95% min.	211A, B
Life (at Elevated Ambient Temp.)	±3%	210A, B
Temperature Cycling	±(0.5% + 0.05Ω)	208C
Endurance (Moisture Load)	±1% < 100K ≥ ±2%	108A 155°C/1000Hr
Endurance (Rated Load)	±1% < 100K ≥ ±2%	102A -40°C 85°C
		103B 1000Hr
		108A 70°C 1000Hr

ORDERING DESIGNATION:

RKC	1/8 B	X	XXX	X	XXX
TYPE	NUMBER OF RESISTORS	CIRCUIT SYMBOL	NOMINAL RESISTANCE	RESISTANCE TOLERANCE	PACKAGING
CIRCUIT SYMBOL B, S, C, D,		None = "B"	2 Significant Figs. + 10 x multiplier	F = ±1% G = ±2% J = ±5%	None = Bulk/Poly Bag STP = Stick Magazine STB = Stick Magazine TBA = Perf. Taping TPA = Perf. Taping

RKC	X	X	R ₁ /R ₂ XXX/XXX	X	XXX
TYPE	NUMBER OF RESISTORS	CIRCUIT SYMBOL	RESISTANCE TOLERANCE	NOMINAL RESISTANCE	PACKAGING
CIRCUIT SYMBOL A, T, E, R,			2 Significant Figs. + 10 x multiplier	F = ±1% G = ±2% J = ±5%	None = Bulk/Poly Bag STP = Stick Magazine STB = Stick Magazine TBA = Perf. Taping TPA = Perf. Taping

RKC	X	L	XXX	XXX
TYPE	NUMBER OF BITS	CIRCUIT SYMBOL	OUTPUT IMPEDANCE	PACKAGING
CIRCUIT SYMBOL L	4 ~ 8		R/2R	None = Bulk/Poly Bag STP = Stick Magazine STB = Stick Magazine TBA = Perf. Taping TPA = Perf. Taping