

26–40 GHz Surface Mount Low Noise Amplifier



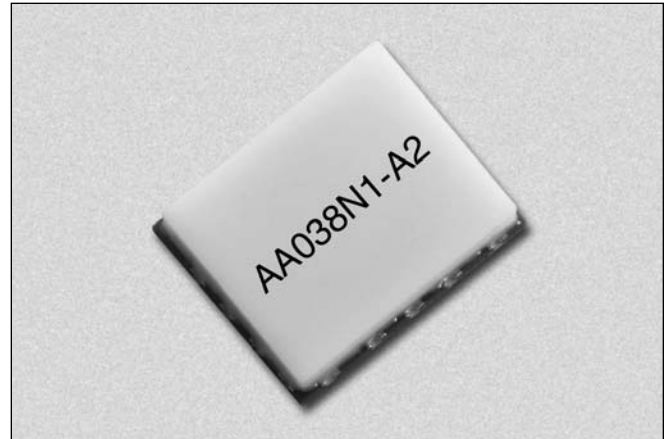
AA038N1-A2
Patent Pending

Features

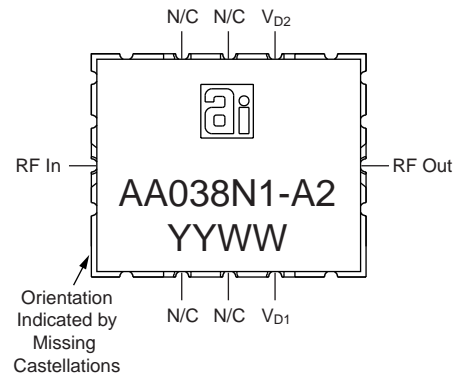
- Surface Mount Package
- 3.8 dB Noise Figure
- 18 dB Gain
- +5 dBm Output Power
- Single Voltage Operation
- 100% RF and DC Testing

Description

The AA038N1-A2 is a broadband millimeterwave low noise amplifier in a rugged surface mount package which is compatible with high-volume solder installation. The amplifier is designed for use in millimeterwave communication and sensor systems as the receiver front-end or as a gain stage when high gain and low noise figure are required. The robust ceramic surface mount package provides excellent electrical performance and a high degree of environmental protection for long-term reliability. A single supply voltage simplifies bias requirements. All amplifiers are screened at the operating frequencies prior to shipment for guaranteed performance. Amplifier is targeted for high-volume millimeterwave applications such as point-to-point and point-to-multipoint wireless communications systems.



Pin Out



Electrical Specifications at 25°C ($V_{D1} = V_{D2} = 5.5\text{ V}$)

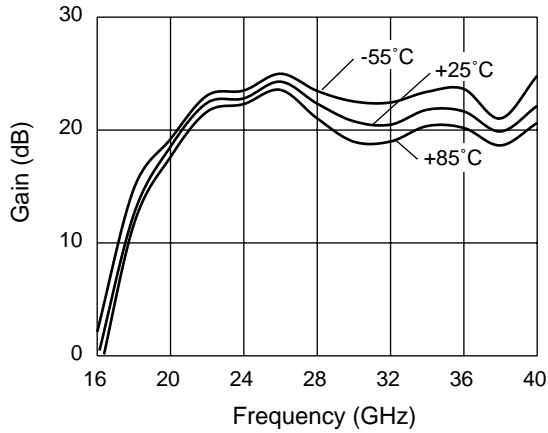
RF

Parameter	Symbol	Low Band			High Band			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
Bandwidth	BW	26		36	36		40	GHz
Small Signal Gain	G	16	20		14	18		dB
Noise Figure	NF		3.5	4.0		3.8	4.2	dB
Input Return Loss	RL _I		7			7		dB
Output Return Loss	RL _O		10			10		dB
Output Power at 1 dB Gain Compression	P _{1 dB}	4	6		3	5		dBm
Temperature Coefficient of Gain	dG/dT		-0.024			-0.024		dB/C

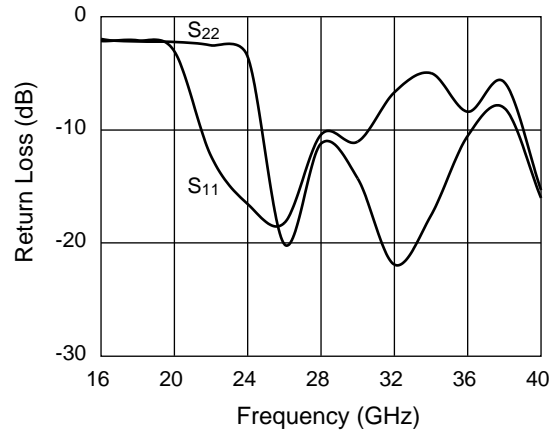
DC

Parameter	Symbol	Min.	Typ.	Max.	Unit
Drain Current 1	I _{D1}		27		mA
Drain Current 2	I _{D2}		1		mA
Total Drain Current	I _{D1} + I _{D2}		28	36.5	mA

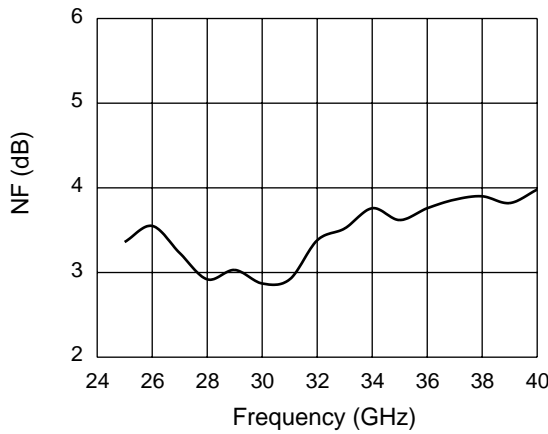
Typical Performance Data ($V_{D1} = V_{D2} = 5.5\text{ V}$)



Gain vs. Frequency



Return Loss vs. Frequency

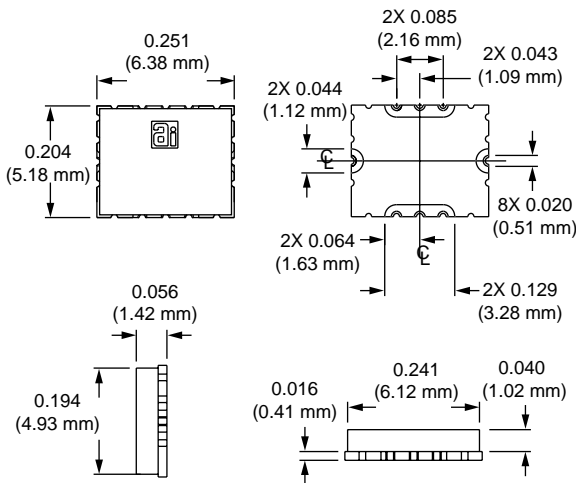


Noise Figure vs. Frequency

Absolute Maximum Ratings

Characteristic	Value
Operating Temperature (T_C)	-55°C to +90°C
Storage Temperature (T_{ST})	-65°C to +150°C
Bias Voltage (V_{D1})	6 V_{DC}
Bias Voltage (V_{D2})	6 V_{DC}
Power In (P_{IN})	13 dBm

Outline



Typical S-Parameters at 25°C ($V_{D1} = V_{D2} = 5.5$ V)

Frequency (GHz)	S ₁₁		S ₂₁		S ₁₂		S ₂₂	
	Mag. (dB)	Ang. (Deg.)	Mag. (dB)	Ang. (Deg.)	Mag. (dB)	Ang. (Deg.)	Mag. (dB)	Ang. (Deg.)
1	-0.10	-36.2	-56.13	95.6	-87.47	-127.6	-0.81	-25.8
5	-0.44	-163.0	-54.22	-61.5	-55.02	-76.5	-1.28	-120.5
8	-0.40	108.1	-57.72	71.0	-57.51	90.8	-0.91	167.3
10	0.45	40.9	-37.91	23.4	-68.29	77.3	-0.20	109.3
12	-0.60	-24.1	-22.15	-149.4	-52.99	-78.8	-0.30	41.7
14	-1.72	-79.1	-13.38	105.0	-50.50	164.9	-0.85	-24.3
16	-2.12	-127.4	-0.36	26.3	-46.51	100.7	-1.94	-81.5
18	-2.06	-177.4	12.55	-103.6	-47.48	97.4	-2.16	-126.2
20	-3.04	130.5	18.52	98.0	-46.68	2.7	-2.20	-162.9
22	-12.31	51.9	22.41	-46.6	-48.30	-46.2	-2.50	149.9
24	-16.56	107.8	22.80	176.4	-50.31	-56.4	-3.50	77.9
26	-18.23	166.7	24.29	37.1	-45.19	-127.4	-20.12	-68.5
28	-10.41	127.7	22.34	-56.8	-44.48	139.6	-11.20	115.6
30	-11.04	122.4	20.77	-133.7	-51.03	95.7	-14.29	57.4
32	-6.66	102.8	20.48	147.9	-62.00	-37.2	-21.93	84.0
34	-4.96	65.0	21.81	62.0	-51.71	20.6	-17.56	109.6
36	-8.38	37.5	21.65	-50.1	-50.60	-68.2	-10.52	121.2
38	-5.77	15.4	19.87	-128.0	-50.27	-122.4	-8.06	49.4
40	-15.32	-50.3	22.14	122.8	-55.45	98.0	-16.02	-86.6

“Alpha Two” Surface Mount Package Handling and Mounting

Millimeterwave components require careful mounting design to maintain optimal performance. The Alpha Two surface mount package (patent pending) provides a rugged and repeatable electrical connection using standard solder techniques.

Handling

The Alpha Two surface mount package is very rugged. However, due to ceramic's brittle nature, one should exercise care when handling with metal tools. Do not apply heavy pressure to the lid. Vacuum tools may be used to pick and place this part.

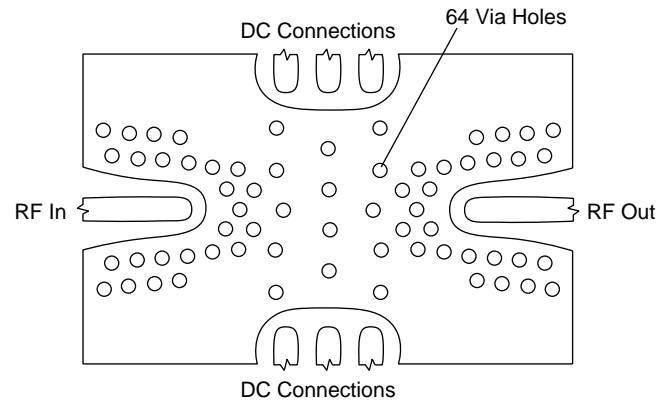
Only personnel trained in both ESD precautions and handling precautions should be allowed to handle these packages.

Package Construction

The Alpha Two surface mount package consists of a base and a lid. The package base is ceramic with filled vias and plated castellations. The package lid is unplated alumina. The lid seal is epoxy.

Mounting Design

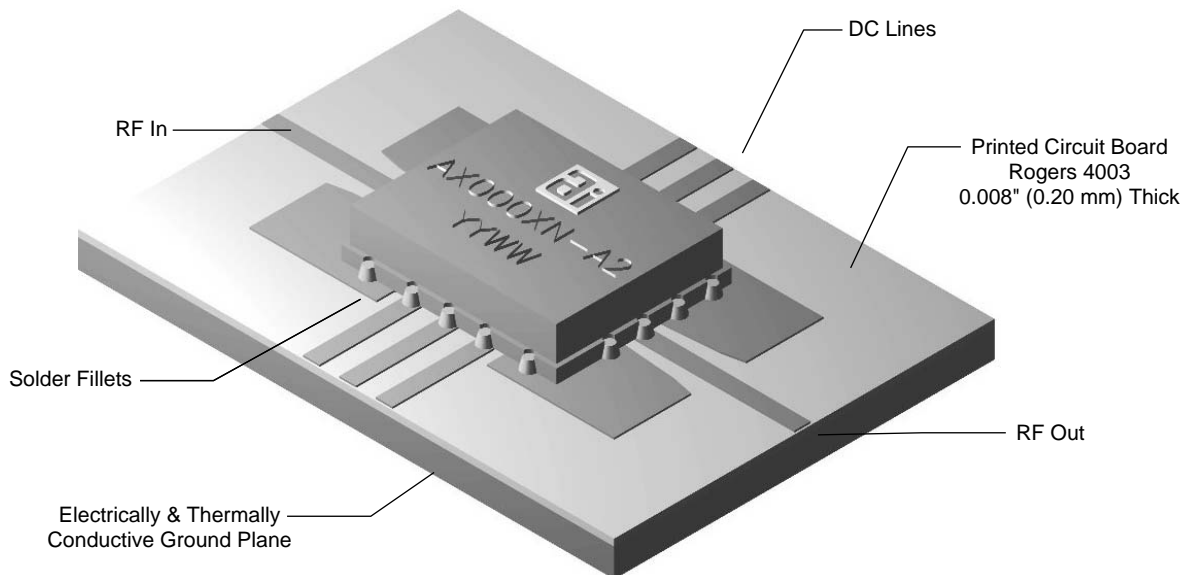
The Alpha Two surface mount package is installed on top of a printed circuit board on a specially designed footprint. Mounting footprint geometry will be supplied by Alpha Industries in electronic formats or paper drawing.



**Footprint Geometry for Alpha Two
Surface Mount Package**

Mounting the Package

The Alpha Two surface mount package is compatible with high-volume surface mount installation using solder. RF and DC connections are accomplished with metallized edge castellations that hold solder fillets. Ground connections are accomplished by both metallized edge castellations and filled vias to the bottom of the package. Care should be taken to ensure that there are no voids or gaps in the solder so that a good RF, DC, and ground contact is maintained.



Alpha Two Surface Mount Package Installation