

PRELIMINARY SPECIFICATION

DESCRIPTION

The NE590/NE591 addressable peripheral drivers are high current latched drivers, similar in function to the 9334 address decoder. The device has 8 darlington power outputs, each capable of 250mA load current. The outputs are turned on or off by respectively loading a logic "1" or logic "0" into the device data input. The required output is defined by a 3 bit address. The device must be enabled by a \overline{CE} input line which also serves the function of further address decoding. A common clear input, \overline{CLR} , turns all outputs off when a logic "0" is applied.

The NE590 has 8 open collector darlington outputs which sink current to ground. The device is packaged in a 16 pin molded or cerdip package.

The NE591 has 8 open emitter darlington outputs which source current to an external load from a common collector line, V_S . The V_S line need not necessarily be the same as the 5 volt V_{CC} supply. The device is packaged in an 18 pin molded or cerdip package.

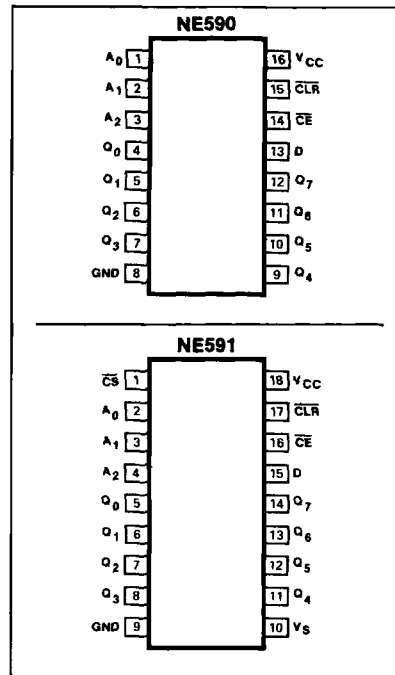
FEATURES

- 8 high current outputs
- Low-loading bus compatible inputs
- Power-on clear ensures safe operation
- NE590 will operate in addressable or demultiplex mode
- Allows random (addressed) data entry
- Easily expandable
- NE590 is pin compatible with 9334.

APPLICATIONS

- Relay driver
- Indicator lamp driver
- Triac trigger
- LED display digit driver
- Stepper motor driver

PIN CONFIGURATIONS



DESCRIPTION

The NE5030 is an 8-bit A-to-D converter employing the triple-slope technique of conversion. The device includes a voltage reference and clock generator and so constitutes the total conversion system with a minimum of components. Conversion time is approximately 10mS; voltage requirement is a single 5V supply and the data outputs are tri-state bus compatible.

PIN CONFIGURATION

