

181                      Orig  
005017  
5017                      ADG

**APPLICATIONS:**

- Fiber Optic Decision Circuits
- A/D Converters
- Line Receivers
- Peak and Threshold Detectors

**POWER SUPPLIES**

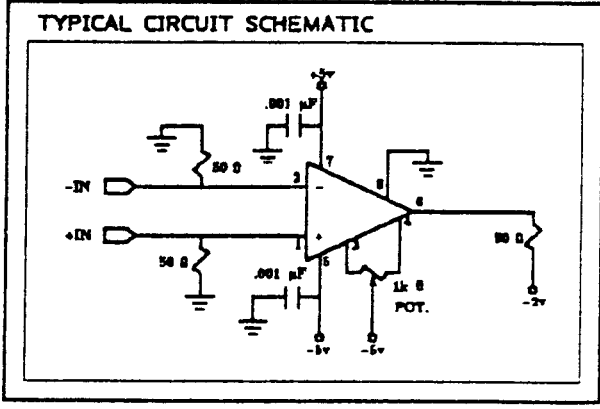
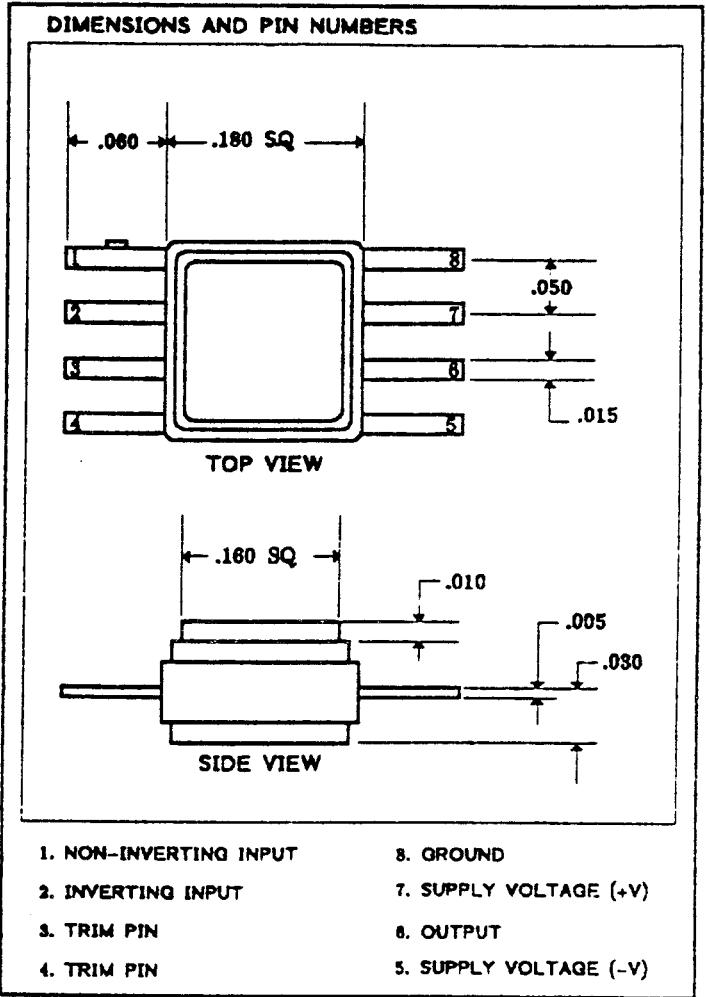
Positive Supply (+V) ----- = +5v  
 Negative Supply (-V) ----- = -5v  
 Max Supply Current(I<sub>s</sub>) ----- = 20mA

**DC SPECIFICATIONS**

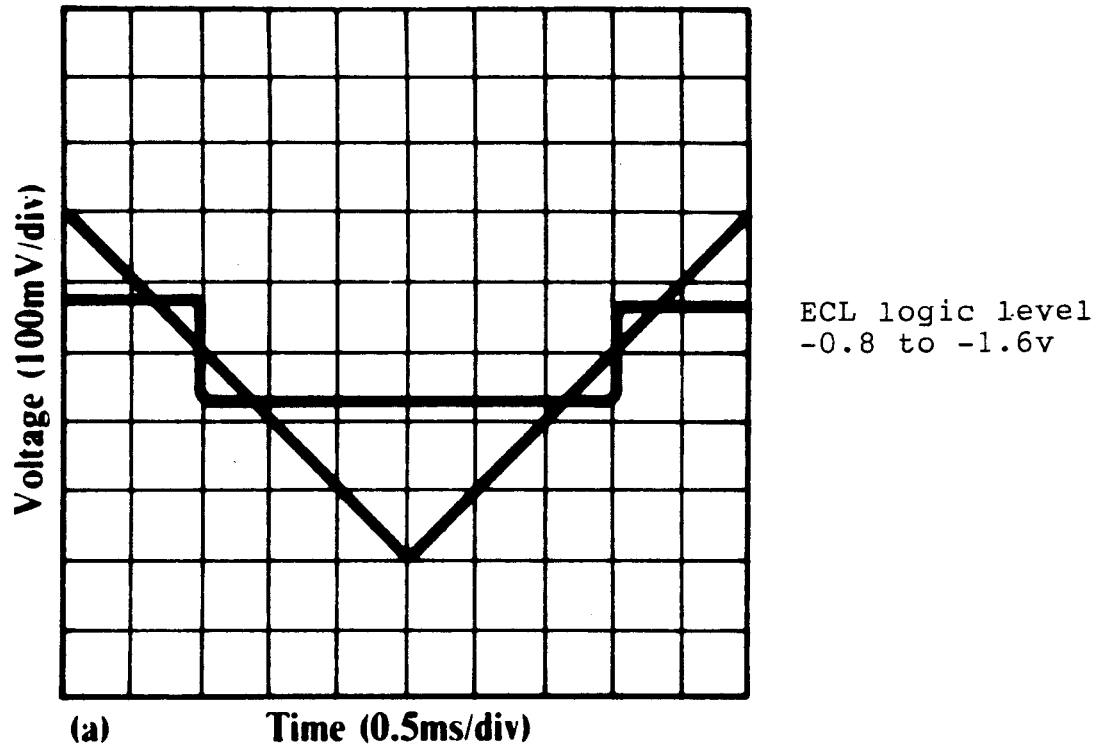
Input Current ----- = 100nA  
 Input Offset Voltage ----- = +30mv  
 Common Mode Input Range = +1v , -2v  
 Output ECL Compatible ( 50Ω Load to -2v )

**AC SPECIFICATIONS**

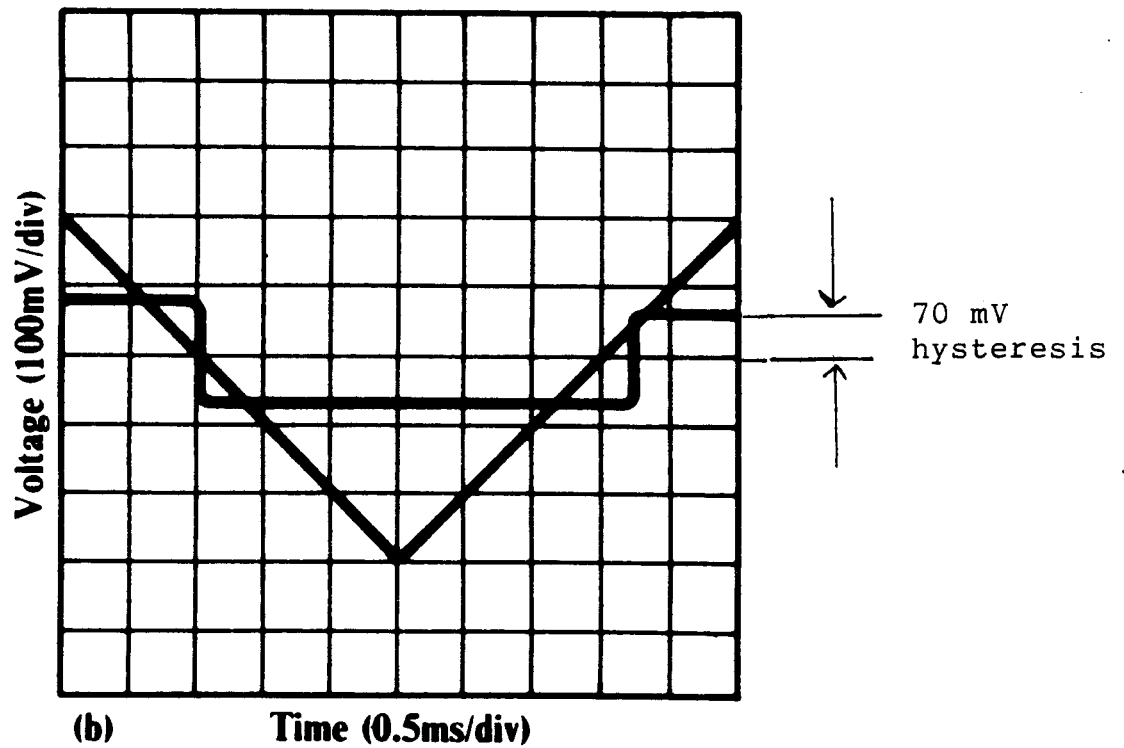
Propagation Delay ( 20mv Overdrive ) = 1.0nsec  
 Propagation Delay ( 100mv Overdrive ) = 0.5nsec  
 Gain ----- = 100



*Anadigics reserves the right to make changes in specifications without notice.*



ANADIGICS ACP10010 switching characteristics showing no hysteresis (at 240 Hz)



Same comparator with removal of "hysteresis-killing" circuitry