

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

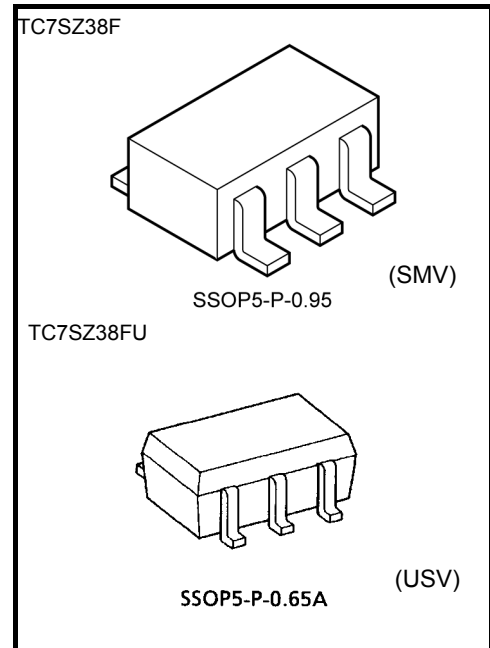
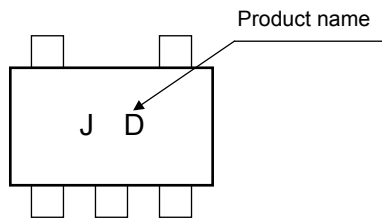
# TC7SZ38F, TC7SZ38FU

2-Input NAND Gate(Open Drain Output)

## Features

- High output current: 24 mA (min) @V<sub>CC</sub> = 3 V
- Super high speed operation:  
 $t_{pz} = 2.2 \text{ ns (typ.) @ } V_{CC} = 5 \text{ V, } 50 \text{ pF}$
- Operation voltage range: V<sub>CC (opr)</sub> = 1.65 to 5.5 V
- 5.5V tolerant inputs.
- Power down protection is provided on output.
- Matches the performance of TC74LCX series when operated at 3.3 V V<sub>CC</sub>.

## Marking

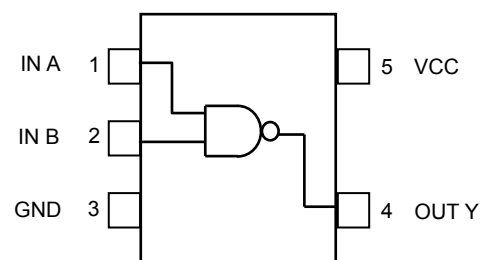


Weight:  
 SSOP5-P-0.95 : 0.016 g (typ.)  
 SSOP5-P-0.65A : 0.006 g (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5 to 6	V
DC input voltage	V <sub>IN</sub>	-0.5 to 6	V
DC output voltage	V <sub>OUT</sub>	-0.5 to 6 (Note 1)	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	I <sub>OK</sub>	-20 (Note 2)	mA
DC output current	I <sub>OUT</sub>	-50	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	P <sub>D</sub>	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C
Lead temperature (10s)	T <sub>L</sub>	260	°C

## Pin Assignment (top view)



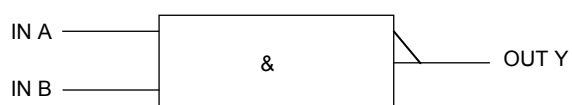
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 2: V<sub>OUT</sub> < GND

## IEC Logic Symbol



## Truth Table

Input		Output
A	B	Y
L	L	Z
L	H	Z
H	L	Z
H	H	L

Z: High Impedance

## Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	1.65 to 5.5	V
		1.5 to 5.5 (Note 3)	
Input voltage	$V_{IN}$	0 to 5.5	V
Output voltage	$V_{OUT}$	0 to 5.5 (Note 4)	V
		0 to $V_{CC}$ (Note 5)	
Operating temperature	$T_{opr}$	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20 ( $V_{CC} = 1.80\text{ V} \pm 0.15\text{ V}, 2.5\text{ V} \pm 0.2\text{ V}$ )	ns/V
		0 to 10 ( $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ )	
		0 to 5 ( $V_{CC} = 5.0\text{ V} \pm 0.5\text{ V}$ )	

Note 3: Data retention only

Note 4: OFF state

Note 5: Low state

## Electrical Characteristics

### DC Characteristics

Characteristics		Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit		
				V <sub>CC</sub> (V)	Min	Typ.	Max	Min		Max	
Input voltage	High level	V <sub>IH</sub>	—	1.65 to 1.95	V <sub>CC</sub> × 0.75	—	—	V <sub>CC</sub> × 0.75	—	V	
				2.3 to 5.5	V <sub>CC</sub> × 0.75	—	—	V <sub>CC</sub> × 0.75	—		
	Low level	V <sub>IL</sub>	—	1.65 to 1.95	—	—	V <sub>CC</sub> × 0.25	—	V <sub>CC</sub> × 0.25		
				2.3 to 5.5	—	—	V <sub>CC</sub> × 0.3	—	V <sub>CC</sub> × 0.3		
High level output leakage current		I <sub>LKG</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	1.65 to 5.5	—	—	±5	—	±10	μA	
Output voltage	Low level	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 100 μA	1.65	—	0	0.1	—	0.1	V
					1.8	—	0	0.1	—	0.1	
					2.3	—	0	0.1	—	0.1	
					3.0	—	0	0.1	—	0.1	
					4.5	—	0	0.1	—	0.1	
				I <sub>OL</sub> = 4 mA	1.65	—	0.08	0.24	—	0.24	
				I <sub>OL</sub> = 8 mA	2.3	—	0.1	0.3	—	0.3	
				I <sub>OL</sub> = 16 mA	3.0	—	0.15	0.4	—	0.4	
				I <sub>OL</sub> = 24 mA	3.0	—	0.22	0.55	—	0.55	
				I <sub>OL</sub> = 32 mA	4.5	—	0.22	0.55	—	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND	0 to 5.5	—	—	±1	—	±10	μA	
Power off leakage current		I <sub>OFF</sub>	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V	0.0	—	—	1	—	10	μA	
Quiescent supply current		I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5	—	—	2	—	20	μA	

**AC Characteristics (unless otherwise specified, Input:  $t_r = t_f = 3$  ns)**

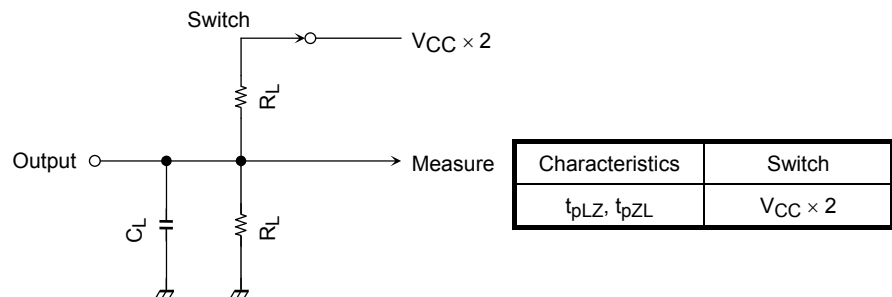
Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit	
			VCC (V)	Min	Typ.	Max	Min		Max
Propagation delay time	tpZL	CL = 50 pF, RL = 500 Ω (Figure 1)	1.80 ± 0.15	1.5	6.5	12.7	1.5	13.2	ns
			2.5 ± 0.2	0.8	3.5	7.0	0.8	7.5	
			3.3 ± 0.3	0.8	2.8	5.0	0.8	5.2	
			5.0 ± 0.5	0.5	2.2	4.3	0.5	4.5	
	tpLZ	CL = 50 pF, RL = 500 Ω (Figure 1)	1.80 ± 0.15	1.5	5.5	12.7	1.5	13.2	ns
			2.5 ± 0.2	0.8	3.0	7.0	0.8	7.5	
			3.3 ± 0.3	0.8	2.1	5.0	0.8	5.2	
			5.0 ± 0.5	0.5	1.3	4.3	0.5	4.5	
Input capacitance	CIN	—	0 to 5.5	—	4	—	—	—	pF
Output capacitance	COU	—	5.5	—	3	—	—	—	pF
Power dissipation capacitance	CPD	(Note 6)	3.3	—	6.7	—	—	—	pF
			5.5	—	13	—	—	—	

Note 6: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

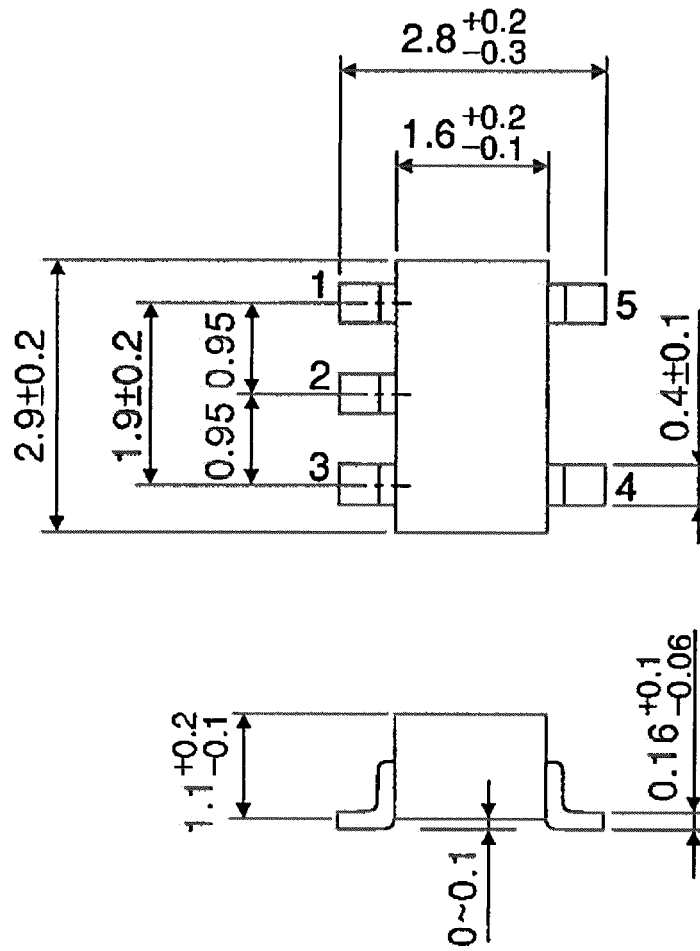
**Figure 1 : AC Characteristics Measurement Circuit**



## Package Dimensions

SSOP5-P-0.95

Unit : mm



Weight: 0.016 g (typ.)



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