

FAIRCHILD INTERFACE

LEVEL TRANSLATORS

LEVEL TRANSLATORS (Cont'd)

Item	DEVICE NO. ^(1,2)	Function	Supply Voltage V ⁺ (Typ)	Supply Voltage V ⁻ (Typ)	VOH-V (Min)	VOL-V (Max)	I _{pd} -ns (Typ)	Power Dissipation mW	Logic/Connection Diagram(s)	Package(s)
1	9112	TTL-HLDTL Hex	+12 to +20	0 0	(+V)-2 0	+0 4	90	440	G12	6A
2	9595	Dual ECL-TTL	+5 0	-5 2	+2 4	+0 4	6 0	375	E15	6B
3	9624	TTL-MOS	+5 0	0 0 to -30	V _{TAP} -1 0	(-V)+2 0	120	40	19	3I,6A,9A
4	9625	MOS-TTL Dual	+5 0	0 0 to -30	+3 2	+0 4	70	60	110	3I,6A,9A
5	9643	Dual TTL to MOS Driver	+5 0 to +12	0 0	V _{CC} -0 5	+0 3	8 0	—	157, 58	6A,6T, 9A,9T
6	9644	Dual TTL to MOS Driver	+5 0 to +12	0 0	V _{CC} -0 5	+0 3	8 0	—	157	6T,9T
7	9645	Quad TTL to MOS Driver	+5 0	0 0	V _{CC} -0 5	+0 3	8 0	—	159	6B,9B
8	9646	Dual MOS Clock Driver	-22 to +22	0 0	V _{CC} -0 5	+1 0	30	—	160, 61	6A,9A 9T
9	11C24	Dual TTL Voltage Controlled Multivibrator	+5 0	—	+2 5	+0 5	30	160	E19	6A
10	11C44	Phase-Freq Detector	+5 0	—	+2 5	+0 5	—	165	E20	6A
11	11C58	ECL Voltage Controlled Multivibrator	+5 0	-5 2	-0 96	-1 62	—	260	E21	6B
12	10124/ 10524	TTL-ECL Quad Diff Driver	+5 0	-5 2	-0 96	-1 65	3 0	265	E16	4L,6B, 9B
13	10125/ 10525	ECL-TTL Quad Buffer	+5 0	-5 2	+2 5	+0 5	3 0	410	E17	4L,6B 9B
14	10177	ECL to MOS	+5 0 or +6 0	-5 2	+3 0 or +4 0	+0 5 or +0 6	6 0	430	E106	4L,6B 9B
15	95124	TTL-ECL Quad Diff Driver	+5 0	-5 2	-1 05	-1 595	3 0	295	E16	6B

1 In some cases only commercial temperature range devices are given. Please request specific information for military versions.

2 105XX and 106XX denote military temperature range.

FAIRCHILD LOGIC/CONNECTION DIAGRAMS

INTERFACE

