

**BOURNS®**

**Features**

- Designed to meet ANSI and ETSI requirements
- Designed to meet UL1950 and EN60950 supplementary insulation requirements for operating voltages up to 250 Vrms
- Excellent Total Harmonic Distortion (THD) performance

- Up to 100 mA DC current capability
- Operating temperature -40 °C to +85 °C
- Custom designs for other pinouts and dimensions are available

**Applications**

- Motorola MC03AX1456

**PT314 Series EP13 ADSL Line Transformers**

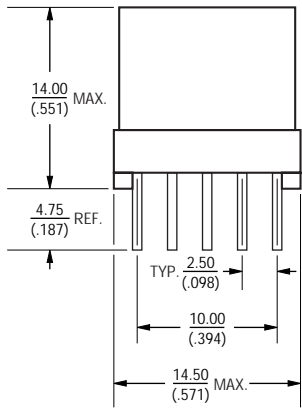
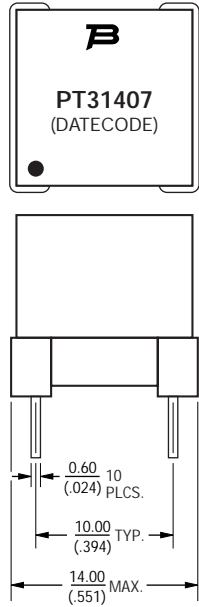
**Electrical Specifications**

IC Manufacturer .....Motorola  
 Chipset .....MC03AX1456  
 Application .....CO & CPE  
 Turns Ratio Line: Chip ±3 %  
 PT31407 .....1.2 : 1  
 PT31408 .....1 : 1.8  
 Line Side Inductance .....1.8 mH min.  
 Leakage Inductance .....10 µH typ.  
 Line Side DCR .....1.26 Ω max.  
 Chip Side DCR  
 PT31407 .....1.2 Ω max.  
 PT31408 .....8 Ω max.  
 THD (dB typ. @ 100 Ω)  
 .....-80 dB @ 30 kHz  
 .....-60 dB @ 300 kHz  
 Insertion Loss (dB typ.)  
 .....0.5 dB @ 100 kHz  
 Longitudinal Balance (dB typ. @ 100 Ω)  
 .....50 dB @ 20 kHz - 100 MHz  
 HIPOT .....1500 Vrms min.

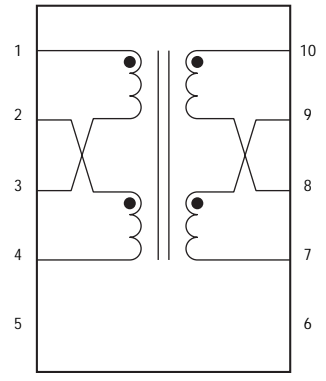
**Packaging Specifications**

Tray .....60 pcs./tray

**Product Dimensions**



**Electrical Schematic**



DIMENSIONS ARE:  $\frac{\text{MM}}{\text{(INCHES)}}$



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