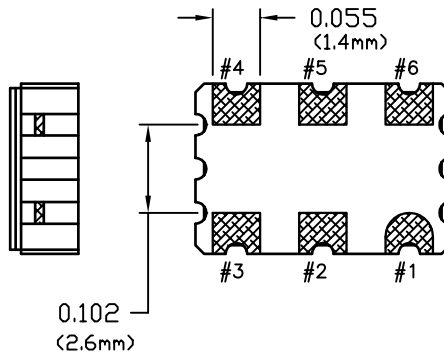
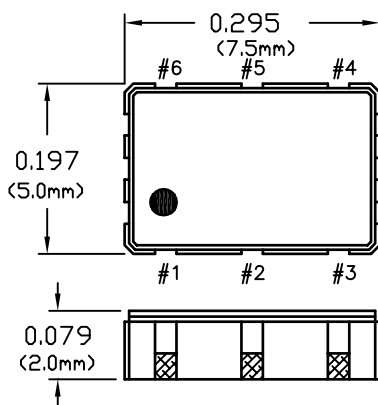


**5V SM HCMOS VCXO WITH TRI-STATE**

SPECIFICATIONS		VKA61A2	
Frequency Range		1.0MHz to 52MHz	
Frequency Vs Temperature		±25ppm	
Temperature Range		-40°C to +85°C	
Output	Waveform	HCMOS/TTL Compatible Squarewave	
	Load	10NTTL/15pF	
	Voltage	Voh	4.5V Minimum
		Vol	0.5V Maximum
	Current	Ioh	-4.0mA
		Iol	16.0mA
	Duty Cycle	40/60 Maximum @ 2.5V	
Rise/Fall Time		5nS Maximum	
Tri-State Input	Output E/D Time	100nS Typical	
	Enable (Vih)	2.7V Minimum	
	Disable (Vil)	0.3V Maximum	
Oscillator output is enabled with no connection on pin 2			
Frequency Control Input		Positive Transfer Characteristic	
Pullability		±100ppm Minimum	
Control Voltage (Vc)		0.5Vdc to 4.5Vdc	
Center Frequency		2.5Vdc ±0.5Vdc	
Monotonic Linearity		< ±10%	
Input Impedance		50K ohms Nominal	
Modulation Bandwidth		10KHz Minimum	
Supply Voltage		+5Vdc ±5%	
Supply Current		1.0 to 18 MHz, 20 mA Maximum 18 to 36 MHz, 30 mA Maximum 36 to 52 MHz, 40 mA Maximum	
Package		Hermetically sealed, leadless ceramic package	

NOTE 1- A bypass capacitor of .01uF must be used between Vdd and Gnd.



PIN	CONNECTION
1	CONTROL VOLTAGE
2	TRI-STATE E/D
3	GND
4	OUT
5	N/C
6	VDD

