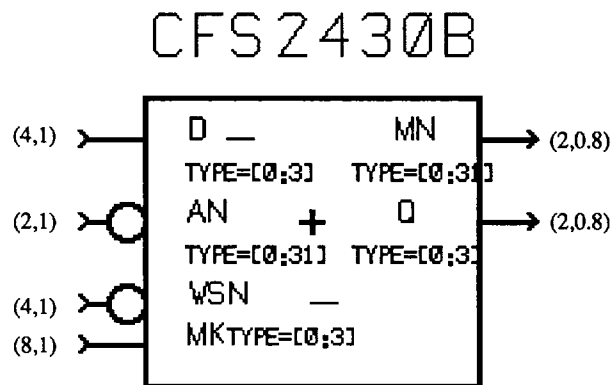


GENERAL DESCRIPTION: 32 X 4 CAM WITH MASK

CFS2430B is a 32-word by 4-bit content addressable memory (CAM). This megafunction is functionally identical to the National (Fairchild) 100142 except that it has 32 words instead of four. For a detailed functional description see the relevant standard product data book.

PIN CONNECTION DIAGRAM:**FEATURES:**

- Mask data inputs
- Each word has its own address select
- Data inputs can compare each of four memory words

EQUIVALENT USED GATES: 1786 GATES
(for rough area estimates)

THIS MEGAFUNCTION CONSISTS OF :
1786 soft-coded gates.

POWER: NOT AVAILABLE.

FAULT COVERAGE(%): 99%

PIN DESCRIPTION:

D3:0 DATA INPUTS.
 AN31:0 ADDRESS INPUTS.
 WSN WRITE STROBE INPUT.
 MK3:0 DATA MASK INPUTS.
 MN31:0 MATCH OUTPUTS.
 Q3:0 DATA OUTPUTS.

PROPAGATION DELAY*:

SET-UP AND HOLD TIMES *:

PROPAGATION DELAY*			SET-UP AND HOLD TIMES *		
	Q0:3 (ns)	MN0:31 (ns)	FROM INPUT	SET-UP TIME (ns)	HOLD TIME (ns)
AN0:31	8.3	6.6	AN0:31	0.0	2.0
D0:3	8.5	6.8	D0:3	0.5	1.0
MK0:3	8.3	6.6	MK0:3	0.5	2.0
WSN	9.2	7.5	*MINIMUM WSN WIDTH: 3.0 ns		

*CMOS10K, NOMINAL CONDITIONS, DELAY PREDICTION BY LPACE.