

100389

Single Supply Hex CMOS-to-ECL Translator with Cutoff Drivers

General Description

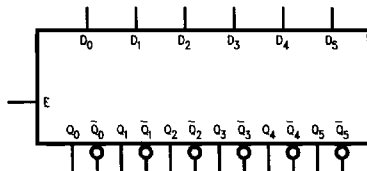
The 100389 is a hex translator for converting CMOS logic levels to positive referenced F100K ECL logic levels (or PECL). This translator is designed to operate from a single +5V supply, eliminating the need for a separate -5V supply. The differential outputs of the 100389 due to its high common mode rejection, overcomes voltages gradients between the CMOS and ECL ground systems.

A LOW on the input enable pin (E) sets the true outputs to a LOW state and the inverting outputs to a HIGH state.

Features

- Operates from a single +5V supply
- Differential ECL outputs
- 2000V ESD protection

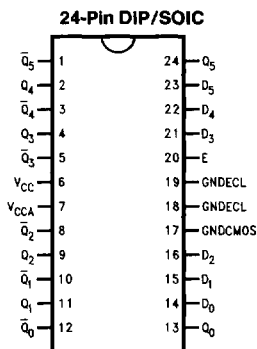
Logic Symbol



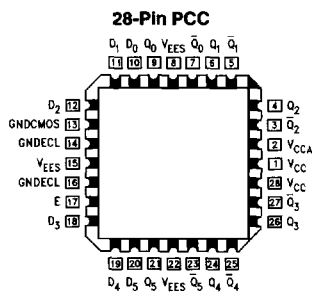
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Pin Names	Description
D ₀ -D ₅	Data Inputs (CMOS)
Q ₀ -Q ₅	Data Outputs (PECL)
\bar{Q}_0 - \bar{Q}_5	Inverting Data Outputs (PECL)
E	Enable Input (CMOS)

Connection Diagrams



TL/F/10999-2



TL/F/10999-3