



SPST ECL Switches With Drivers

2662 Series

Features

- Broadband Frequency Ranges
- Environmentally Sealed
- ECL Compatible

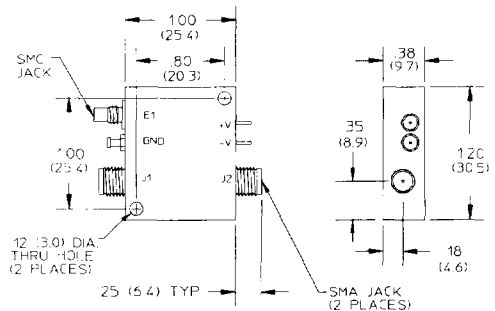
Description

M/A-COM's Emitter Coupled Logic (ECL) PIN diode switches offer multi-octave bands from UHF to Ku-band. Advantages of an ECL switch are narrow pulse width (25 ns) and high pulse repetition rates (20 MHz). These devices typically have lower RF power characteristics due to the thin I-region needed for fast transition times. Standard options for bias voltages are not available on ECL switches. Typical insertion loss, VSWR, and isolation curves are shown below.

Environmental

These devices are designed to meet the following conditions:

Test	MIL-STD	Method	Cond
Temperature Cycle	883	1010	C
Const. Acceleration	883	2001	A
Vibration	202	214	
Solvent Resistance	883	2015	
Salt Spray	202	101	A
Moisture Resistance	202	106	

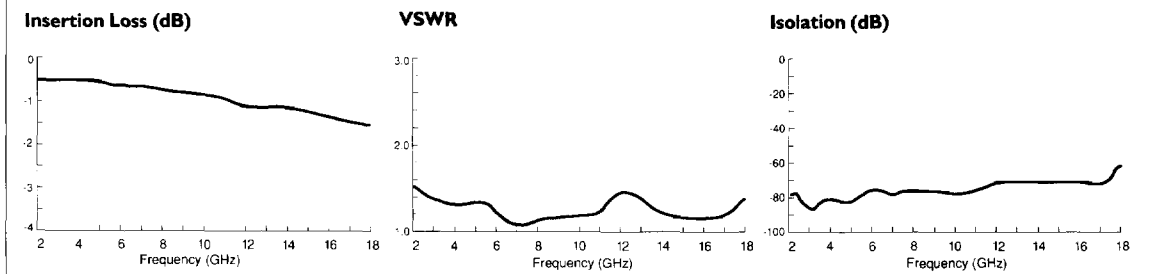


Maximum Ratings

Storage Temp. -65°C to +125°C

Operating Temp. -55°C to +95°C

Typical Performance Data 2662-1008-XY



Specifications Subject to Change Without Notice.

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Specifications 25°C

Frequency Range (GHz)	Insertion Loss (dB)	VSWR	Isolation (dB)	Transition Time (nS)	Switching Speed (nS)	Operating Power (W)	Part Number ⁹
0.5-2.0	0.7	1.40:1	40	5	15	0.02	2662-1001-XY
2-8	1.0	1.50:1	40	5	15	0.02	2662-1003-XY
	1.2	1.50:1	60	5	15	0.02	2662-1004-XY
6-18	2.0	1.90:1	40	5	15	0.02	2662-1005-XY
	2.2	1.90:1	55	5	15	0.02	2662-1006-XY
2-18	2.5	2.00:1	55	5	15	0.02	2662-1008-XY

Notes:

1. Driver current req. ± 90 mA typ.
2. Logic "1" for low loss.
3. Transition Time measured from 10% to 90% of detected RF.
4. Switch Speed measured from 50% TTL to 10%/90% of detected RF.
5. Nominal ECL levels are:
"1" -0.8V
"0" -1.8V
6. Minimum pulse width 25 nS.
7. Maximum PRF 20 MHz.
8. Logic input impedance: 75 ohms.
9. Specify voltage and logic connector from option table.

- XY Option Table

	X Bias Voltage	Y Logic Conn.
0	+12V/-5.2V	0 N/A
1	+5V/-5.2V	1 SMC Conn.
2	+15V/-5.2V	2 SMA Conn.

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