

GFB2000A

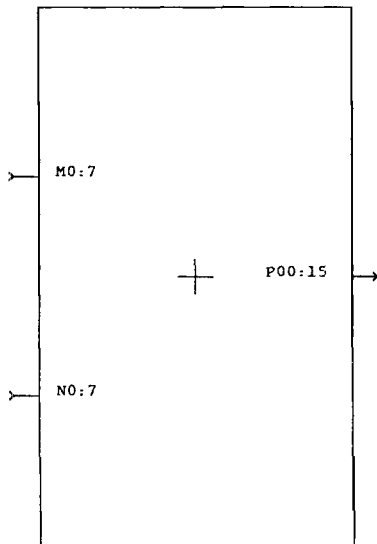
8X8 2'S COMPLEMENT MULTIPLIER

GENERAL DESCRIPTION:

THE GFB2000A MEGAFUNCTION IS AN 8-BY-8 2'S COMPLEMENT MULTIPLIER WHICH GENERATES A 16-BIT PRODUCT. BY USING A MODIFIED BOOTH ALGORITHM, THIS MEGAFUNCTION GIVES A REASONABLE SPEED AND GATE COUNT.

PIN DIAGRAM:

GFB2000A



- GATES USED - 517
- AREA USED - 673 GATE LOCATIONS
- LL7000 SERIES COMPATIBLE
- LSA2000 SERIES COMPATIBLE

INPUT LOADING: (LOADING IN TRANSISTOR PAIRS)

M0:7 - 2                      N0:7 - 2

OUTPUT DRIVE: (DRIVE IN (#P,#N))

P00:15 - (2,2)

NDL SYNTAX:

Z(P00,P01,P02,P03,P04,P05,P05,P07,P08,P09,P010,P011,P012,P013,P014,P015)

-GFB2000A (M0,M1,M2,M3,M4,M5,M6,M7,N0,N1,N2,N3,N4,N5,N6,N7)\$

## AC CHARACTERISTICS\*:

FROM	TO	5K TYP. DELAY (NS)	7K TYP. DELAY (NS)
ANY M	ANY P	64	39
ANY N	ANY P	64	39

\*ASSUMING OUTPUT LOADING OF 3