

MOTOROLA SEMICONDUCTOR TECHNICAL DATA

Advance Information High Performance Decoder/ Sink Driver

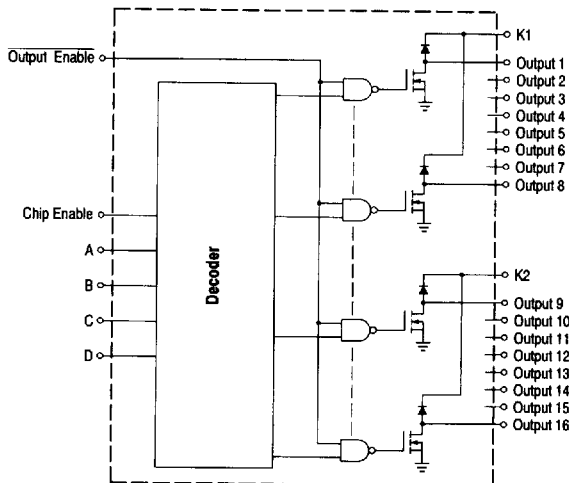
The MC34142 is a high performance 4 to 16 multiplexed driver. This integrated circuit features a 4 to 16 decoder, 16 open drain N-channel MOS output devices with clamp diodes. The outputs are controlled by 4 address inputs, an output enable, and a chip enable.

Typical applications include solenoid drivers, LED drivers, lamp drivers, and relay drivers.

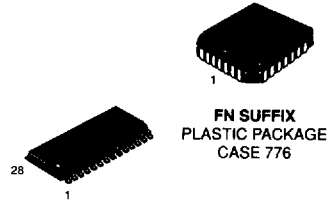
This device is offered in a PLCC and a wide body surface mount package.

- SMARTMOS™ Technology
- 35 V Maximum Output Off-State Voltage
- 500 mA Maximum Output Sink Current
- Regulated Output Saturation Voltage
- 16 Open Drain MOS Outputs
- 4 Input CMOS Decoder
- Chip Select and Output Enable Input Pins
- Internal Freewheel Diodes
- Functional Equivalent to the UCN5816A

Simplified Block Diagram



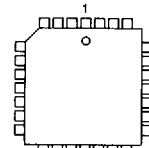
MC34142



FN SUFFIX
PLASTIC PACKAGE
CASE 776

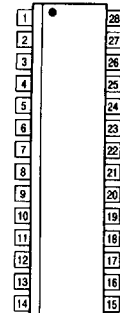
DW SUFFIX
PLASTIC PACKAGE
CASE 751F
(SO-28L)

PIN CONNECTIONS



(Top View)

| Pin | Function | Pin | Function | Pin | Function |
|-----|-----------------|-----|-----------|-----|-----------|
| 1 | Chip Enable | 11 | Output 6 | 20 | Output 13 |
| 2 | N.C. | 12 | Output 7 | 21 | Output 14 |
| 3 | V _{CC} | 13 | Output 8 | 22 | Output 15 |
| 4 | Output Enable | 14 | Ground | 23 | Output 16 |
| 5 | K1 | 15 | Ground | 24 | K2 |
| 6 | Output 1 | 16 | Output 9 | 25 | Input A |
| 7 | Output 2 | 17 | Output 10 | 26 | Input B |
| 8 | Output 3 | 18 | Output 11 | 27 | Input C |
| 9 | Output 4 | 19 | Output 12 | 28 | Input D |
| 10 | Output 5 | | | | |



(Top View)

ORDERING INFORMATION

| Device | Temperature Range | Package |
|-----------|-------------------|---------|
| MC34142FN | 0° to +70°C | PLCC |
| MC34142DW | | SO-28L |

MC34142

MAXIMUM RATING

| Rating | Symbol | Value | Unit |
|---|------------------|-------------|------|
| Power Supply Voltage | V _{CC} | 7.0 | V |
| Output Voltage | V _O | 35 | V |
| Drive Output Sink Current (Note 1) | I _O | | mA |
| Continuous | | 500 | |
| Pulsed (10 μs) | | 1000 | |
| Power Dissipation and Thermal Characteristics FN Suffix, Plastic Package, Case 776 | | | |
| T _A = 25°C | P _D | 1.9 | W |
| Thermal Resistance, Junction to Air | R _{θJA} | 66 | °C/W |
| DW Suffix, Plastic Package, Case 751F | | | |
| T _A = 25°C | P _D | 1.5 | W |
| Thermal Resistance, Junction to Air | R _{θJA} | 80 | °C/W |
| Operating Junction Temperature | T _J | +150 | °C |
| Operating Ambient Temperature MC34142 | T _A | 0 to +70 | °C |
| Storage Temperature | T _{stg} | -55 to +150 | °C |

ELECTRICAL CHARACTERISTICS (V_{CC} = 5.0 V, for typical values T_A = 25°C, for min/max values T_A = 0° to +70°C.)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

POWER SUPPLY SECTION

| | | | | | |
|----------------------|-----------------|------|-----|------|----|
| Logic Supply Voltage | V _{CC} | 4.75 | 5.0 | 5.25 | V |
| Supply Current | I _{CC} | — | — | 0.5 | mA |
| Outputs Off | | — | — | 4.0 | |
| Outputs On | | — | — | — | |

LOGIC INPUT SECTION

| | | | | | |
|--|-----------------|-----|---|-----|----|
| Input Threshold Voltage — High State Logic 1 | V _{IH} | 2.2 | — | — | V |
| — Low State Logic 0 | V _{IL} | — | — | 0.8 | |
| Input Current (V _{in} = 5.0 V) | I _{IN} | — | — | 20 | μA |

OUTPUT SECTION

| | | | | | |
|---|-------------------|-----|---|-----|----|
| Output Saturation Voltage | V _{Sat} | | | | V |
| I _{Sink} = 100 mA | | 1.1 | — | 1.3 | |
| I _{Sink} = 400 mA | | 1.2 | — | 1.4 | |
| Output Leakage Current (V _O = 35) | I _{Leak} | — | — | 100 | μA |
| Clamp Diode Leakage Current (V _R = 35 V) | I _R | — | — | 100 | μA |
| Clamp Diode Forward Voltage | V _F | | | | V |
| I _{forward} = 100 mA | | 0.8 | — | 1.2 | |
| I _{forward} = 400 mA | | 1.1 | — | 1.6 | |

SWITCHING CHARACTERISTICS (T_A = 25°C)

| | | | | | |
|-----------------------------------|------------------|----|----|-----|----|
| Output Rise Time | t _r | — | 40 | — | ns |
| Output Fall Time | t _f | — | 40 | — | ns |
| Propagation Delay Time | | | | | ns |
| Output Enable Low to Output Low | t _{pll} | 50 | — | 150 | |
| Output Enable High to Output High | t _{phh} | 50 | — | 150 | |
| Setup Time, Data to Output Enable | t _{su} | — | 40 | — | ns |
| Hold Time, Output Enable to Data | t _h | — | 40 | — | ns |

MC34142

APPLICATION CIRCUIT INFORMATION

The MC34142 is a high performance 4 x 16 multiplexed driver. This integrated circuit features a 4 x 16 decoder, 16 open-drain output devices with clamp diodes, an output enable, and a chip enable. Typical applications include solenoid drivers, LED drivers, lamp drivers, and relay drivers.

The inputs to this device are TTL/CMOS compatible, making them ideal to be driven from a microcomputer. Table 1 is a truth table for the input logic versus the appropriate activated output. Notice, for a specific input, only one output can be activated.

The outputs on the MC34142 are open drain DMOS power MOSFETs. Each output is capable of sinking in excess of

500 mA. The outputs have been uniquely designed to control the "on-resistance" of the power MOSFET. The voltage drop across the MOSFET is regulated and temperature compensated to give a consistent saturation voltage characteristic over load and temperature. Figure 2 shows a curve of the Output Saturation Voltage versus Sink Current.

Each output also has a flyback diode clamp to protect the device from inductive load kickbacks. Special care should be taken when laying out the printed circuit board to use these clamp diodes effectively. A capacitor should be placed close to the K1 and K2 clamp outputs.

Figure 1. Typical Application

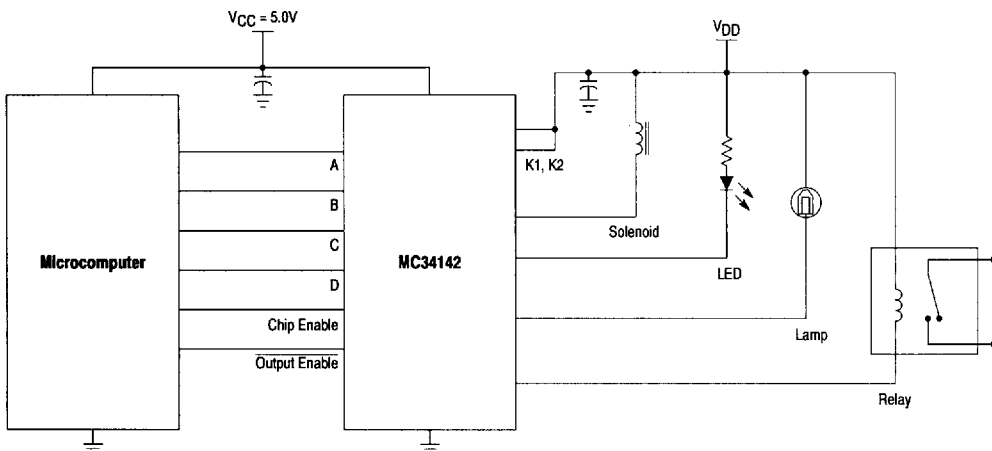


Figure 2. Output Saturation Voltage versus Load Current

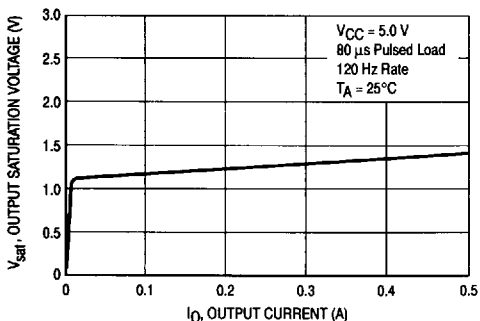
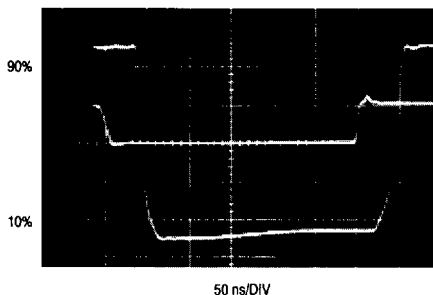
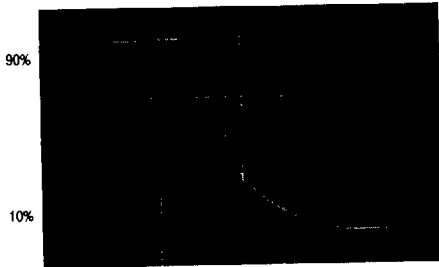


Figure 3. Propagation Delay



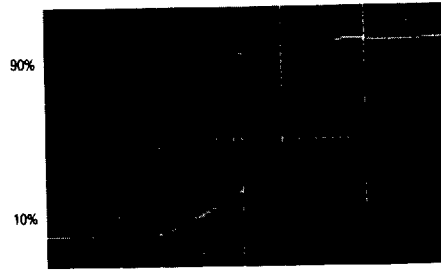
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Figure 4. Output "Turn-On" Time



10 ns/DIV

Figure 5. Output "Turn-Off" Time



10 ns/DIV

Table 1. Truth Table

| Output Enable | Chip Enable | Data Inputs | | | | Selected Output Active Low |
|---------------|-------------|-------------|---|---|---|----------------------------|
| | | D | C | B | A | |
| 0 | 1 | 0 | 0 | 0 | 0 | Output 1 |
| 0 | 1 | 0 | 0 | 0 | 1 | Output 2 |
| 0 | 1 | 0 | 0 | 1 | 0 | Output 3 |
| 0 | 1 | 0 | 0 | 1 | 1 | Output 4 |
| 0 | 1 | 0 | 1 | 0 | 0 | Output 5 |
| 0 | 1 | 0 | 1 | 0 | 1 | Output 6 |
| 0 | 1 | 0 | 1 | 1 | 0 | Output 7 |
| 0 | 1 | 0 | 1 | 1 | 1 | Output 8 |
| 0 | 1 | 1 | 0 | 0 | 0 | Output 9 |
| 0 | 1 | 1 | 0 | 0 | 1 | Output 10 |
| 0 | 1 | 1 | 0 | 1 | 0 | Output 11 |
| 0 | 1 | 1 | 0 | 1 | 1 | Output 12 |
| 0 | 1 | 1 | 1 | 0 | 0 | Output 13 |
| 0 | 1 | 1 | 1 | 0 | 1 | Output 14 |
| 0 | 1 | 1 | 1 | 1 | 0 | Output 15 |
| 0 | 1 | 1 | 1 | 1 | 1 | Output 16 |
| x | 0 | x | x | x | x | All Outputs High |
| 1 | x | x | x | x | x | |

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