SEMI CONDUCTOR

### **Ultra Low Capacitance ESD Protection**

#### Voltage

ΡΛΝ

5 V

#### Features

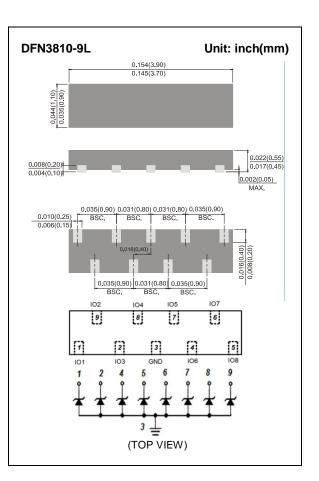
- IEC61000-4-2(ESD) : ±20kV Air, ±15kV Contact
- IEC61000-4-4(EFT) : 40A(5/50ns)
- IEC61000-4-5(Lightning) : 4A(8/20µS)
- Low leakage current, maximum of 50nA at rated voltage
- Ultra low capacitance
- Low clamping voltage
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std.. (Halogen Free)

#### **Mechanical Data**

• Case: DFN3810-9L, Plastic

#### Applications

- USB Type-C Interface
- HDMI Interface 2.0 version
- V-By-One Interface
- LVDS Interface
- Display Port Interface



#### **Maximum Ratings**

PARAMETER	SYMBOL	VALUE	UNITS	
ESD IEC61000-4-2(Air)	M	±20	kV	
ESD IEC61000-4-2(Contact)	$V_{ESD}$	±15		
Operating Junction Temperature Range	ΤJ	-55 to +150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C	



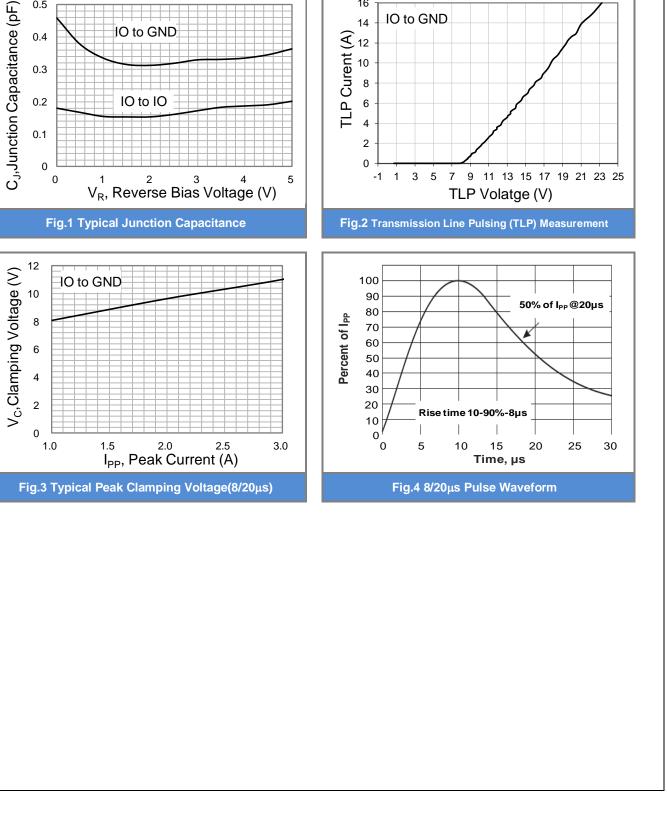
#### **Electrical Characteristics**

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage (Note 1)	V <sub>RWM</sub>	-	-	-	5	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	5.5	-	-	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> =5.0V	-	-	50	nA
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs, any I/O pins to GND	-	-	10	V
		I <sub>PP</sub> =4A, t <sub>P</sub> =8/20μs, any I/O pins to GND	-	-	15	V
Clamping Voltage TLP (Note 2)	V <sub>CL</sub>	I <sub>PP</sub> =8A, t <sub>P</sub> =100ns, any I/O pins to GND	-	16	-	V
		I <sub>PP</sub> =16A, t <sub>P</sub> =100ns, any I/O pins to GND	-	23.5	-	V
Dynamic Resistance	R <sub>DYN</sub>	t <sub>P</sub> =100ns	-	0.94	-	Ω
Off State Junction Capacitance	CJ	2.5Vdc Bias f=1MHz, any I/O pins to GND	-	0.3	0.35	pF
		2.5Vdc Bias f=1MHz, Between any I/O pins	-	0.2	-	pF

Note :

- 1. A transient suppressor is selected according to the working peak reverse voltage( $V_{RWM}$ ), which should be equal to or greater than the DC or continuous peak operation voltage level.
- 2. Testing using Transmission Line Pulse (TLP) conditions:  $Z0 = 50\Omega$ ,  $t_P = 100$  ns.

September	8,2016-REV.00
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TLP Curent (A)

2

IO to GND

# **PE1605S8Q**

**TYPICAL CHARACTERISTIC CURVES** 

IO to GND

IO to IO

0.5

0.4

0.3

0.2

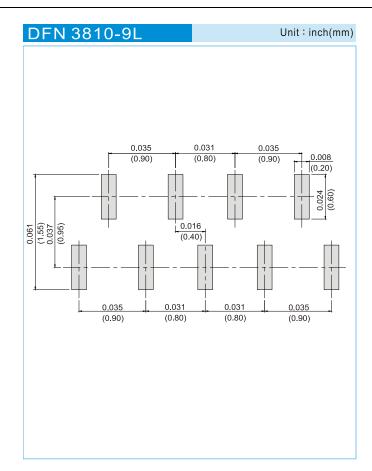
0.1



#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PE1605S8Q_R1_00001	DFN3810-9L	3K pcs / 7" reel	1605	Halogen free

#### **Mounting Pad Layout**



Notes: This pad layout is for reference purposes only.



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