

LE507 DATA SHEET

FEATURES

- 76 dB of gain
- internal DC bias for I/P stage
- applications include AF output stages, hearing aids and RF amplifiers
- total harmonic distortion 1% (typical)

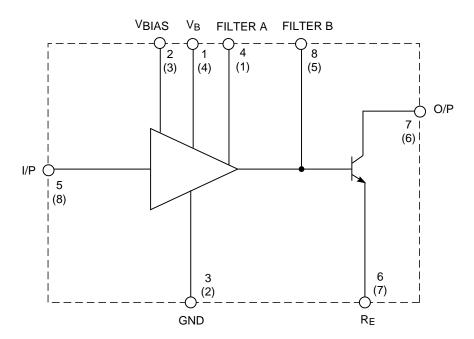
STANDARD PACKAGING

- 8 pin MICROpac
- 8 pin PLID ®
- 8 pin DIP
- Chip (32 x 34 mils)

DESCRIPTION

The LE507 is a low voltage class A amplifier design, primarily for low voltage and low power use. The broad application include AF output stages, RF amplifiers and output stages of the hearing instruments. The circuit is comprised of three stages all of them having access to their collectors for frequency shaping, feedback, etc. The provision for the bias network and alternative gain increase to 76 dB are included on the circuit.

For more application information refer to Information Note 520 - 30.



All external resistors in ohms, all capacitors in μ F unless otherwise stated. Numbers in brackets are for DIP package.

BLOCK DIAGRAM

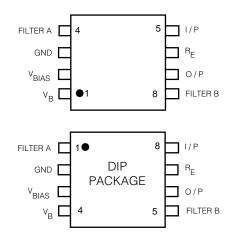
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ABSOLUTE MAXIMUM RATINGS

PARAMETER	VALUE/UNITS			
Supply Voltage	5 V DC			
Power Dissipation	25 mW			
Operating Temperature Range	-10°C to40° C			
Storage Temperature Range	-20°C to 70° C			
CAUTION CLASS 1 ESD SENSITIVITY	R			

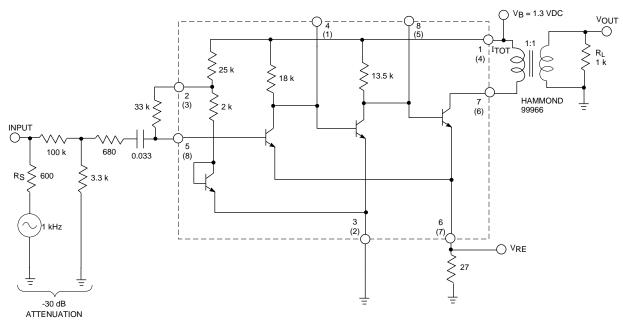
PIN CONNECTION



ELECTRICAL CHARACTERISTICS

Supply voltage 1.3 VDC, ambient temperature 25^oC Conditions are as per test circuit unless otherwise stated

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Voltage Gain	V _A	Output level 0.5 VRMS	28	30	32	dB
Total Harmonic Distortion	T.H.D.	Output level 0.5 VRMS	-	1	2	%
Total Current	I _{Total}		1.05	1.3	1.6	mA
Emitter Voltage	V _{RE}		27	34	41	mV



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Fig.1 Test Circuit

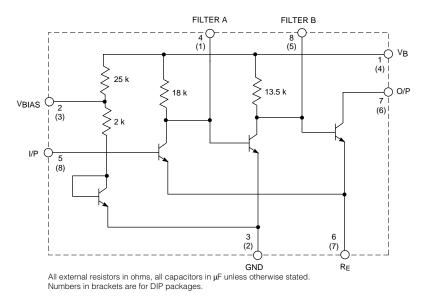
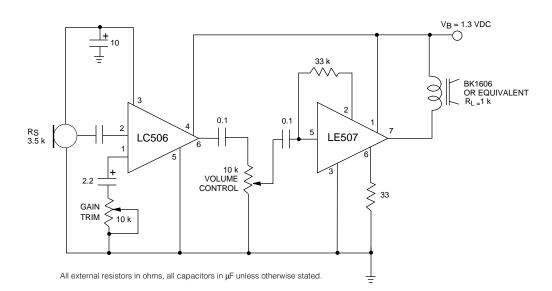
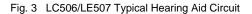


Fig. 2 Functional Schematic





DOCUMENT IDENTIFICATION

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DATA SHEET

The product is in production. Gennum reserves the right to make changes at any time to improve reliability, function or design, in order to provide the best product possible.

REVISION NOTES

DIP Package added.

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