

AN7147N

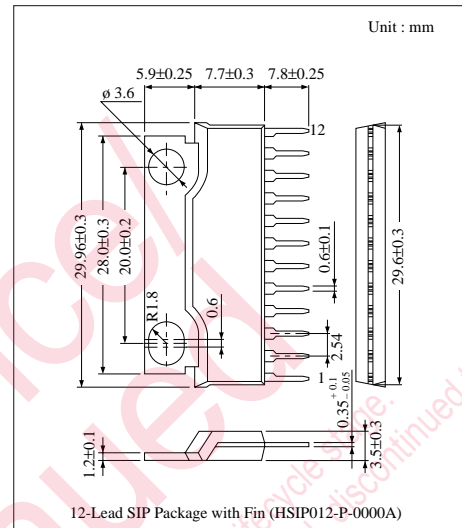
Dual 5.3W Audio Power Amplifier Circuit

■ Overview

The AN7147N is an integrated circuit designed for power amplifier of 5.3W (12.0V, 3Ω) output. Stereo operation is enabled due to incorporating two amps. on one chip. It makes high reliability by smaller quiescent current compared with power amp. current and protectors built in. As quiescent circuit current is very small, it is most suitably used for battery operated set such as radio cassette recorder. Furthermore, low distortion and low noise are realized and external components are very few. 12-pin SIL package enabled compact and high integrated set.

■ Features

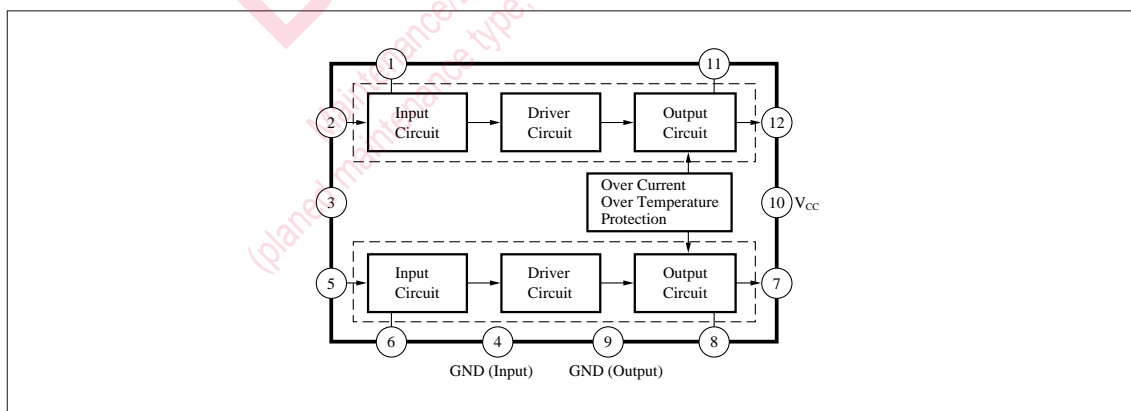
- Low quiescent current
- High operation stability
- Low radiation
- Low distortion
- Low noise
- Low shock noise from power ON/OFF operation
- Fewer external componetns



■ Pin Descriptions

Pin No.	Pin Name	Pin No.	Pin Name
1	N.F.B Ch.1	7	Output Ch.2
2	Input Ch.1	8	Bootstrap Ch.2
3	Ripple Filter	9	GND (Output)
4	GND (Input)	10	V _{CC}
5	Input Ch.2	11	Bootstrap Ch.1
6	N.F.B Ch.2	12	Output Ch.1

■ Block Diagram



■ Absolute Maximum Ratings (Ta= 25°C)

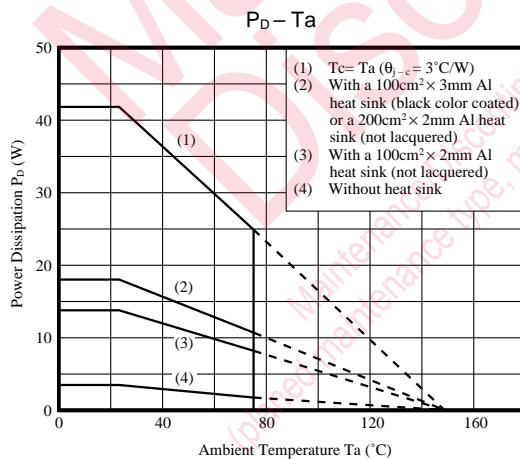
Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	24	V
Supply Current	I _{CC}	4	A
Power Dissipation *	P _D	41.7	W
Operating Ambient Temperature	T _{opr}	- 30 ~ + 75	°C
Storage Temperature	T _{stg}	- 55 ~ + 150	°C

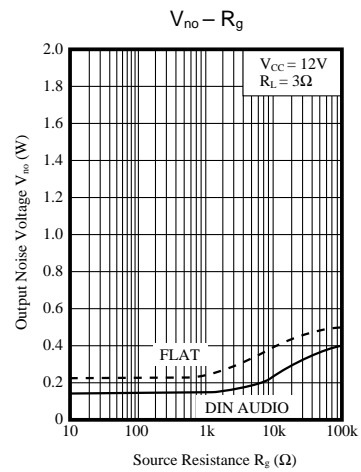
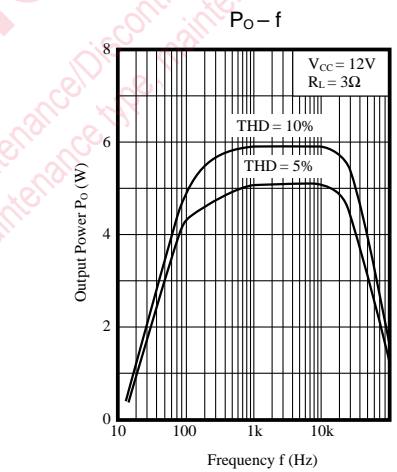
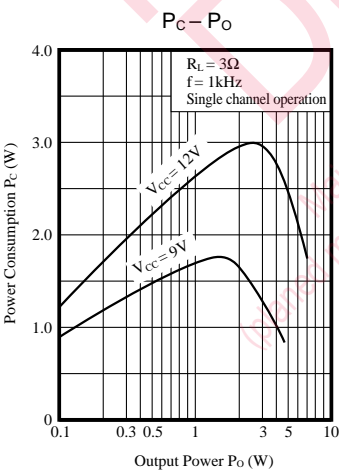
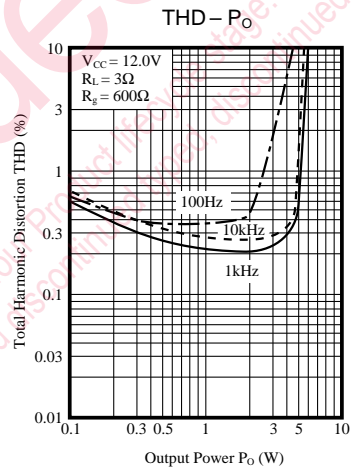
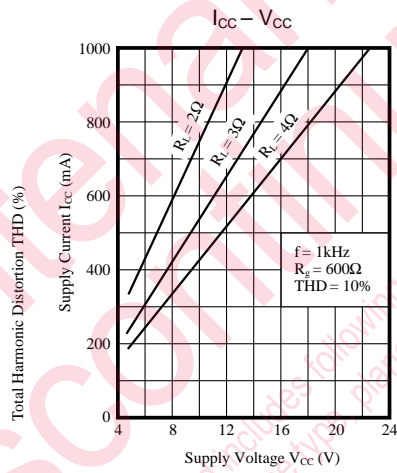
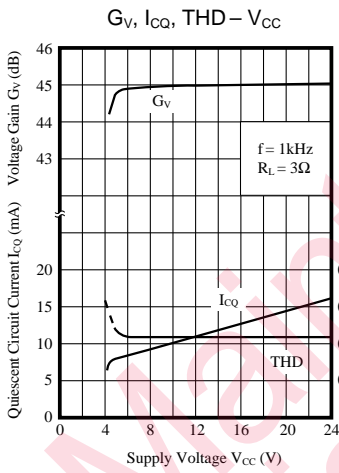
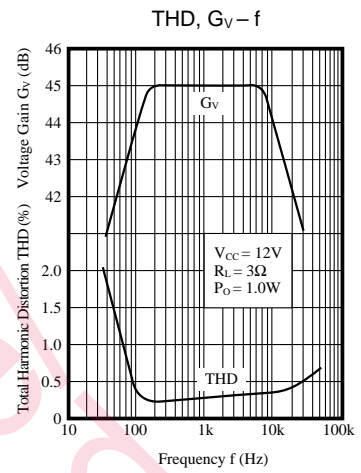
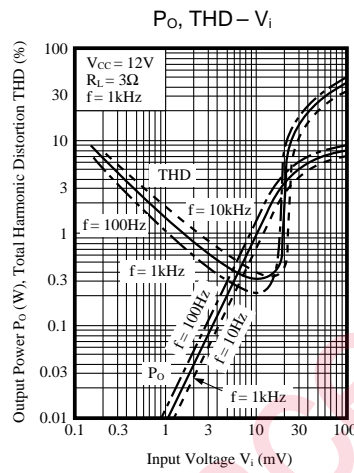
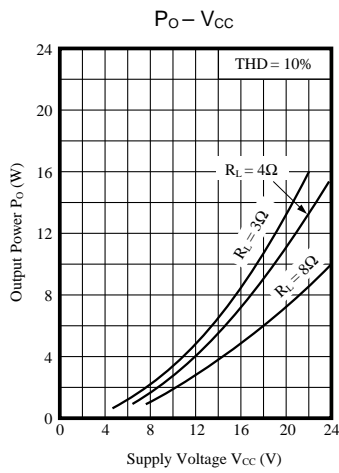
* R_{θj-c} = 3°C/W

■ Electrical Characteristics (V_{CC} = 12V, R_C = 3Ω, f = 1kHz, Ta = 25°C)

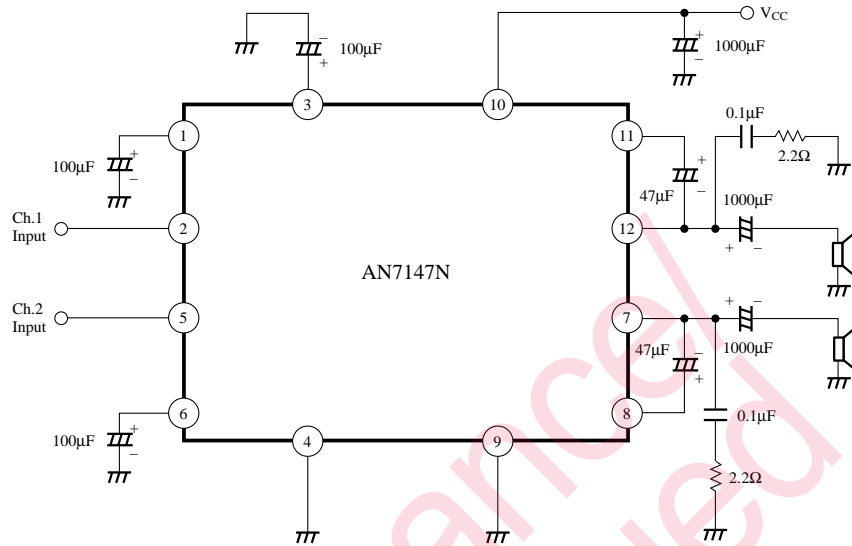
Parameter	Symbol	Condition	min.	typ.	max.	Unit
Quiescent Circuit Current	I _{CQ}	V _i = 0mV	—	13	19	mA
Voltage Gain	G _V	P _O = 1.0W	42.5	44.5	46.5	dB
Total Harmonic Distortion	THD	P _O = 1.0W, f = 1kHz	—	0.2	0.75	%
		P _O = 1.0W, f = 100Hz	—	0.4	—	
		P _O = 1.0W, f = 10kHz	—	0.3	—	
Maximum Output Power	P _O	THD = 10%, R _L = 3Ω	4.7	5.3	—	W
		THD = 10%, R _L = 4Ω	—	4.3	—	
		THD = 10%, V _{CC} = 9V, R _L = 3Ω	—	2.9	—	
Output Noise Voltage	V _{no}	R _g = 10kΩ, f = 15Hz ~ 30kHz, 12dB/OCT	—	0.25	—	mV
		R _g = 10kΩ, Without Filter	—	0.40	0.5	
Channel Balance	CB	P _O = 0.5W	—	0	1	dB
Channel Separation	CS	P _O = 0.5W	40	50	—	dB
Ripple Rejection Ratio	RR	P _O = 0.5W	45	50	—	dB
Output Offset Voltage	V _{O(offset)}	V _i = 0	—	0	200	mV

Note) Operating Supply Voltage Range : 5V to 22V

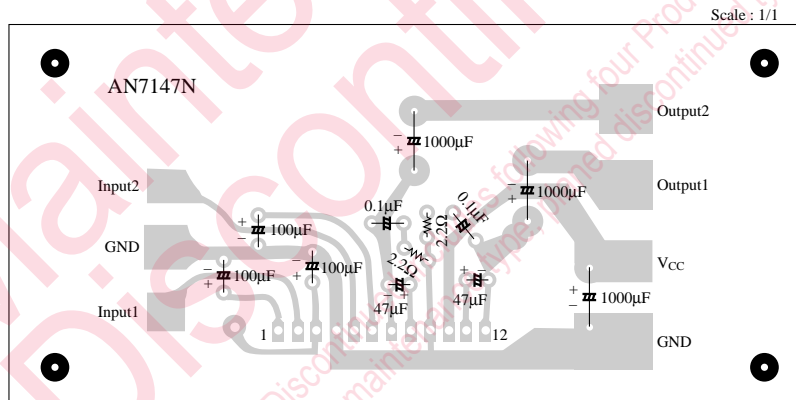




■ Application Circuit



■ Printed Circuit Board Layout



Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products, and no license is granted under any intellectual property right or other right owned by our company or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
 - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
 - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
 - Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of Matsushita Electric Industrial Co., Ltd.