

JEIDA Ver. 4 LOW POWER STATIC RAM

VARIATION

Part Number	Memory Size	Description
LWB065SD*0	64K Bytes	32K × 16 bits MIX CMOS LOW POWER SRAM CARD
LWB128SD*0	128K Bytes	64K × 16 bits MIX CMOS LOW POWER SRAM CARD
LWB257SD*0	256K Bytes	128K × 16 bits MIX CMOS LOW POWER SRAM CARD
LWB513SD*0	512K Bytes	256K × 16 bits MIX CMOS LOW POWER SRAM CARD
LWB101SD*0	1M Bytes	512K × 16 bits MIX CMOS LOW POWER SRAM CARD

Note: * : Attribute Memory Type
 2 : 2K Bytes EEPROM (can be read/written)
 3 : 16 Bytes battery back-up memory (can be read/written)
 4 : No attribute memory (output "FF") (read only)

OUTLINE OF FUNCTIONS AND FEATURES

- (1) This memory card conforms to JEIDA Ver. 4.
- (2) Size of the card
 - Width : 54.0 mm
 - Length : 85.6 mm
 - Thickness : 3.3 mm
- (3) Includes exclusive IC's for the control of I/O and power functions.
- (4) Two digital outputs (BVD1, BVD2) for the condition of the battery.
- (5) A write protect switch for protection from accidental overwriting.
- (6) Locking mechanism for battery holder.
- (7) Replaceable battery. (Replace only when power is supplied.)
- (8) Support 3 type attribute memory.
 - 8-1) With 2K Bytes EEPROM which can be read/written.
 - 8-2) With 16 Bytes battery back-up memory which can be read/written.
 - 8-3) No attribute memory which can be read only. (output "FF")
- (9) The standby current of LWB series is lower than AWB series's.
- (10) The active current of LWB series is lower than AWB series's at 8 bit data access.
- (11) Card Type: 68 pin Two-piece Type.