

**4. TELECOMMUNICATION APPLICATION** (Continued)

Application	Device	Package	Circuit Function
1 Chip CLP	†† KA8510 †† KA8511 †† KA8522 †† KA8523	64 DIP/64 QFP 64 DIP/64 QFP	Included dual conversion receiver, transmitter, compandor and PLL Excellent input sensitivity Included ALC circuit Mute function Unlock detector Included VCO circuit KA8510/22: 15CH(Parallel/Serial), KA8511/23: 10CH(Parallel/Serial)
PLL	† KS8803B † KS8804B	16 DIP/16 SOP 16 DIP/16 SOP	10/15 Channel Selectable(Both Tx/Rx) Included Oscillation Circuit with external X-tal(10.24MHz) 5KHz Output for guard tone Stand-by function for power saving KS8803B: 10CH, KS8804B: 15CH
	†† KS8810	16 DIP/16 SOP	Low Supply Current(8mA) High operating frequency(1.1 GHZ) Pulse Sallow function: 64/65 or 128/129
	†† KS8815	16 DIP/16 SOP	Low Supply Current(10mA) High operating frequency(520 MHz) Pulse sallow function: 32/33 or 64/65
1 Chip Codec	KT8554 KT8557	16 CERDIP † 16 DIP	Exceeds all D3/D4 and CCITT spec. Complete CODEC and filtering system including ±5V operation Low power consumption TTL and CMOS compatible logic
TSAC	KT8555	20 CERDIP	Controls up to eight 1 CHIP CODEC/Filters Low power consumption Single 5V operation Up to 32 time slots per frame
4 × 4 Cosspoint Switch	KT8592	16 DIP	4 × 4 matrix-array with control memory Low on resistance 2VPP analog signal capability Less than 1% total distortion at 0dBm
12 × 8 Cosspoint Switch	KT8593	40 DIP	12 × 8 matrix-array with control memory Low on resistance 2VPP analog signal capability Less than 1% total distortion at 0dBm
TDX-CODEC	†† KT8580	28 PLCC	Complete CODEC and filtering system including Programmable function controlled via serial port On-chip time slot assignment circuit Ringing signal synchronization
Analog Line Card Controller	† KT8584	32 PLCC	Peripheral Processor interface for TDX GSLA SLIC off-hook detection data read Control data read & Write for TDX-CODEC MCLK & DCLK alarm function including

† New Product    †† Under Development