

Multiple Digital Delay Units

SERIES: MDU-13
(16 pins DIP) 10KH ECL Interfaced



Features:

- Input & Output Buffered.
- 3 Delay lines/package.
- Fits in Standard 16 Pins DIP.

Specifications:

- Total Delay Tolerance: $\pm 5\%$ or better, or 2 ns whichever is greater.
- Rise-time: 2 ns typical.
- Supply voltage: $-5.2V$
- Operating Temperature: $-30^{\circ}C$ to $85^{\circ}C$.
- Power Dissipation: ~ 200 mw typ. (no load).
- Temperature coefficient: 100 PPM/ $^{\circ}C$.
- Fan-out: 70 ECL loads.
- DC parameters: See ECL-10KH Logic Table on Page 6.

Test Conditions:

- Input pulse-width: 150% of total delay.
- Input pulse rise-time: ≤ 6 ns.
- Input pulse voltage: $\sim .7V$
- Rise-time measured from 20% to 80% of leading edge.
- Delay time measured at 50% of leading edge.
- All measurements taken ($V_{EE} = -5.2V$ and $T_A = 25^{\circ}C$).

Part No.	Delay Per Line (ns)
MDU-13-5	5
MDU-13-10	10
MDU-13-15	15
MDU-13-20	20
MDU-13-25	25
MDU-13-30	30
MDU-13-35	35
MDU-13-40	40
MDU-13-45	45
MDU-13-50	50

Other delay times on request.

