

M66800SP/FP

DESCRIPTION

The M66800SP/FP is a semiconductor integrated circuit containing memory controller for Mitsubishi single chip 16-bit microcomputer MELPS7700 series (M33700S/SA etc.), which is supported 16M-byte full address access area.

M66800P/FP is able to operate fast, since it is fabricated with MAST (Mitsubishi Advanced Schottkey TTL) technology.

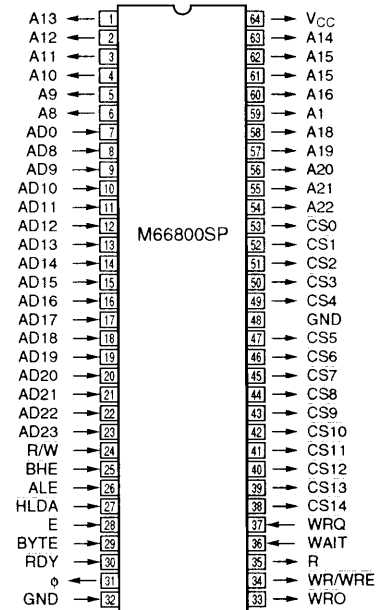
FEATURES

- Have M37700S/SA operate No wait, as below conditions
 - At 13MHz, using 100ns Max Address access time memories
 - At 12MHz, using 120ns Max Address access time memories
- Memory read and write control circuit with independent read and write terminals
- 16 Address data latches
- Chip select circuit, which is decode for full 16Mbyte
- Ready signal generator

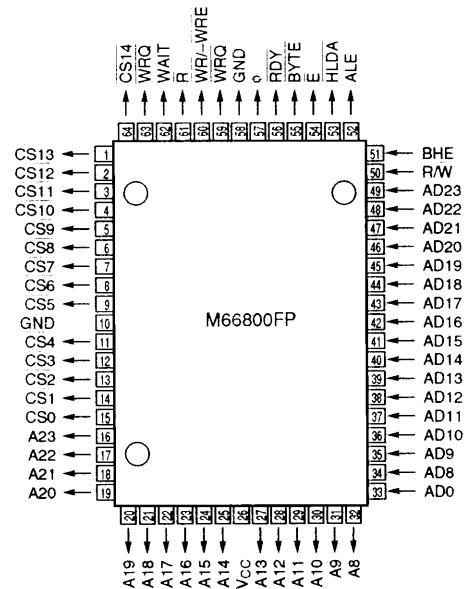
APPLICATION

Printer, office automation equipment, musical instruments, use of wide external memory area for MELPS-7700 series

PIN CONFIGURATION (TOP VIEW)



OUTLINE: 64P4P



OUTLINE: 64P6W