

**flow PFC 0**

500 V/ 2 x 14 A/ 200 kHz

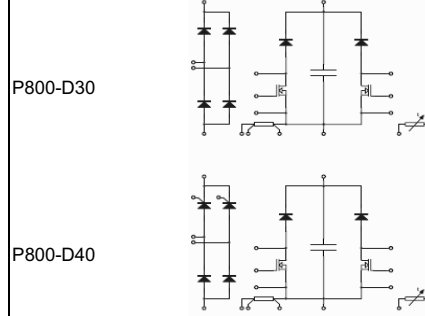
**Features**

- Clip-in housing
- Compact and low inductance design

**flow0 housing**

**Target Applications**

- PFC for welding
- PFC for SMPS
- PFC for motor drives
- PFC for UPS

**Schematic**

**Types**

- V23990-P800-D30-PM without Thyristor
- V23990-P800-D40-PM with Thyristor

## Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
<b>Input Rectifier Diode</b>				
Repetitive peak reverse voltage	$V_{RRM}$		1600	V
Forward current per diode	$I_F$	DC current $T_j = T_{jmax}$ $T_h = 80^\circ C$ $T_c = 80^\circ C$	28	A
Surge forward current	$I_{FSM}$	$t_p = 10ms$ $T_j = 25^\circ C$	200	A
$I^2t$ -value	$I^2t$		200	A <sup>2</sup> s
Power dissipation per Diode	$P_{tot}$	$T_j = T_{jmax}$ $T_h = 80^\circ C$ $T_c = 80^\circ C$	33	W
Maximum junction temperature	$T_{jmax}$		150	°C
<b>Input Rectifier Thyristor</b>				
Repetitive peak reverse voltage	$V_{RRM}$		1600	V
Forward current per Thyristor	$I_F$	DC current $T_j = T_{jmax}$ $T_h = 80^\circ C$ $T_c = 80^\circ C$	35	A
Surge forward current	$I_{FSM}$	$t_p = 10ms$ $T_j = 25^\circ C$	250	A
$I^2t$ -value	$I^2t$		310	A <sup>2</sup> s
Power dissipation per Thyristor	$P_{tot}$	$T_j = T_{jmax}$ $T_h = 80^\circ C$ $T_c = 80^\circ C$	45	W
Maximum junction temperature	$T_{jmax}$		150	°C

## Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
<b>PFC Transistor (MOSFET)-per leg</b>				
Drain to source voltage	$V_{DS}$		500	V
DC drain current	$I_d$	$T_j=T_{jmax}$ $T_h=80^{\circ}C$ $T_c=80^{\circ}C$	16	A
Pulsed drain current	$I_{dpuls}$	$T_j=150^{\circ}C$ $t_p$ limited by $T_{jmax}$ $T_h=80^{\circ}C$ $T_c=80^{\circ}C$	96	A
Avalanche energy, single pulse	$E_{AS}$	$I_D=10 A$ $V_{DD}=50 V$	1100	mJ
Avalanche energy, repetitive	$E_{AR}$	$I_D=20A$ $V_{DD}=50V$ $t_{AR}$ limited by $T_{jmax}$	1	mJ
Avalanche current, repetitive	$I_{AR}$	$t_p$ limited by $T_{jmax}$	20	A
Drain source voltage slope	dv/dt	$I_D=32A$ $V_{DS}=400V$ $T_j=125^{\circ}C$	50	V/ns
Reverse diode dv/dt	dv/dt	$I_D=32A$ $V_{DS}=400V$ di/dt=100A/ $\mu s$ $T_j=150^{\circ}C$	6	kV/ $\mu s$
Power dissipation	$P_{tot}$	$T_j=T_{jmax}$ $T_h=80^{\circ}C$ $T_c=80^{\circ}C$	63	W
Gate-source peak voltage	$V_{GS}$		20	V
Maximum junction temperature	$T_{jmax}$		150	$^{\circ}C$

### PFC Diode-per leg

Peak repetitive reverse voltage	$V_{RRM}$		600	V
DC forward current	$I_F$	$T_j=T_{jmax}$ $T_h=80^{\circ}C$ $T_c=80^{\circ}C$	20	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_{jmax}$ $T_h=80^{\circ}C$	64	A
Power dissipation	$P_{tot}$	$T_j=T_{jmax}$ $T_h=80^{\circ}C$ $T_c=80^{\circ}C$	47	W
Maximum junction temperature	$T_{jmax}$		175	$^{\circ}C$

### PFC Shunt

DC forward current	$I_F$	$T_k=170^{\circ}C$ $T_k$ =Terminal temperature $T_h=80^{\circ}C$	40	A
Power dissipation per Shunt	$P_{tot}$	$T_k=170^{\circ}C$ $T_k$ =Terminal temperature $T_h=80^{\circ}C$	17	W

### Capacitor

Max.DC voltage	$V_{MAX}$		500	V
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### Thermal properties

Storage temperature	$T_{stg}$		-40...+125	$^{\circ}C$
Operation temperature	$T_{op}$		-40...+125	$^{\circ}C$

### Insulation properties

Insulation voltage	$V_{is}$	$t=1min$	4000	Vdc
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

**Characteristic Values**

Parameter	Symbol	Conditions					Value			Unit			
		$V_{GE}(V)$ or $V_{GS}(V)$	$V_C(V)$ or $V_{CE}(V)$ or $V_{DS}(V)$	$I_C(A)$ or $I_F(A)$ or $I_D(A)$	$T(^{\circ}C)$	Min	Typ	Max					
<b>Input Rectifier Bridge</b>													
Forward voltage	$V_F$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		1,21 1,18	1,5	V			
Threshold voltage (for power loss calc. only)	$V_{to}$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,93 0,79		V			
Slope resistance (for power loss calc. only)	$r_t$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,009 0,013		Ohm			
Reverse leakage current	$I_r$			1500		$T_J=25^{\circ}C$ $T_J=150^{\circ}C$			0,01 1	mA			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness $\leq$ 50um $\lambda = 0,61$ W/mK						2,14		K/W			
<b>Input Rectifier Thyristor</b>													
Forward voltage	$V_F$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		1,25 1,22	1,6	V			
Threshold voltage (for power loss calc. only)	$V_{to}$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,93 0,82		V			
Slope resistance (for power loss calc. only)	$r_t$				30	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,011 0,014		Ohm			
Reverse current	$I_r$			800		$T_J=25^{\circ}C$ $T_J=125^{\circ}C$			0,05 2	mA			
Gate controlled delay time	$t_{gd}$	$I_g=0,5A$ $di/dt=0,5A/us$		$VD=1/2V_{drm}$		$T_J=25^{\circ}C$			2	$\mu s$			
Critical rate of rise of off-state voltage	$(dv/dt)_{cr}$			$VD=2/3V_{drm}$		$T_J=125^{\circ}C$			500	V/ $\mu s$			
Critical rate of rise of on-state current	$(di/dt)_{cr}$	$I_g=0,2A$ $f=50Hz$		$VD=2/3V_{drm}$	40	$T_J=125^{\circ}C$			150	A/ $\mu s$			
Circuit-commutated turn-off time	$t_q$	$VD=2/3V_{drm}$ $t_p=200\mu s$		100	26	$T_J=125^{\circ}C$			150	$\mu s$			
Holding current	$I_H$	$VD=6V$				$T_J=25^{\circ}C$			50	mA			
Latching current	$I_L$	$t_p=10\mu s$ $I_g=0,2A$				$T_J=25^{\circ}C$			90	mA			
Gate trigger voltage	$V_{GT}$	$VD=6V$				$T_J=25^{\circ}C$ $T_J=-40^{\circ}C$			1,3 1,6	V			
Gate trigger current	$I_{GT}$	$VD=6V$				$T_J=25^{\circ}C$ $T_J=-40^{\circ}C$	11		28 50	mA			
Gate non-trigger voltage	$V_{GD}$			$VD=1/2V_{drm}$		$T_J=125^{\circ}C$			0,2	V			
Gate non-trigger current	$I_{GD}$			$VD=1/2V_{drm}$		$T_J=125^{\circ}C$			1	mA			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness $\leq$ 50um $\lambda = 0,61$ W/mK						1,57		K/W			
<b>PFC Transistor (MOSFET)-per leg</b>													
Avalanche breakdown voltage	$V_{(BR)DS}$		0		0,0003	$T_J=25^{\circ}C$	500			V			
Static drain to source ON resistance	$R_{ds(on)}$		10		20	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,11 0,24		Ohm			
Gate threshold voltage	$V_{(GS)th}$		$V_{ds}$		0,00018	$T_J=25^{\circ}C$		3	3,9	V			
Zero gate voltage drain current	$I_{DSS}$		0	500		$T_J=25^{\circ}C$			2	$\mu A$			
Gate to Source Leakage Current	$I_{GSS}$		20	0		$T_J=25^{\circ}C$			150	nA			
Turn On Delay Time	$t_{d(ON)}$	$R_{gon}=4$ Ohm $R_{goff}=4$ Ohm	10	400	20	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$			21	ns			
Rise Time	$t_r$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$				5,4		ns	
Turn off delay time	$t_{d(OFF)}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$				167,8		ns	
Fall time	$t_f$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$				13,9		ns	
Turn-on energy loss per pulse	$E_{on}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$				0,10		mWs	
Turn-off energy loss per pulse	$E_{off}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$				0,078		mWs	
Total gate charge	$Q_g$											170	nC
Gate to source charge	$Q_{gs}$						10	380	32	$T_J=25^{\circ}C$		15	nC
Gate to drain charge	$Q_{gd}$											90	nC
Input capacitance	$C_{iss}$											4200	pF
Output capacitance	$C_{oss}$	$f=1MHz$	0	25		$T_J=25^{\circ}C$		1700	pF				
Reverse transfer capacitance	$C_{rss}$							90	pF				
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness $\leq$ 50um $\lambda = 0,61$ W/mK						1,12		K/W			

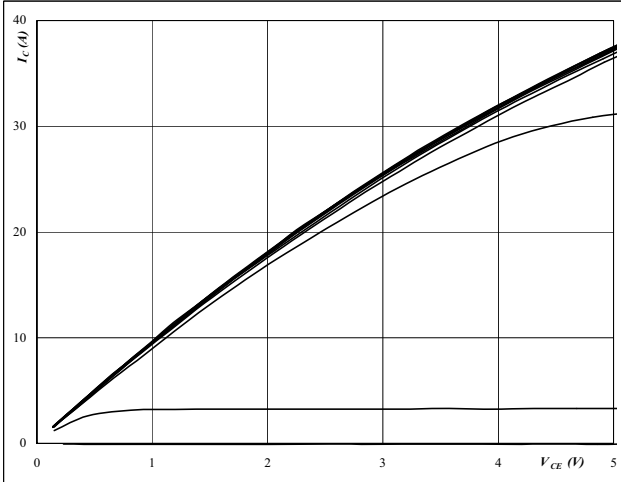
**Characteristic Values**

Parameter	Symbol	Conditions					Value			Unit
		$V_{GE}(V)$ or $V_{GS}(V)$	$V_{r}(V)$ or $V_{CE}(V)$ or $V_{DS}(V)$	$I_c(A)$ or $I_F(A)$ or $I_b(A)$	$T(^{\circ}C)$	Min	Typ	Max		
<b>PFC Diode-per leg</b>										
Forward voltage	$V_F$				16	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$	1,59 1,74	1,8		V
Reverse leakage current	$I_r$			600		$T_J=25^{\circ}C$		400		$\mu A$
Peak reverse recovery current	$I_{RRM}$	Rgon=4 Ohm Rgoff=4 Ohm	10	400	20	$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		18,7		A
Reverse recovery time	$t_{rr}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		9,8		ns
Reverse recovery charge	$Q_{rr}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,066		nC
Reverse recovered energy	$E_{rec}$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		0,0012		mWs
Peak rate of fall of reverse recovery current	$di(rec)max/dt$					$T_J=25^{\circ}C$ $T_J=125^{\circ}C$		5444		A/ $\mu s$
Thermal resistance chip to heatsink per chip	$R_{th,JH}$					Thermal grease thickness $\leq 50\mu m$ $\lambda = 0,61 W/mK$				
<b>PFC Shunt</b>										
R1 value	R					$T_J=25^{\circ}C$	21,85	23,5	26,45	mOhm
Temperature coefficient	$t_c$	20 $^{\circ}C$ to 60 $^{\circ}C$						< 30		ppm/K
Internal heat resistance	$R_{thi}$							<5		K/W
Inductance	L							< 3		nH
<b>Capacitor</b>										
C value	C					$T_J=25^{\circ}C$	400	540	680	nF
<b>NTC Thermistor</b>										
Rated resistance	$R_{25}$	Tol. $\pm 5\%$				$T_J=25^{\circ}C$	20,9	22	23,1	kOhm
Deviation of $R_{100}$	$\Delta R/R$	$R_{100} = 1486 Ohm$				$T_J=100^{\circ}C$		5		%/K
Power dissipation	$P_{25}$					$T_J=25^{\circ}C$		210		mW
B-value	$B_{(25/100)}$	Tol. $\pm 3\%$						4000		K

**PFC**
**Figure 1** PFC MOSFET

**Typical output characteristics**

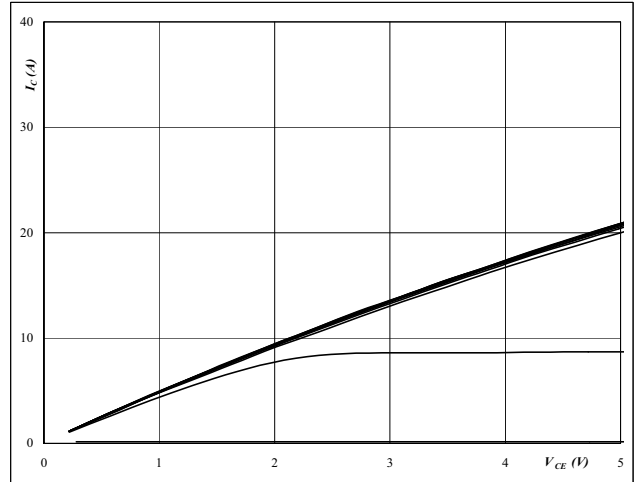
$I_C = f(V_{CE})$


 $t_p = 250 \mu s$   
 $T_J = 25 \text{ }^\circ C$   
 VGE from 3 V to 13 V in steps of 1 V

**Figure 2** PFC MOSFET

**Typical output characteristics**

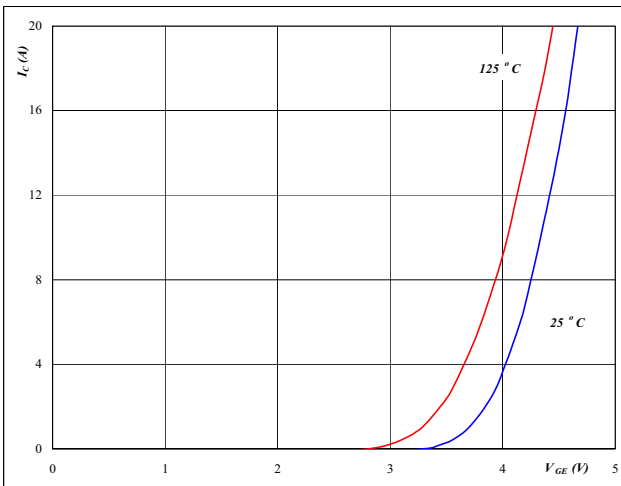
$I_C = f(V_{CE})$


 $t_p = 250 \mu s$   
 $T_J = 125 \text{ }^\circ C$   
 VGE from 3 V to 13 V in steps of 1 V

**Figure 3** PFC MOSFET

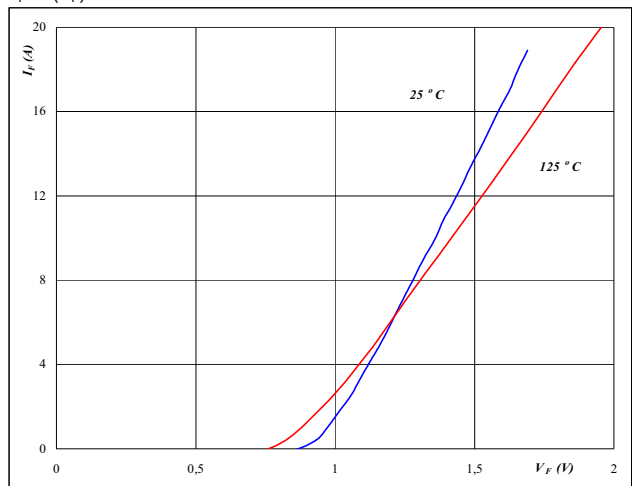
**Typical transfer characteristics**

$I_C = f(V_{GE})$

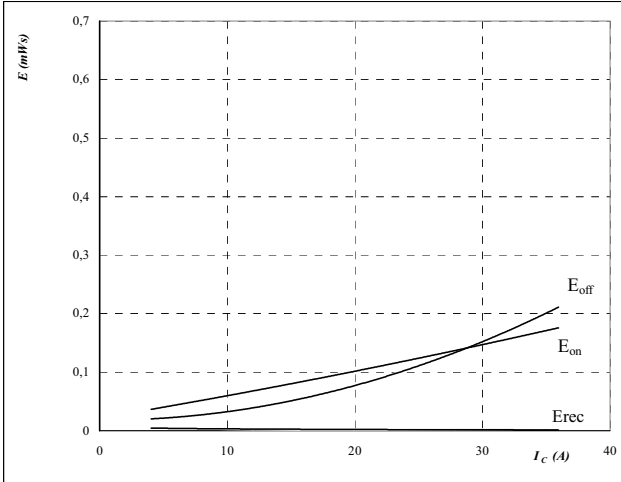

 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$ 
**Figure 4** PFC FRED

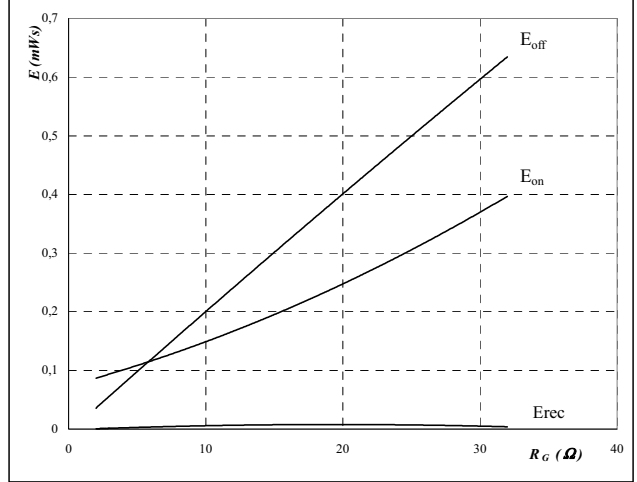
**Typical diode forward current as a function of forward voltage**

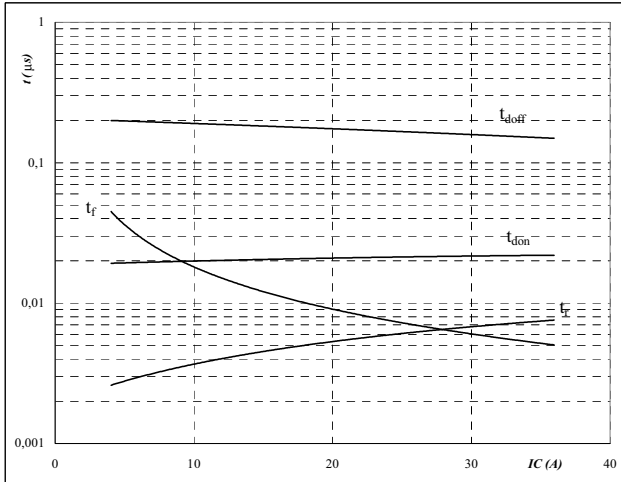
$I_F = f(V_F)$

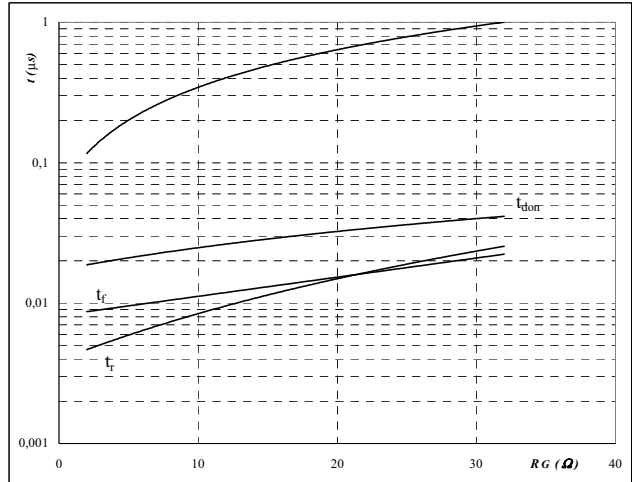

 $t_p = 250 \mu s$

**PFC**
**Figure 5** PFC MOSFET

**Typical switching energy losses**  
 as a function of collector current  
 $E = f(I_C)$ 

 inductive load  
 $T_J = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 400 \text{ V}$   
 $V_{GE} = 10 \text{ V}$   
 $R_{gon} = 4 \text{ } \Omega$   
 $R_{goff} = 4 \text{ } \Omega$ 
**Figure 6** PFC MOSFET

**Typical switching energy losses**  
 as a function of gate resistor  
 $E = f(R_G)$ 

 inductive load  
 $T_J = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 400 \text{ V}$   
 $V_{GE} = 10 \text{ V}$   
 $I_C = 20 \text{ A}$ 
**Figure 7** PFC MOSFET

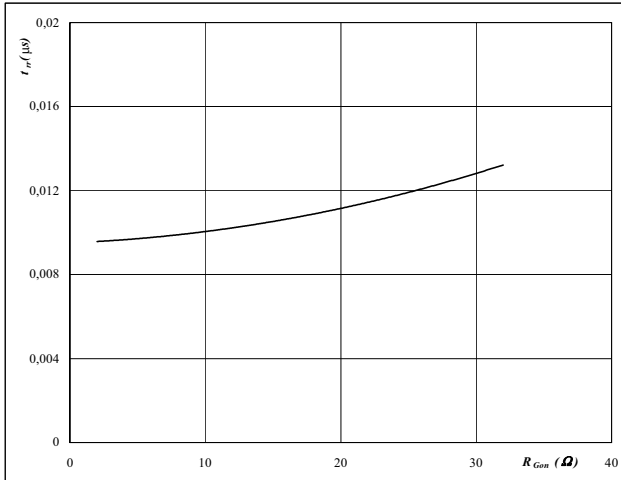
**Typical switching times as a**  
 function of collector current  
 $t = f(I_C)$ 

 inductive load  
 $T_J = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 400 \text{ V}$   
 $V_{GE} = 10 \text{ V}$   
 $R_{gon} = 4 \text{ } \Omega$   
 $R_{goff} = 4 \text{ } \Omega$ 
**Figure 8** PFC MOSFET

**Typical switching times as a**  
 function of gate resistor  
 $t = f(R_G)$ 

 inductive load  
 $T_J = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 400 \text{ V}$   
 $V_{GE} = 10 \text{ V}$   
 $I_C = 20 \text{ A}$

**PFC**
**Figure 9** PFC FRED diode

**Typical reverse recovery time as a function of MOSFET turn on gate resistor**

$$t_{rr} = f(R_{gon})$$

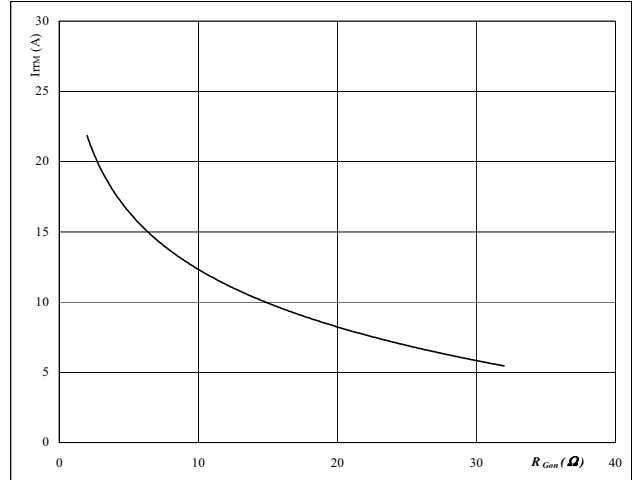


$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 400 \text{ V}$   
 $I_F = 20 \text{ A}$   
 $V_{GE} = 10 \text{ V}$

**Figure 10** PFC FRED diode

**Typical reverse recovery current as a function of MOSFET turn on gate resistor**

$$I_{RRM} = f(R_{gon})$$

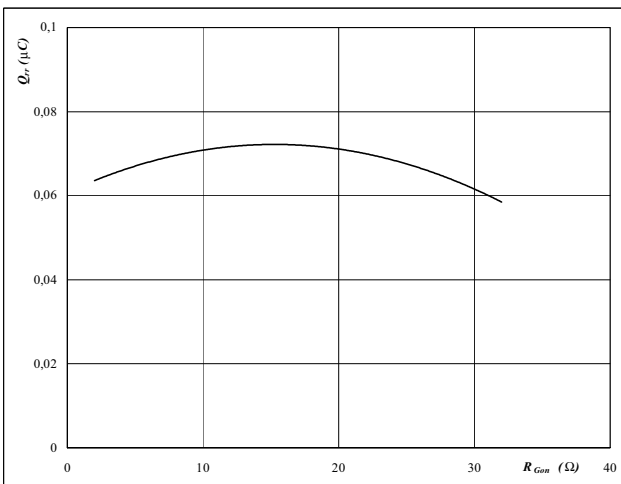


$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 400 \text{ V}$   
 $I_F = 20 \text{ A}$   
 $V_{GE} = 10 \text{ V}$

**Figure 11** PFC FRED diode

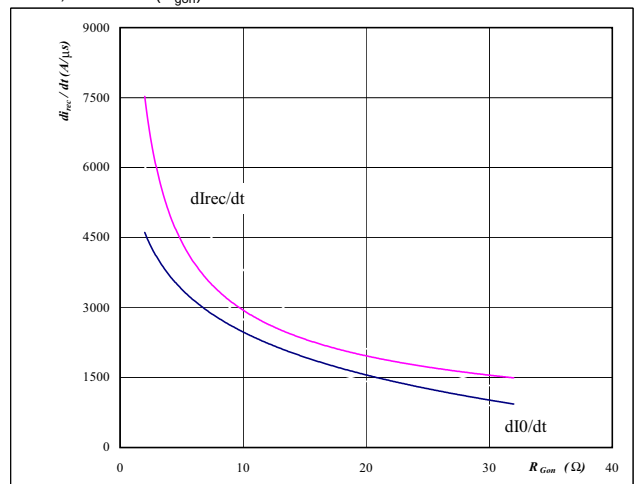
**Typical reverse recovery charge as a function of MOSFET turn on gate resistor**

$$Q_{rr} = f(R_{gon})$$



$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 400 \text{ V}$   
 $I_F = 20 \text{ A}$   
 $V_{GE} = 10 \text{ V}$

**Figure 12** PFC FRED diode

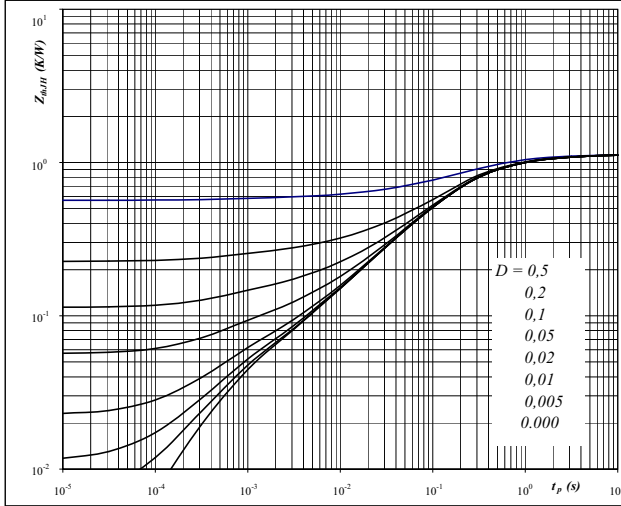
**Typical rate of fall of forward and reverse recovery current as a function of MOSFET turn on gate resistor**  
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$ 


$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 400 \text{ V}$   
 $I_F = 20 \text{ A}$   
 $V_{GE} = 10 \text{ V}$

**PFC**
**Figure 13** PFC MOSFET

**MOSFET transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



$$D = \frac{t_p}{T}$$

$$R_{thJH} = 1,12 \quad \text{K/W}$$

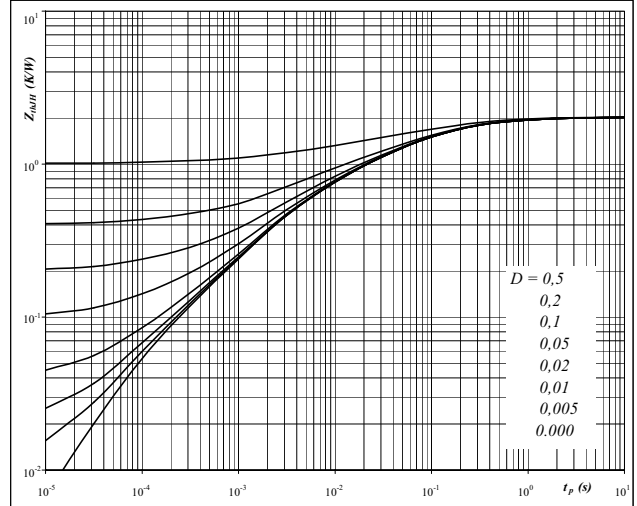
IGBT thermal model values

R (C/W)	Tau (s)
5,04E-02	9,74E+00
2,02E-01	1,14E+00
6,23E-01	2,01E-01
1,73E-01	3,65E-02
5,04E-02	5,53E-03
3,53E-02	6,12E-04

**Figure 14** PFC FRED diode

**FRED transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



$$D = \frac{t_p}{T}$$

$$R_{thJH} = 2,02 \quad \text{K/W}$$

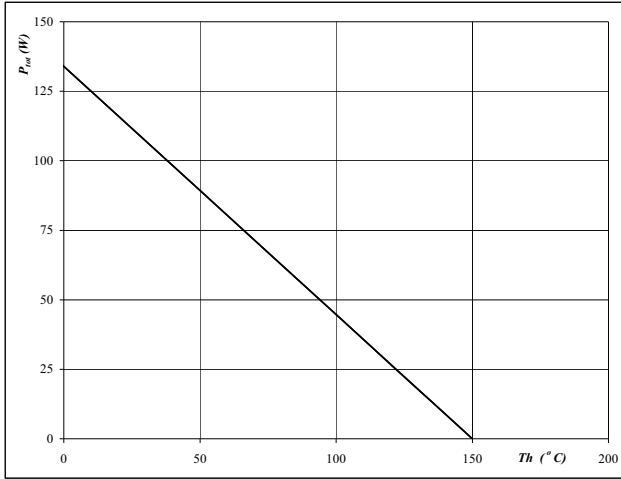
FRED thermal model values

R (C/W)	Tau (s)
1,46E-01	1,52E+00
5,50E-01	1,86E-01
5,72E-01	4,49E-02
4,60E-01	8,20E-03
2,42E-01	1,49E-03
5,35E-02	1,16E-04

**PFC**
**Figure 15** PFC MOSFET

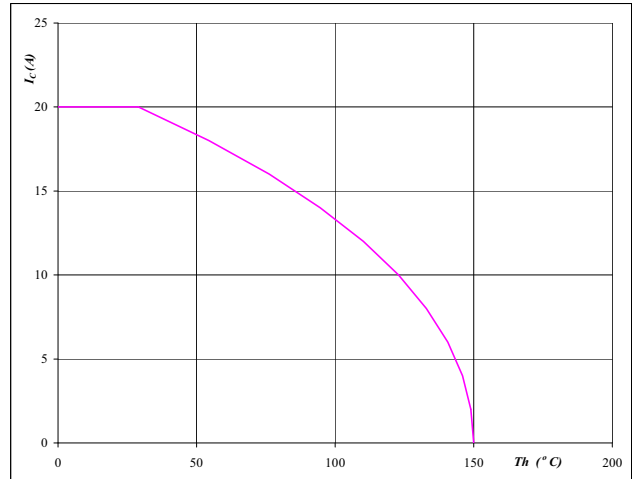
**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$


 $T_j = 150 \text{ } ^\circ\text{C}$ 
**Figure 16** PFC MOSFET

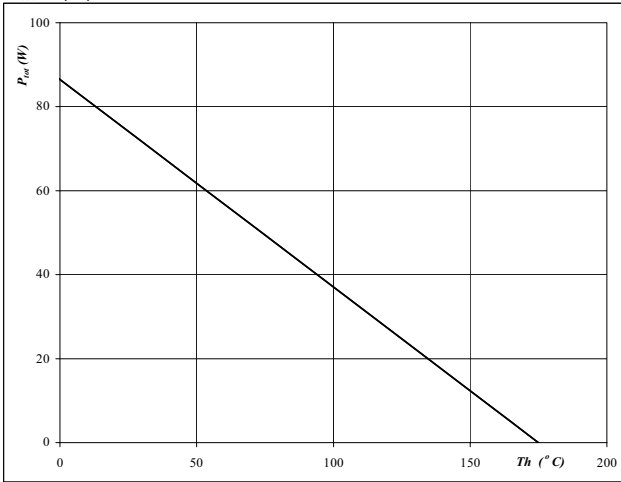
**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


 $T_j = 150 \text{ } ^\circ\text{C}$   
 $V_{GE} = 15 \text{ V}$ 
**Figure 17** PFC FRED

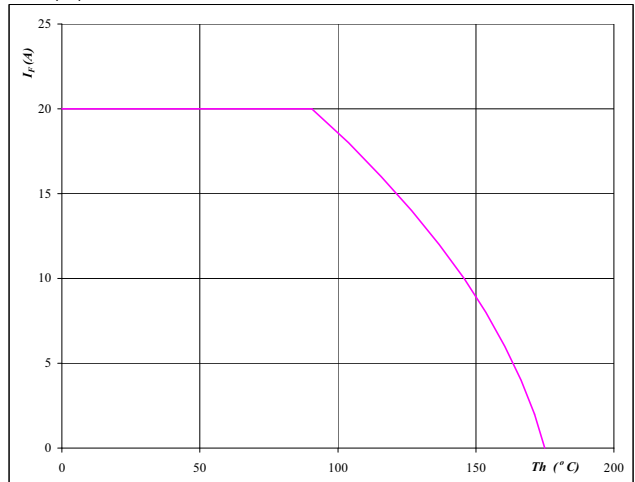
**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$


 $T_j = 175 \text{ } ^\circ\text{C}$ 
**Figure 18** PFC FRED

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$

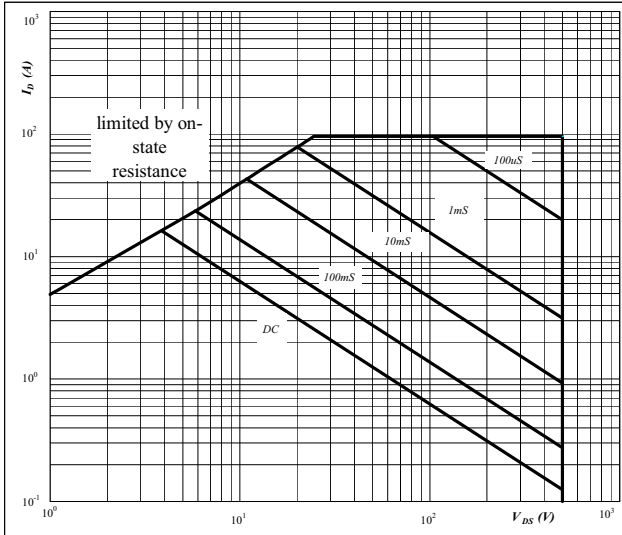

 $T_j = 175 \text{ } ^\circ\text{C}$

**PFC**
**Figure 19** PFC MOSFET

Safe operating area function of drain-source voltage

**Output inverter MOSFET**

$$I_D = f(V_{DS})$$



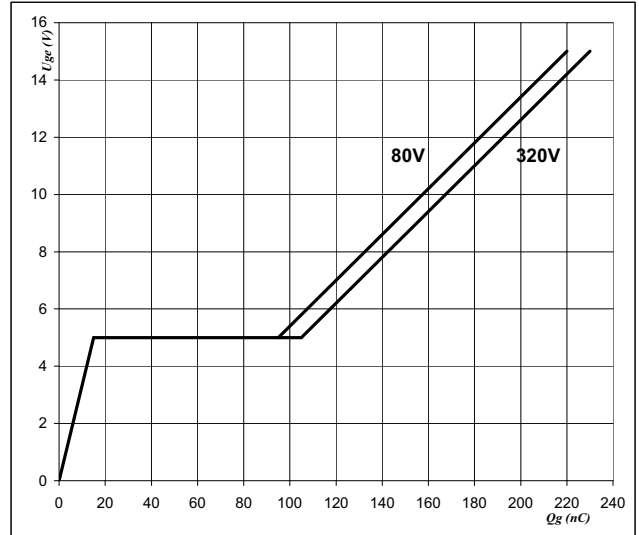
D =	0	
Th =	80	°C
Vgs =	10	V
Tj =	125	°C

**Figure 20** PFC MOSFET

Gate voltage vs Gate charge

**Output inverter MOSFET**

$$U_{ge} = f(Q_g)$$



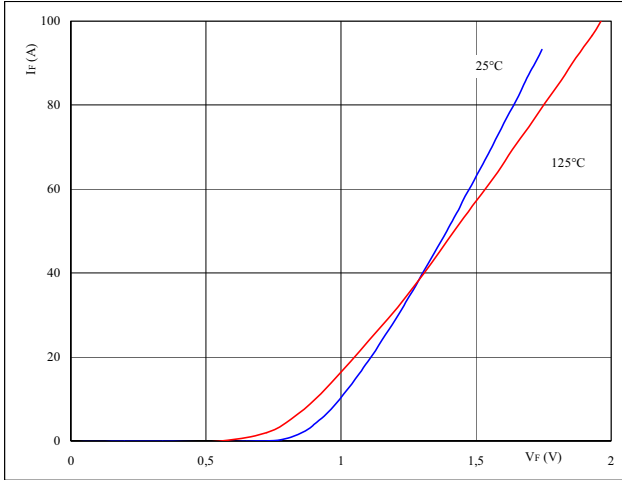
$V_{GEoff} =$	0	V
$V_{GEon} =$	10	V
$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$Q_g =$	131	nC

## Input Rectifier Bridge

**Figure 1** Rectifier diode

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

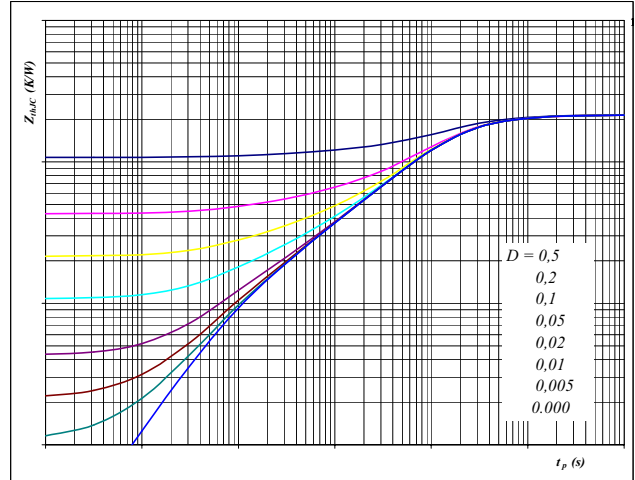


$$t_p = 250 \mu s$$

**Figure 2** Rectifier diode

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



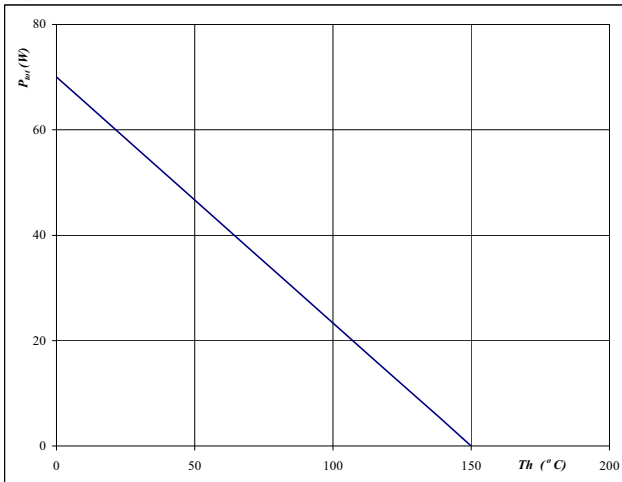
$$D = t_p / T$$

$$R_{thJH} = 2,14 \text{ K/W}$$

**Figure 3** Rectifier diode

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

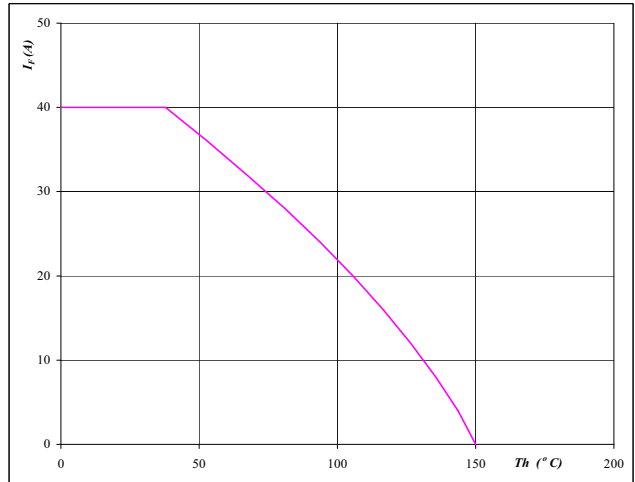


$$T_j = 150 \text{ } ^\circ\text{C}$$

**Figure 4** Rectifier diode

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



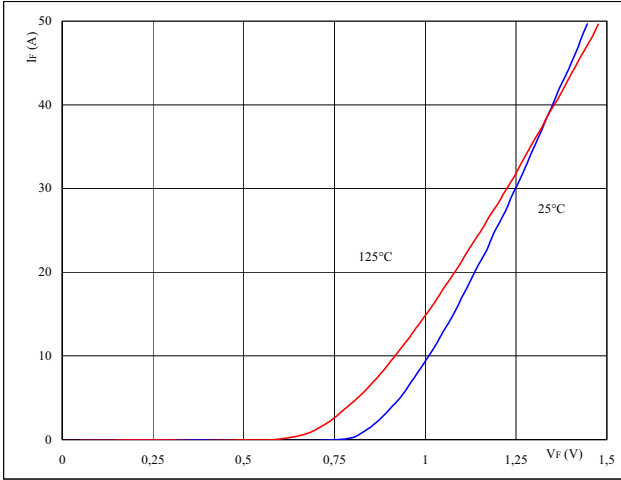
$$T_j = 150 \text{ } ^\circ\text{C}$$

## Thyristor

**Figure 1** Thyristor

**Typical thyristor forward current as a function of forward voltage**

$$I_F = f(V_F)$$

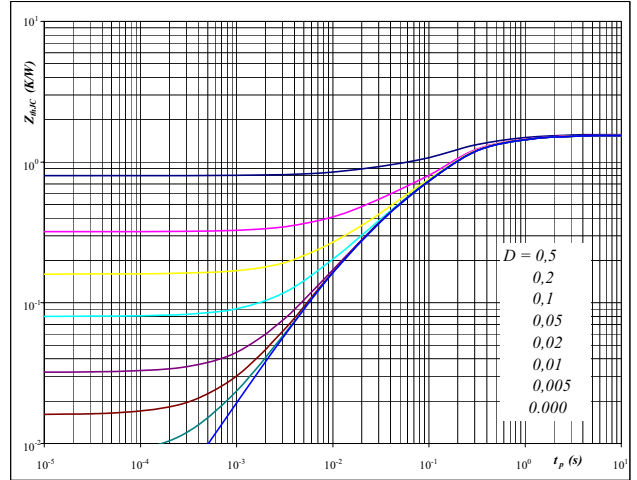


$$t_p = 250 \mu s$$

**Figure 2** Thyristor

**Thyristor transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



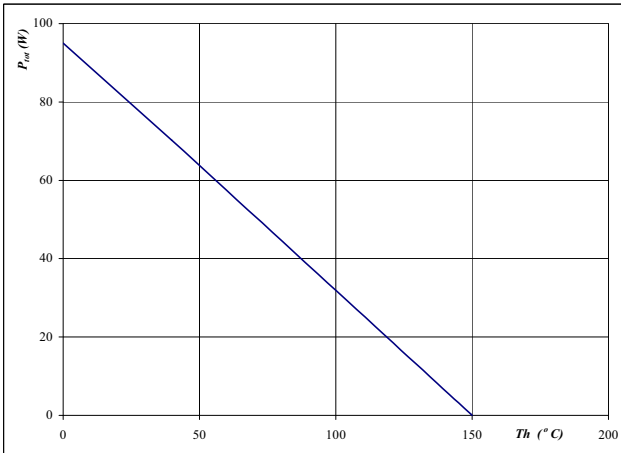
$$D = t_p / T$$

$$R_{thJH} = 1,57 \text{ K/W}$$

**Figure 3** Thyristor

**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$

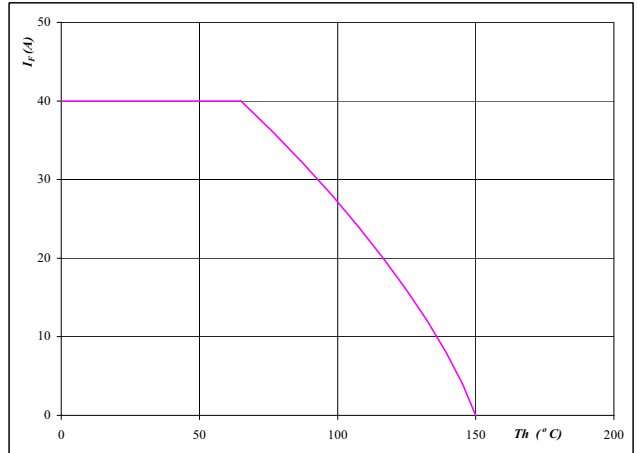


$$T_j = 150 \text{ } ^\circ\text{C}$$

**Figure 4** Thyristor

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$

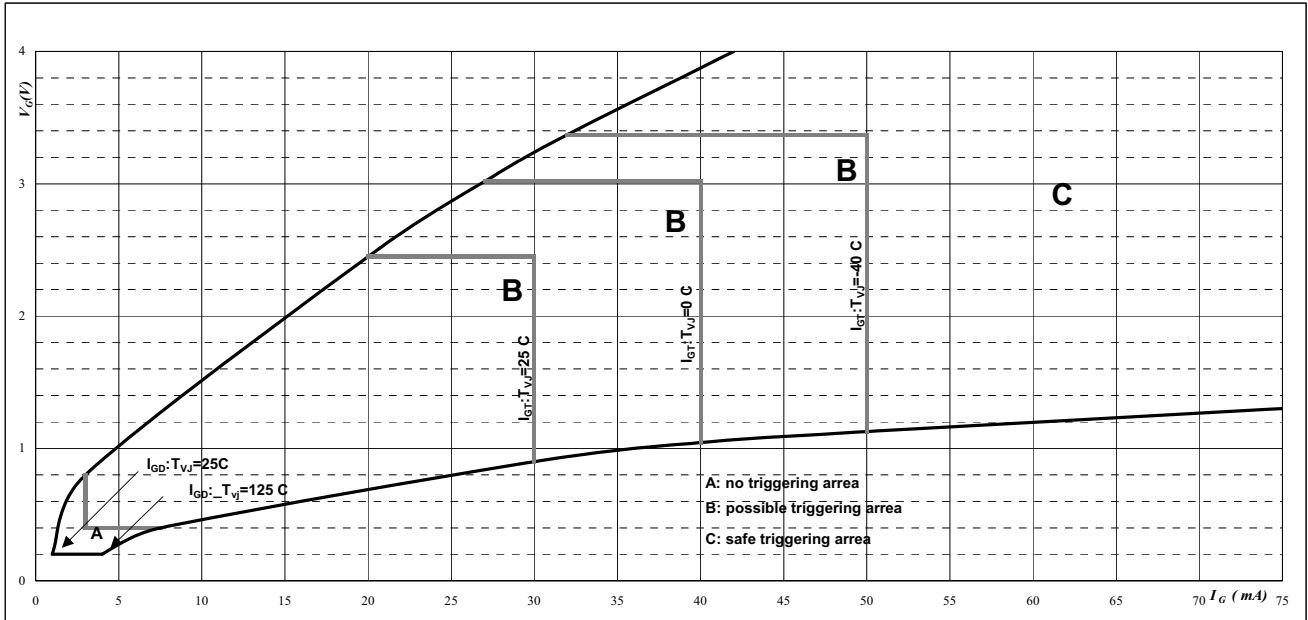


$$T_j = 150 \text{ } ^\circ\text{C}$$

## Thyristor

**Figure 5**

Thyristor

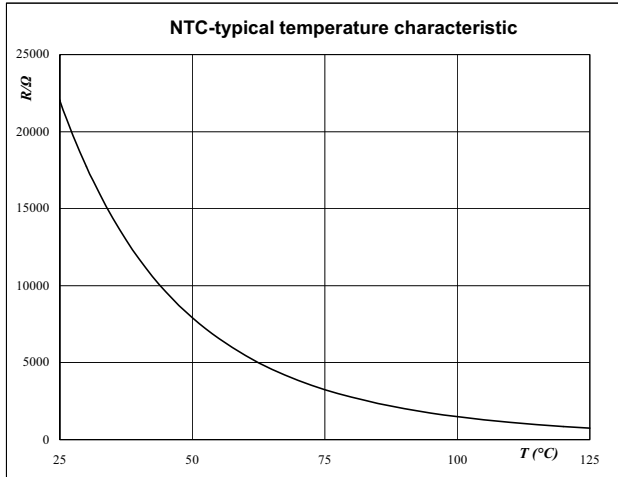
**Gate trigger characteristics**


## Thermistor

Figure 1 Thermistor

Typical NTC characteristic  
as a function of temperature

$R_T = f(T)$

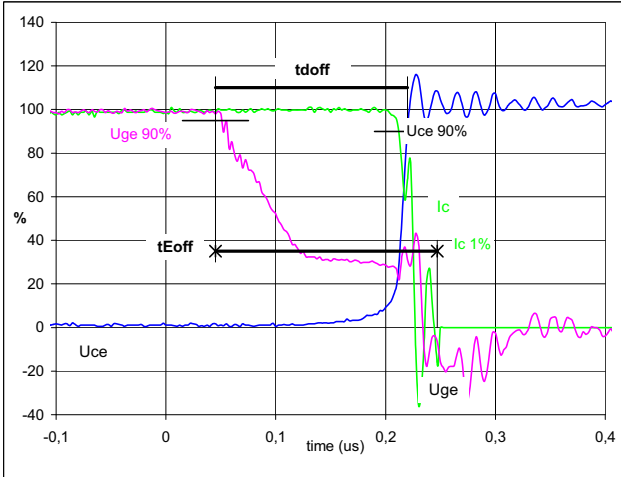


## Switching Definitions PFC

**General conditions**

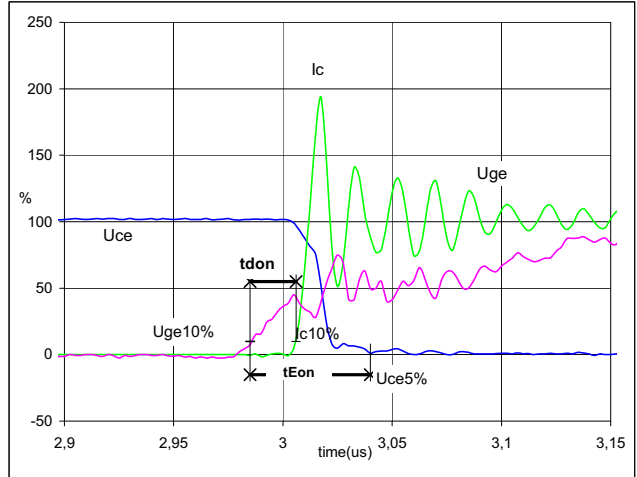
$T_j$	=	125 °C
$R_{gon}$	=	4 $\Omega$
$R_{goff}$	=	4 $\Omega$

**Figure 1** PFC MOSFET

**Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$**   
 ( $t_{Eoff}$  = integrating time for  $E_{off}$ )


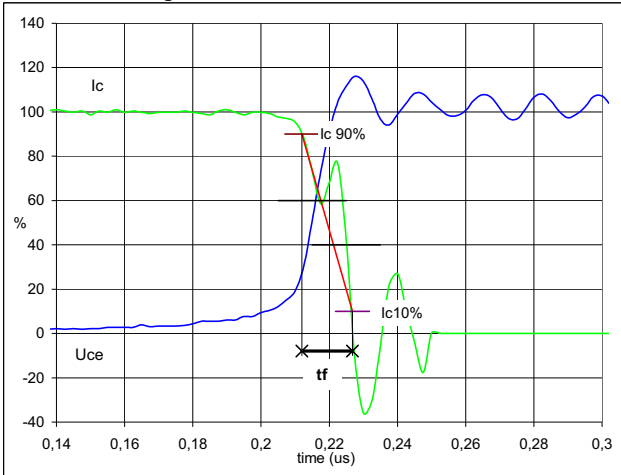
$V_{GE}(0\%) =$	0	V
$V_{GE}(100\%) =$	10	V
$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$t_{doff} =$	0,17	$\mu s$
$t_{Eoff} =$	0,20	$\mu s$

**Figure 2** PFC MOSFET

**Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$**   
 ( $t_{Eon}$  = integrating time for  $E_{on}$ )


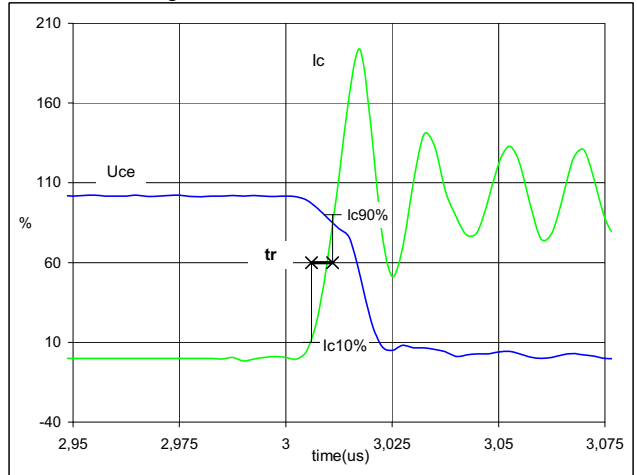
$V_{GE}(0\%) =$	0	V
$V_{GE}(100\%) =$	10	V
$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$t_{don} =$	0,021	$\mu s$
$t_{Eon} =$	0,055	$\mu s$

**Figure 3** PFC MOSFET

**Turn-off Switching Waveforms & definition of  $t_f$** 


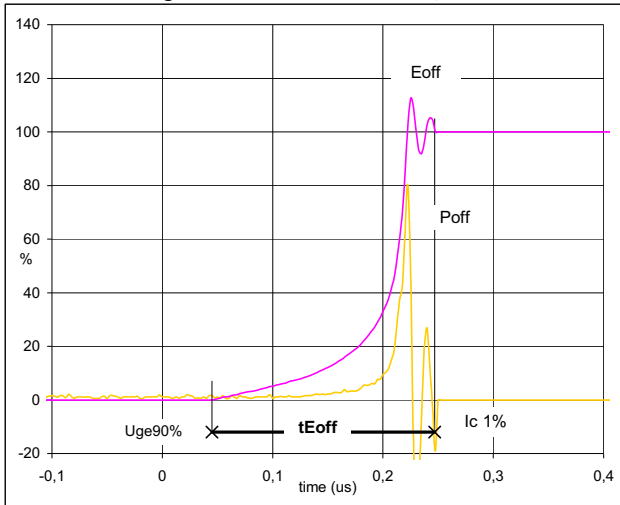
$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$t_f =$	0,015	$\mu s$

**Figure 4** PFC MOSFET

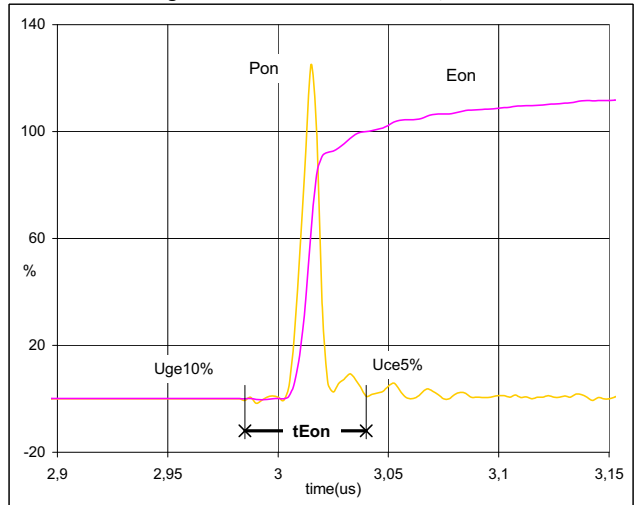
**Turn-on Switching Waveforms & definition of  $t_r$** 


$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$t_r =$	0,005	$\mu s$

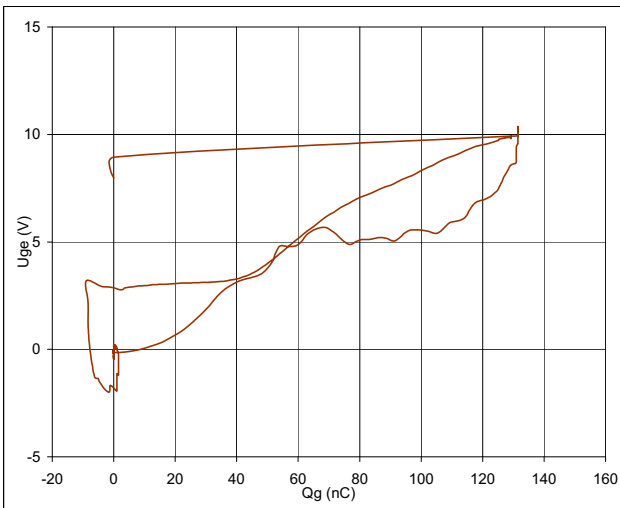
### Switching Definitions PFC

**Figure 5** PFC MOSFET  
**Turn-off Switching Waveforms & definition of  $t_{Eoff}$** 


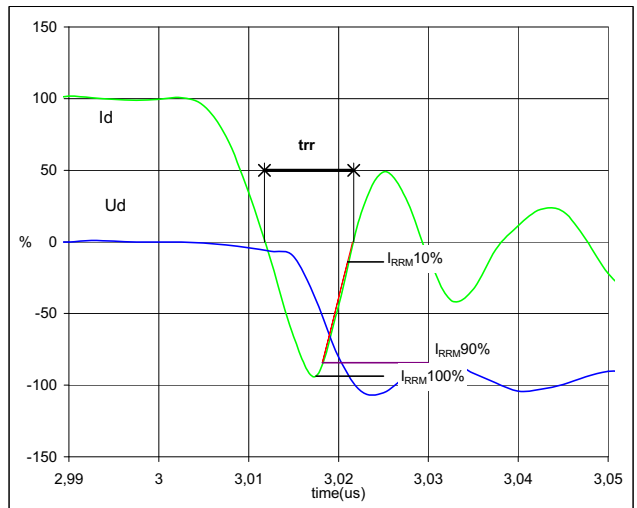
$P_{off}(100\%) =$	7,98	kW
$E_{off}(100\%) =$	0,078	mJ
$t_{Eoff} =$	0,20	$\mu$ s

**Figure 6** PFC MOSFET  
**Turn-on Switching Waveforms & definition of  $t_{Eon}$** 


$P_{on}(100\%) =$	7,98	kW
$E_{on}(100\%) =$	0,10	mJ
$t_{Eon} =$	0,055	$\mu$ s

**Figure 7** PFC MOSFET  
**Gate voltage vs Gate charge**


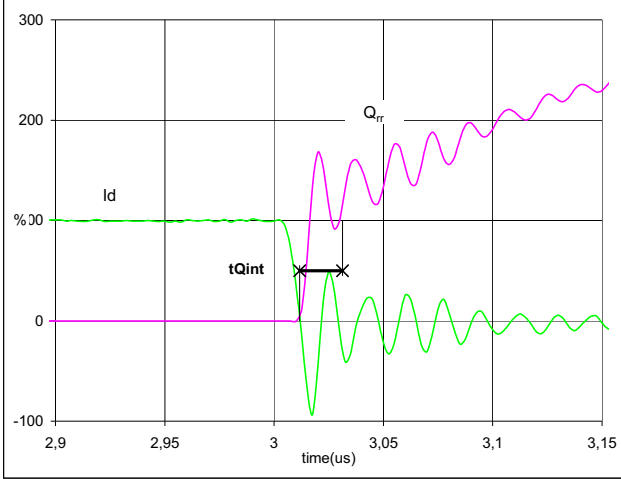
$V_{GEoff} =$	0	V
$V_{GEon} =$	15	V
$V_C(100\%) =$	400	V
$I_C(100\%) =$	20	A
$Q_g =$	131	nC

**Figure 8** PFC FRED  
**Turn-off Switching Waveforms & definition of  $t_{tr}$** 


$V_d(100\%) =$	400	V
$I_d(100\%) =$	20	A
$I_{RRM}(100\%) =$	19	A
$t_{tr} =$	0,01	$\mu$ s

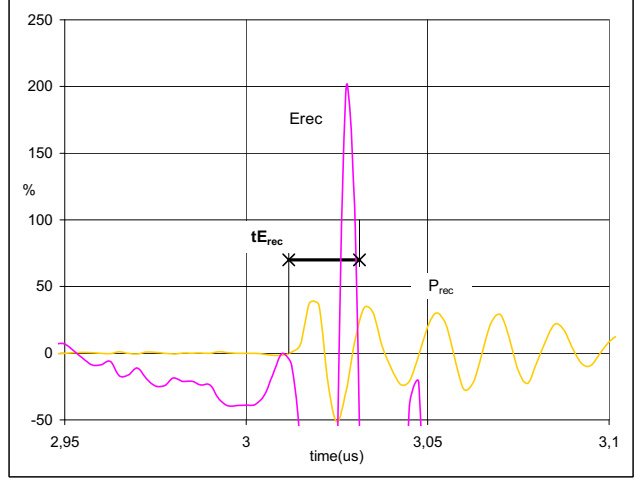
### Switching Definitions PFC

**Figure 9** PFC FRED

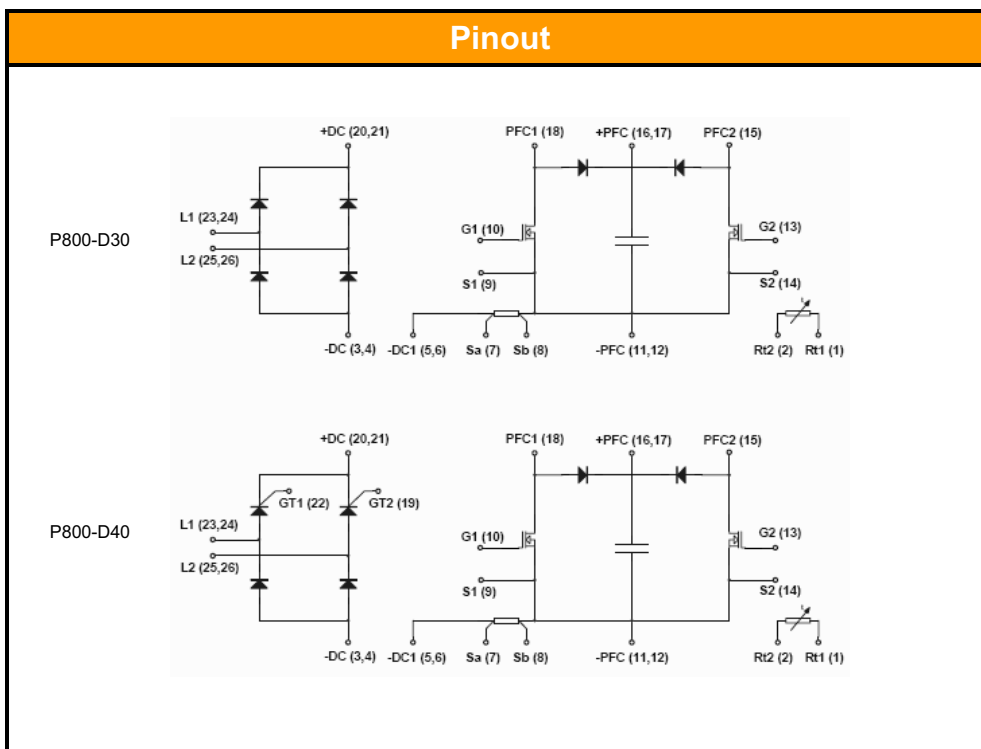
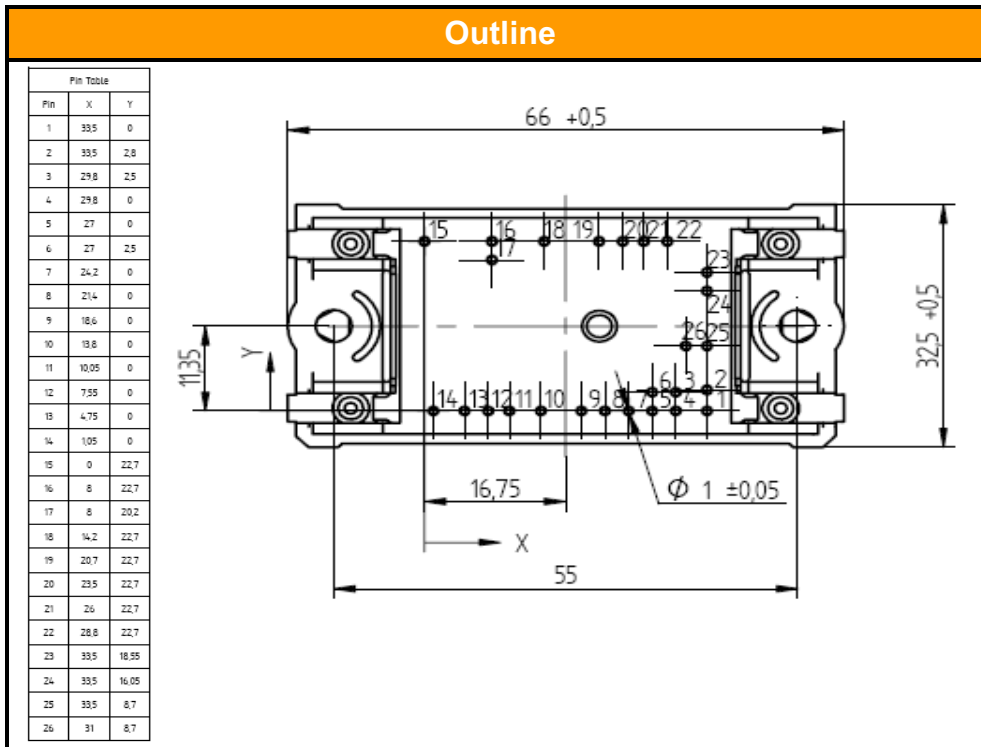
**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 ( $t_{Qrr}$  = integrating time for  $Q_{rr}$ )


$I_d$ (100%) =	20	A
$Q_{rr}$ (100%) =	0,066	$\mu$ C
$t_{Qint}$ =	0,019	$\mu$ s

**Figure 10** PFC FRED

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 ( $t_{Erec}$  = integrating time for  $E_{rec}$ )


$P_{rec}$ (100%) =	7,98	kW
$E_{rec}$ (100%) =	0,001	mJ
$t_{Erec}$ =	0,019	$\mu$ s

**Package Outline and Pinout**


**PRODUCT STATUS DEFINITIONS**

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.

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