SIEMENS

Product Brief

VDSL PEB 22810
PEB 22811
PEB 22812

Very High Bitrate Digital Subscriber Line Chipset

VDSL (Very high bitrate Digital Subscriber Line) is the fastest technology for communication on the existing conventional copper infrastructure currently available. The Siemens POTSWIRE™ technology VDSL chipset is a highly sophisticated low-cost solution for a very versatile VDSL system. Based on the QAM line code the system complies with the upcoming standards. It excellently deals with POTS/ISDN on the same twisted pair and xDSL services on the same bundle, and can also be configured to support spectral compatibility with amateur radio.

Potential Applications

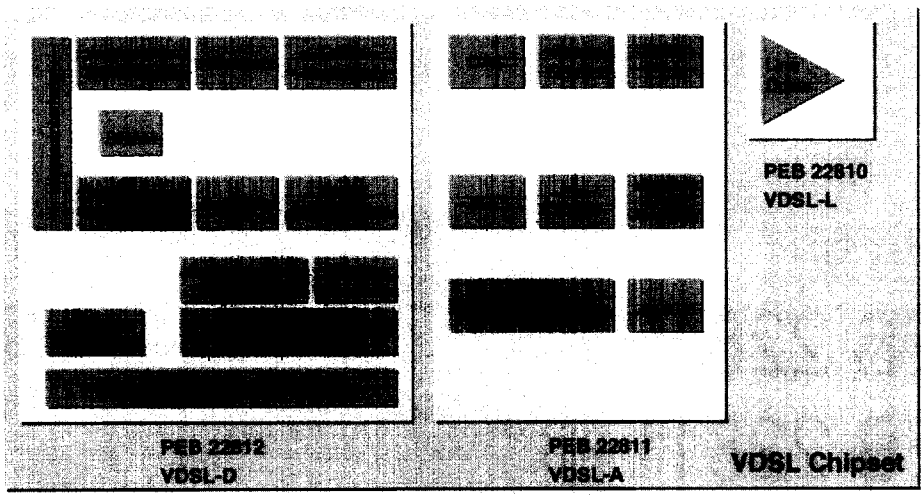
- Remote LAN Access
- Fast Internet Access
- Teleworking
- Video on demand
- Video conferencing
- TV/Radio Broadcasting
- Video Telephony
- Telemedicine

Features

- QAM line code
- Frequency Division Duplexing
- Symmetric and asymmetric data transmission of up to 13 Mbit/s
- Robust operation on severely distorted lines
- Spectral compatibility with xDSL, ISDN (2B1Q / 4B3T) and narrowband interferers
- Convolutional interleaver with internal SRAM
- UTOPIA Level 1 and Level 2 interface
  -ATM TC layer
- Transmit power management
- Low power consumption <3 W
- Power down mode with fast warmstart capability (< 100 ms)
- Configureable for CO and RT side
- Embedded Digital Controlled Crystal Oscillator (DCXO) for timing recovery
- Embedded microcontroller
- JTAG for chip level and board level testing

Packing

Type				
VDSL-D	PEB 22812	Q67231-H1087	MQFP-144	Q1/99
VDSL-A	PEB 22811	Q67233-H1062	MQFP-64	now
VDSL-L	PEB 22810	Q67231-H1074	P-DSO-20-5	now



VDSL Chipset
Block Diagram

Development and Support Tools

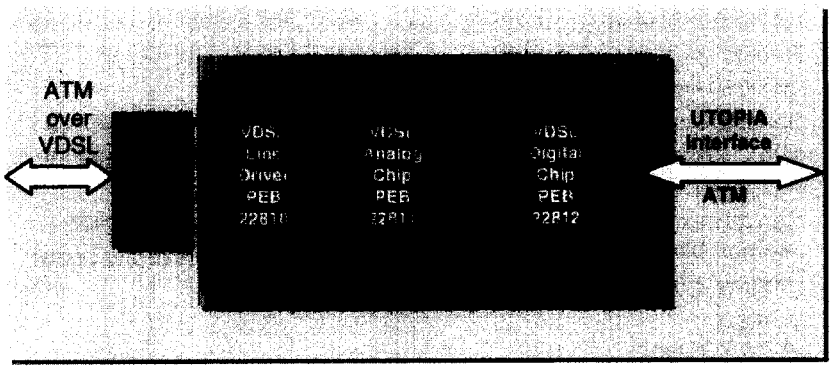
- Evaluation Board VDSL Chipset
- Evaluation Board VDSL-A + VDSL-L

Documentation

VDSL Product Overview	04.98 preliminary / 1.0
VDSL-D Data Sheet	09.98 preliminary / 1.0
VDSL-A Data Sheet	09.98 preliminary / 1.0
VDSL-L Data Sheet	09.98 preliminary / 1.0

Application Example

VDSL Modem



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