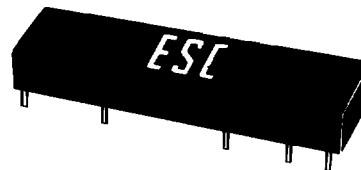
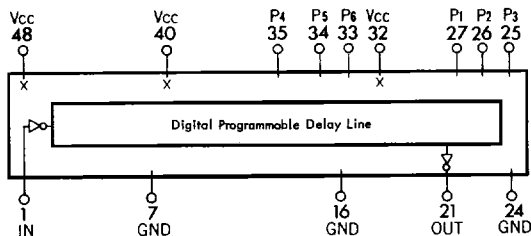
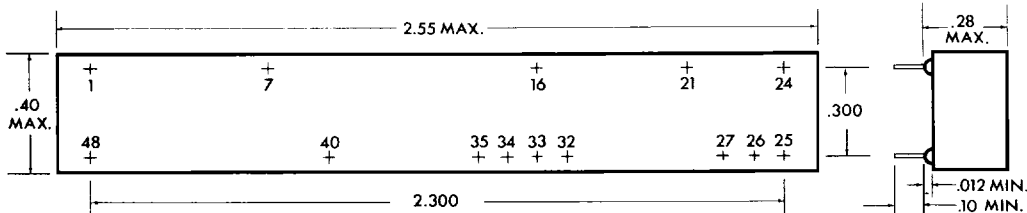


TTL DIGITAL PROGRAMMABLE DELAY LINES 48 PIN PACKAGE 6 BIT

SERIES PTT6



Model No.	Zero Step Delay (ns)	Maximum Delay (ns) (nom)	Delay per step (ns)
PTT6-1	20 ± 2	83	1 ± .5
PTT6-2		146	2 ± .5
PTT6-3		209	3 ± .6
PTT6-4		272	4 ± .8
PTT6-5		335	5 ± 1

Data Select Pins	Model Number																		
	P6-33	P5-34	P4-35	P3-25	P2-26	P1-27													
PTT6-1	—	20	1	2	3	4	5	6	7	8	9	10	11	12	13	14	H	H	H
																	H	H	H
																	H	H	H
																	H	H	H
																	L	L	L
																	L	L	L
																	H	L	H

SPECIFICATIONS:

- Zero step delay is measured from the input pin #1
- All programmable delays after step zero are referenced to step zero
- Programmable delay tolerances: ± 2ns or 5% whichever is greater
- Rise Time: 4ns max
- Minimum Pulse Width: 40% of Total Delay
- Maximum Duty Cycle: 50%
- Operating temp. range: 0°C to + 70°C
- Temp. coeff. of delay: 1.0ns + 500ppm/°C
- Vol: .4V
- Voh: 2.4V min
- Vcc: 5VDC ± 0.25V
- Icc: 190mA max
- Terminals: Electro tin plated alloy 42 .020w x .010th

TEST CONDITIONS:

- Vcc=5.0VDC, Temp. 25°C
- Time delay measured at the 1.5volt level of the leading edge
- Rise Time measured from .75V to 2.4 volts
- Output loaded with 15pf
- Input pulse width: 3 x max time delay
- Input rise time: 2ns
- Pulse amplitude: 3.0 volts
- Pulse spacing: 1000ns

* Available for -55°C to + 125°C operation. Specify PTT6 series. The height will change to .360.