

RCM1584U-A

● Applications

● Features

- 1) Most suitable for the alphabet and number display.
- 2) Wide viewing angle and high contrast.
- 3) Compact and light weight for easy assembly.
- 4) Low power consumption.

Technical drawing of the PV module showing front, side, and rear views with dimensions in mm.

Front View Dimensions:

- Overall width: 174 ± 0.3
- Overall height: 162.5 MIN.
- Top edge dimensions: 0.3 MAX. , 1 MAX. , 100 ± 0.3 , 98 MIN. (85.0), 7.5 ± 0.3 , 2 MAX. , 7.7 ± 0.3
- Cell dimensions: 26.5 MAX. , 20.5 MAX. , 10 MAX. , 26.5 MAX. , 20.5 MAX. , 10 MAX.
- Bottom edge dimensions: 4 ± 0.3 , 32 ± 1.0 , 65 ± 0.5 , 1.3 ± 0.5 , 48.4 , 82 ± 0.3
- Mounting hole dimensions: $2 - \phi 3.96^{+0.15}_0$ Hole, 41 ± 0.1 , 51 ± 0.5 , 27.94 (P2.54x11), 11.53
- Other dimensions: 170 , 127.475 , 42.525 , 26.5 MAX. , 20.5 MAX. , 10 MAX. , 152.4

Side View Dimensions:

- Overall height: 11.12
- Top edge dimensions: 1.1 ± 0.1 , 2.75 ± 0.3 , 1.1 ± 0.1
- Cell thickness: 0.7 ± 0.7
- Mounting bracket dimensions: 0.7 ± 0.7 , 4.7 ± 0.7
- UV REGIN labels
- Bottom edge dimensions: 8.54 , 1.6
- Viewing angle: (6.00)

Rear View Dimensions:

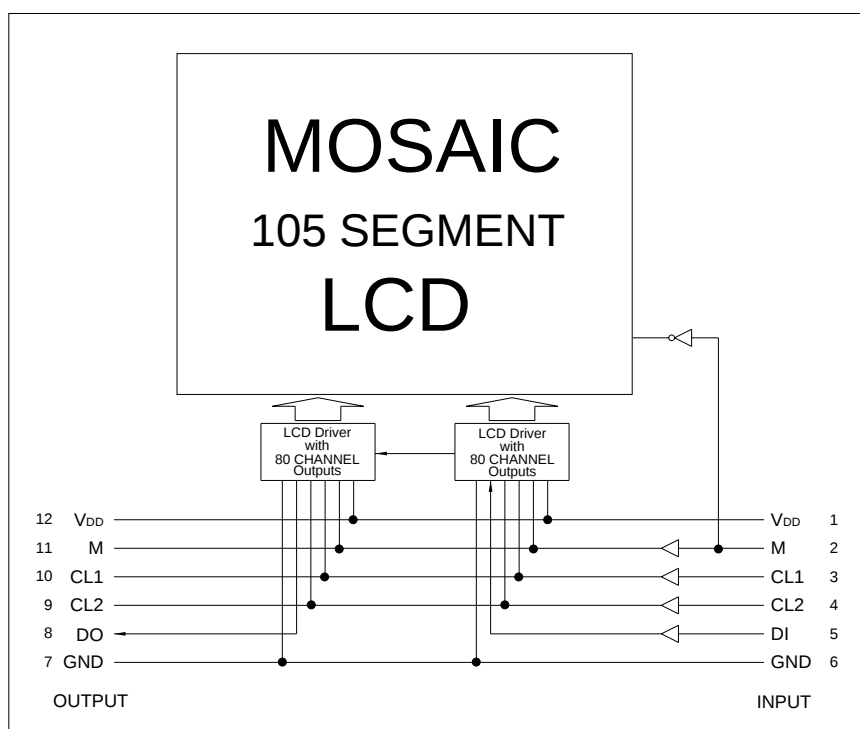
- Overall width: 85.0
- Cell dimensions: 9.96 , 1.86 , 19.34 , 1.86 , 9.96 , 19.15 , 1.96
- Cell thickness: 1.6
- Other dimensions: 11.12 , 1.6 , 17.6 , 24.32 , 11.12 , 17.6 , 1.6 , 1.6 , 11.12 , 24.16 , 1.6

Other Labels:

- ①: Junction box
- ②: 4-Hole
- ③: Mounting bracket
- ④: Mounting bracket

Liquid crystal displays

●Block diagram

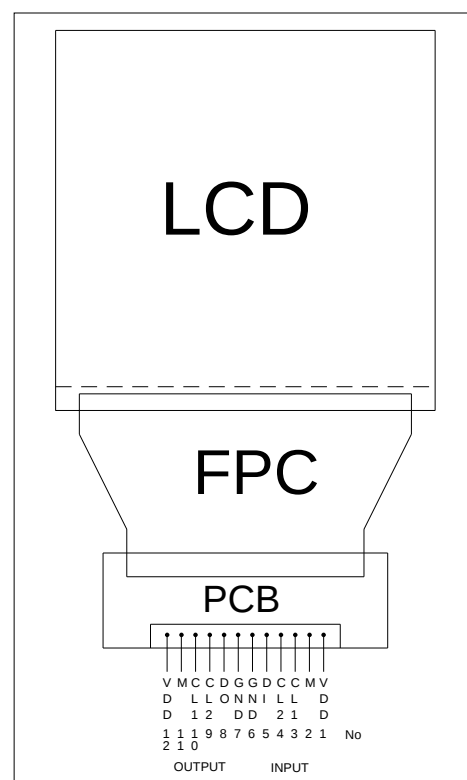


●Pin functions

(1) Input (CN1)

Pin no.	Symbol	IN / OUT	Function
1	V _{DD}	—	Charge 5Volt
2	M	IN	Alternating signal of LCD operating output
3	CL1	IN	Data latch signal (display at descending edge)
4	CL2	IN	Shift resistor signal (displayed at descending edge)
5	DI	IN	Display data signal (1: Lighting 0: Non-Lighting)
6	GND	—	Ground electric potential
7	GND	—	Ground electric potential
8	DO	OUT	Display data signal
9	CL2	OUT	Shift resistor signal
10	CL1	OUT	Data latch signal
11	M	OUT	Alternating signal
12	V _{DD}	—	5Volt

●Pin



Liquid crystal displays

●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Power supply voltage	Circuit	V _{DD}	-0.3 ~ +7.0	V
	LCD operation	V _{DD} -V _{EE}	-0.3 ~ +7.0	V
Input voltage		V _{IN}	-0.3 ~ V _{DD} +0.3	V
Operating temperature		T _{opr}	-20 ~ +70	°C
Storage temperature		T _{stg}	-40 ~ +85	°C

●Electrical characteristics (V_{DD} = 5.0V, GND = 0V, Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input high level voltage	V _{IH}	0.8V _{DD}	—	V _{DD}	V	
Input low level voltage	V _{IL}	—	—	0.2V _{DD}	V	
Output high level voltage	V _{OH}	V _{DD} -0.4	—	—	V	I _{OH} = -0.4mA
Output low level voltage	V _{OL}	—	—	0.4	V	I _{OL} = +0.4mA
Input LCD voltage	V _{LCD}	—	5.0	—	V	
Consuming current	I _{DD}	—	—	10.0	mA	f _{CL} = 1MHz, f _M = 100Hz

●AC characteristics (V_{DD} = 5.0V, GND = 0V, Ta = 25°C)

Parameter	Symbol	Applicable terminal	Min.	Typ.	Max.	Unit
Data shift frequency	f _{CL}	CL2	—	—	1	MHz
Clock high level width	t _{CWH}	CL1, CL2	470	—	—	ns
Clock low level width	t _{CWL}	CL2	470	—	—	ns
Data setup time	t _{SU}	DI	120	—	—	ns
Clock setup time 1	t _{SL}	CL2	220	—	—	ns
Clock setup time 2	t _{LS}	CL1	220	—	—	ns
Data hold time	t _{DH}	DI	120	—	—	ns
Clock rise / fall time	t _{ct}	CL1, CL2	—	—	50	ns
Output delay time	t _{pd}	DO	—	—	250	ns
Alternating signal	f _M	M	50	100	150	Hz

●Timing characteristics

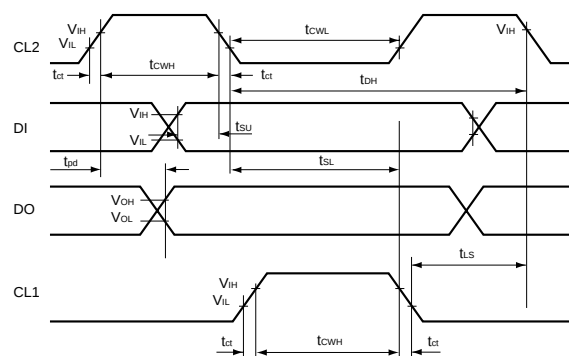


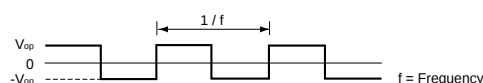
Fig.1

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●Optical characteristics (Ta = 25°C)

NO.	Parameter		Symbol	Temperature (°C)	Min.	Typ.	Max.	Unit	Note
1	Response time		Tr	0	–	400	800	ms	(Ref. 2)
				25	–	65	100		
			Td	0	–	150	300		
				25	–	45	100		
2	Viewing Range	Vertical	θ	25	0	–	60	deg	(Ref. 3) K≥3
		horizontal	φ	25	90	–	270		
3	Contrast ratio		K	25	35	50	–	–	φ = 180°, θ = 10°

(Note 1) Driving pulse
Static drive

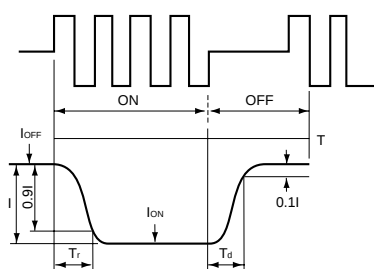


(1) ϕ : Angle which an observer will become Z Z'.

(2) θ : When observers position is considered as flat X,Y over a projector angle which makes Y Y'.

(3) Greatest viewing angle direction : Time axis which represent best contrast ratio.

(Note 2) Response time definition and condition



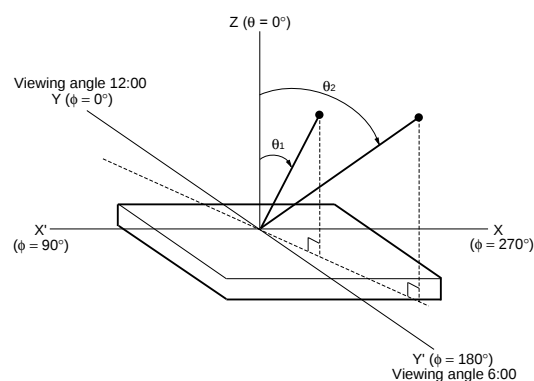
Tr : The time required to activate from non-selecting wave pattern to selecting wave pattern and to change 90% for darken.

$\phi = 180^\circ, \theta = 10^\circ$

Td : The time required to activate from selecting wave pattern to non-selecting wave pattern and to change 90% for darken.

$\phi = 180^\circ, \theta = 10^\circ$

(Note 3) Definition of viewing angle (ϕ, θ)



(Note 4) Definition of contrast ratio

<Definition>

$$\text{Contrast ratio} = \left(\frac{\text{Luminous intensity at off segment transmissivity}}{\text{Luminous intensity at on segment transmissivity}} \right)^n$$

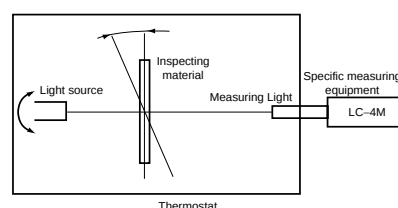
Positive type $n = 1$, Negative type $n = -1$

<Measurement conditions>

Drive conditions: Specific value condition

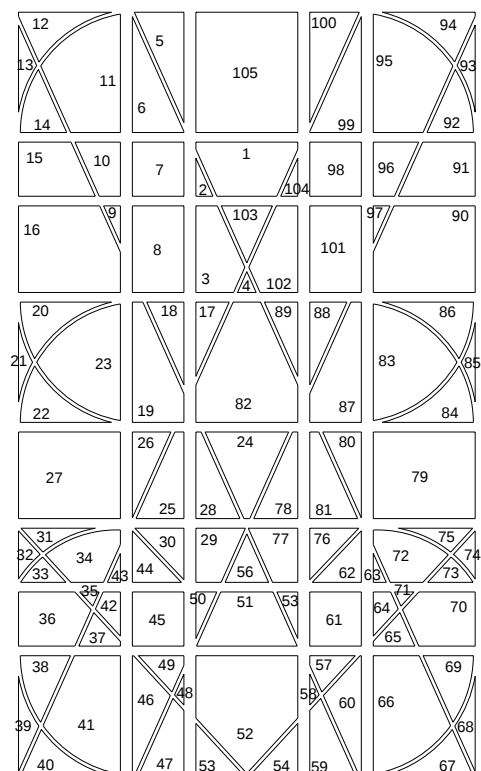
Viewing angle: $\phi = 180^\circ, \theta = 10^\circ$

(Note 5) Optical measuring equipment theory plan



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● Data format (data and display mapping)



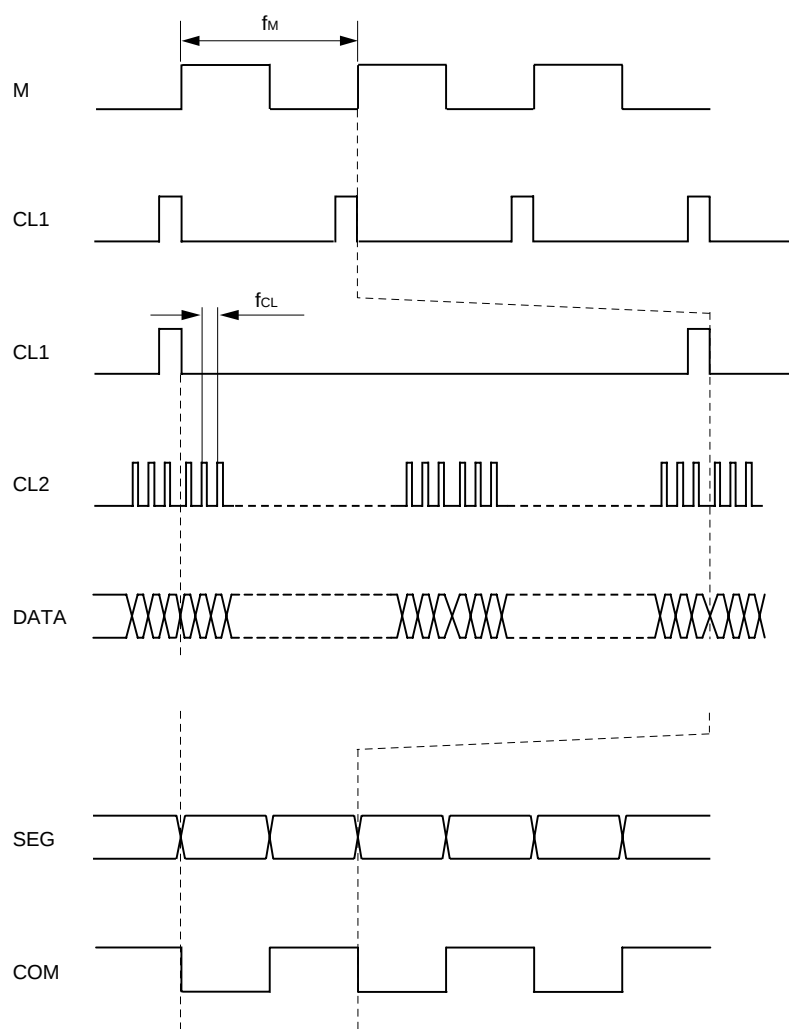
FIRST DATA ←

D1	D2	D3	—	—	—	—	D103	D104	D105	D106	D107	—	—	D159	D160
effective data										invalid data					

→ LAST DATA

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●Timing chart



Note) LCD operation output voltage

M	DATA	COM Voltage	SEG Voltage
1	1	GND	V_{DD}
1	0	GND	GND
0	1	V_{DD}	GND
0	0	V_{DD}	V_{DD}

Liquid crystal displays

●Operation notes

(1) Handling instruction

- Attention must be paid to avoid external shock, which will cause operational failure.
- Polarizer on the surface is gentle and can be damaged easily by scratch, thus please take extra care when handling. For surface termination, please wipe off with alcohol.
- The liquid used in the LCD panel is a harmful substance and must not be licked or swallowed. If you touch this liquid, wash it out completely.
- Do not touch IC lead and terminal.
- Do not expose to direct sunlight for a long period of time and if it will be used at direct sunlight, recommend to use UV cut filter.
- For storage please avoid in high temperature and high humidity. When long-term storage is required, keep the panels in low temperature (5°C ~ 25°C) and low humidity.
- To prevent TAB damage, TAB bent time must be up to twice.

(2) Operational instruction

- Please do not connect or take away the LCD module to the system in the condition of power on.
- Please input signal after LCD module power is turned on when turning off. Please turn off from input signal. In worst case IC can be broken by ratch up phenomenon.

(3) Mounting instruction

- In the circuit CMOS-IC is used. Please be careful for ESD.
- Protection sheet is put on LCD module surface and back side. At removing the sheet., electric static is generated. So it must be removed slowly and recommend to use Ion blower etc.

(4) Cautions for LCD with FPC

- Do not bend nor pull FPC.
- Do not hold FPC with fingers directly nor suspend FPC.
- When bending FPC, keep 5mm from the edge of glass (FPC joint) and bend toward mother board side.

(5) Production

- Production in Japan or China.