



GENERAL DESCRIPTION 功能叙述

The M6D/F is a CMOS ASIC decoder. It will de-code the serial data from data input. The VT signal will go high and data will be copied to the output if address are checked the same.

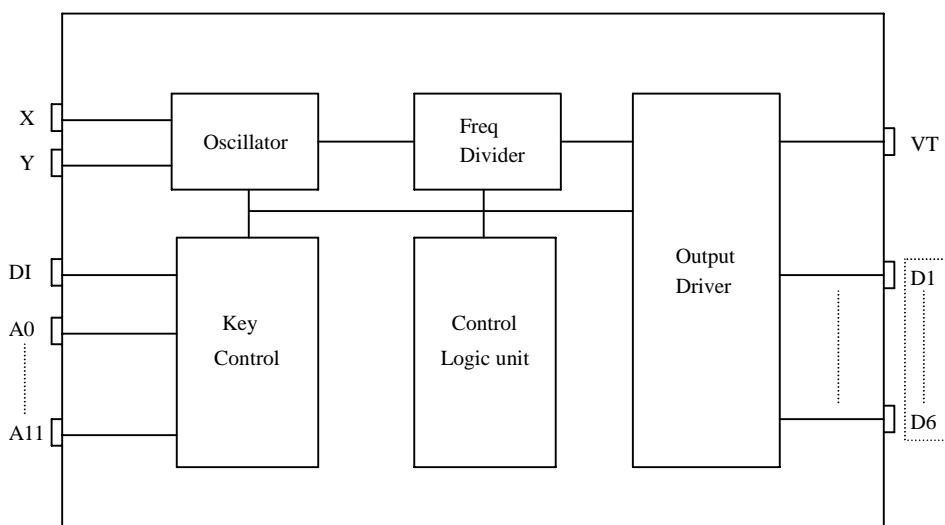
FEATURES 产品特长

- Same Rosc matched to the ENCODER M5E
- $3^{12} = 531,411$ codes, "0"、"X"、"1" Tri-states.
- 3 cycles checked each time.
- 1~6 data mask option.
- Address / Data types :
 - M6D : Latched data outputs.
 - M6F : Full address type.

APPLICATIONS 产品应用

- Car/home alarm system, garage control etc..

BLOCK DIAGRAM 功能方块图



*All specs and applications shown above subject to change without prior notice.

(以上电路及规格仅供参考,本公司得径行修正)



3 STATES DECODER

3 态译码 IC

EN/DECODER

M6D/F

ABSOLUTE MAXIMUM RATING

(TA=25°C)

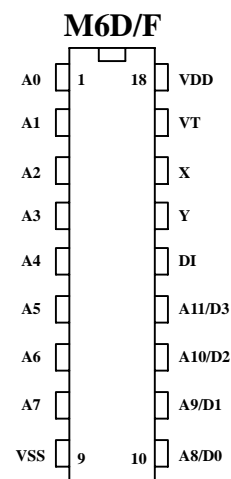
Parameter	Rating	Unit
Supply Voltage	-0.3 to 15	V
Input Voltage	-0.2~V _{DD} +0.2	V
Operating Temperature	-20 to 75	°C
Storage Temperature	-50 to 125	°C

ELECTRICAL CHARACTERISTICS

Characteristics	Sym.	Min.	Typ.	Max.	Unit	Conditions
Operating Voltage	V _{DD}	2.4	—	15	V	
Quiescent Current	I _{SB}	—	0.1	1	μ A	
Operating Current	I _{OP}	—	0.1	0.5	mA	No load
Output Drive Current	I _O	—	2	—	mA	@V _{DS} =1.2V
Input Voltage	V _{IH}	V _{DD} -0.2	V _{DD}	V _{DD}	V	
	V _{IL}	V _{SS}	V _{SS}	V _{SS} +0.2		
Oscillator Frequency	Fosc	—	76	—	KHz	External ± 30%

PIN DESCRIPTION

No.	M6D	M6F	Description
1~8	A0~A7	A0~A7	3 states address inputs
9	VSS	VSS	Negative power supply
10	A8	A8	3 states address inputs
11~13	D0~D2	A9~A11	Data outputs/Address inputs
14	DI	DI	Data input
15	Y	Y	Oscillator output
16	X	X	Oscillator input
17	VT	VT	Valid transmit
18	VDD	VDD	Positive power supply



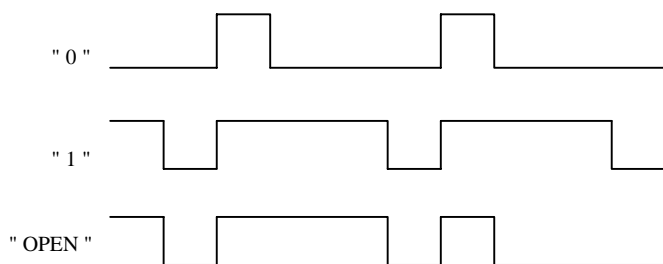


3 STATES DECODER

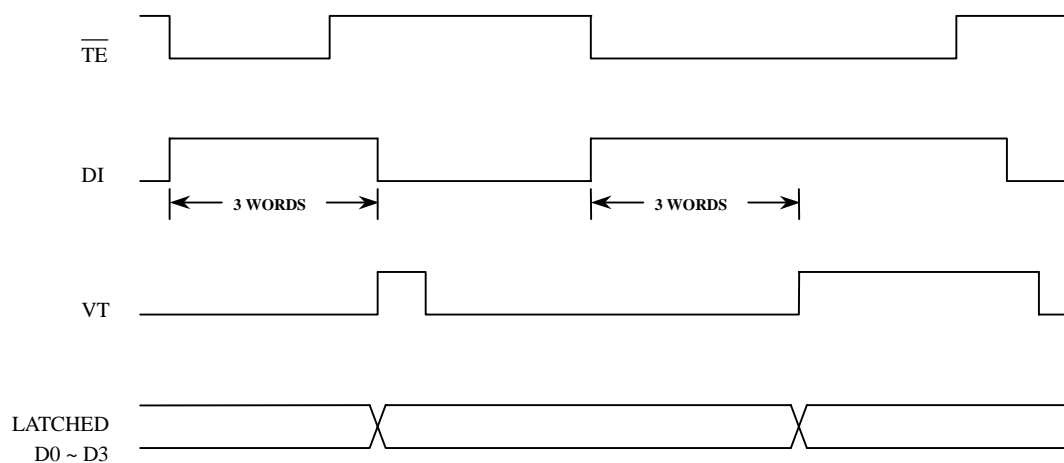
3 态译码 IC

TIMING WAVEFORM

(1) Bit format



(2) TIMING DIAGRAM

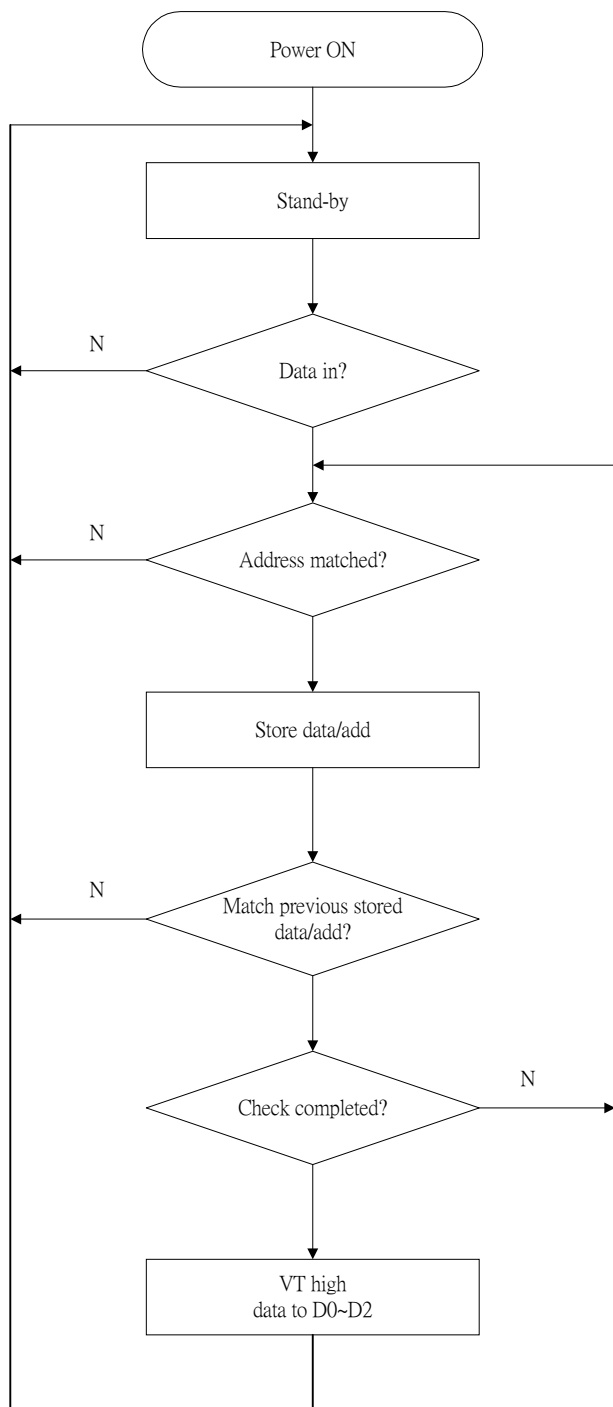




3 STATES DECODER

3 态译码 IC

OPERATING FLOWCHART





3 STATES DECODER

3 态译码 IC

M6D/F

RECONNENDED OSCILLATOR PARAMETERS

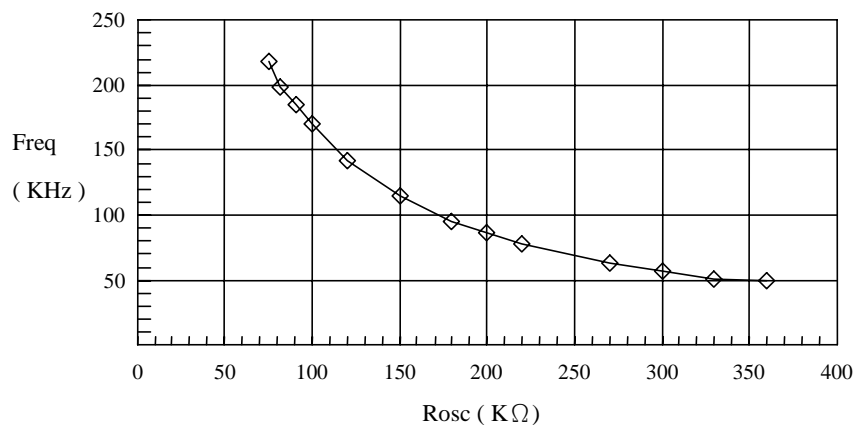
Rosc (KΩ)	M6D/E/F (KHz)
75	218
82	199
91	185
100	170
120	142
150	115
180	95
200	86
220	78
270	63
300	57
330	51
360	49

DATA OUTPUT

M6E (D0-D3)	M6D/F (D0-D3)
0 (VSS)	0 (VSS)
X (OPEN)	1 (VDD)
1 (VDD)	1 (VDD)
POWER ON	0 (VSS)

Freq-Rosc Chart

(@Vdd=12V)



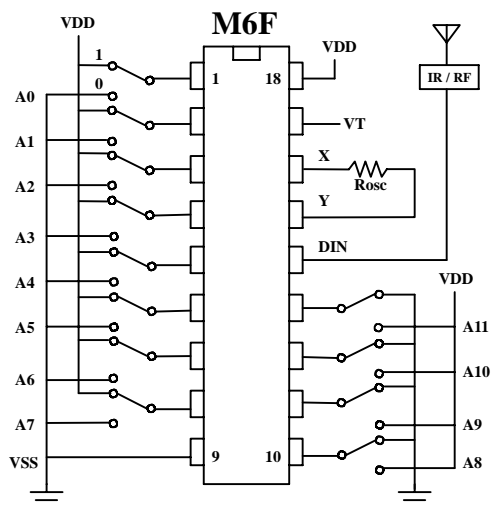
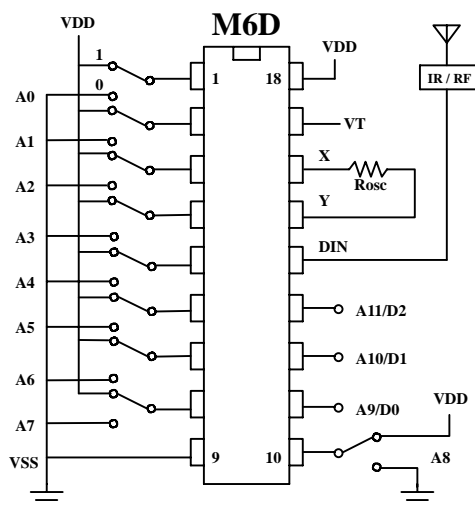
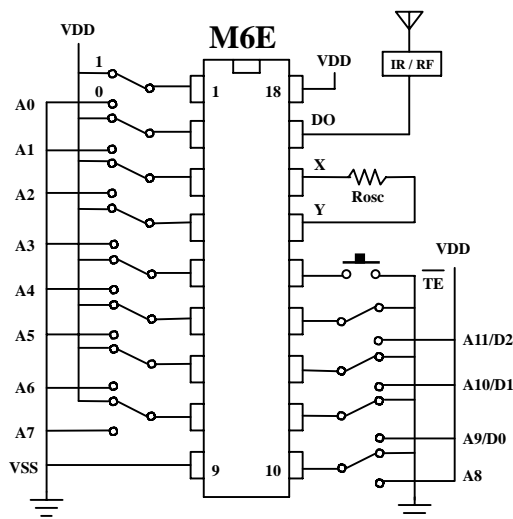


3 STATES DECODER

3 态译码 IC

M6D/F

APPLICATION DIAGRAM 参考电路图





3 STATES DECODER

3 态译码 IC

APPLICATION DIAGRAM 参考电路图 (SOP PACKAGE)

