

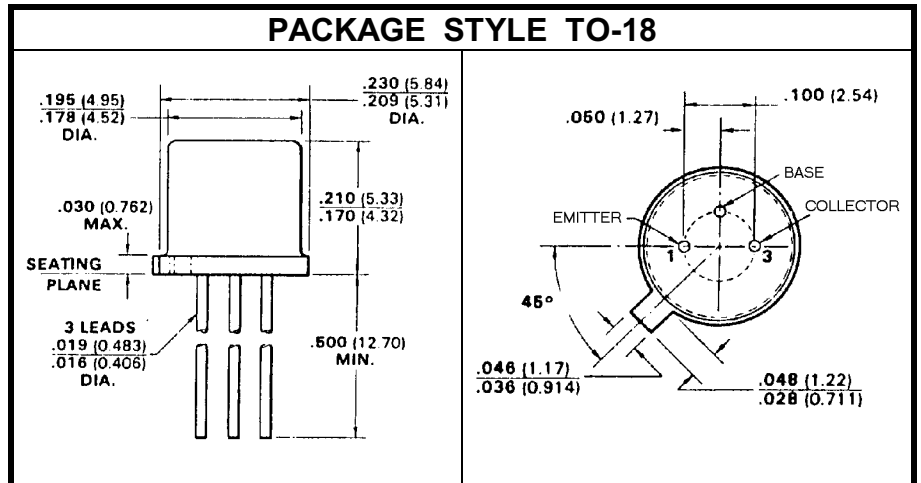
SILICON NPN SWITCHING TRANSISTOR

DESCRIPTION:

The **2N2432** is Designed for Low Level High Speed Chopper Applications and is Used Inverted.

MAXIMUM RATINGS

I_C	100 mA
V_{CE}	30 V
P_{DISS}	600 mW @ $T_C = 25^\circ C$
T_J	$-65^\circ C$ to $+200^\circ C$
T_{STG}	$-65^\circ C$ to $+200^\circ C$
θ_{JC}	291 $^\circ C/W$


CHARACTERISTICS $T_C = 25^\circ C$

SYMBOL	TEST CONDITIONS		MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	$I_C = 100 \mu A$		30			V
BV_{CEO}	$I_C = 10 \text{ mA}$		30			V
BV_{ECO}	$I_E = 100 \mu A$		15			V
I_{CBO}	$V_{CB} = 25 \text{ V}$				10	nA1
I_{EBO}	$V_{EB} = 15 \text{ V}$				2.0	nA
I_{CES}	$V_{CE} = 25 \text{ V}$	$V_{BE} = 0 \text{ V}$			10	nA
		$T_A = 125^\circ C$			250	
I_{ECS}	$V_{CE} = 15 \text{ V}$	$V_{BC} = 0 \text{ V}$			2.0	nA
		$T_A = 125^\circ C$			200	
h_{FE}	$V_{CE} = 5.0 \text{ V}$	$I_C = 10 \mu A$	30			---
	$V_{CE} = 5.0 \text{ V}$	$I_C = 1.0 \text{ mA}$	50			
$h_{FE(INV)}$	$V_{CE} = 5.0 \text{ V}$	$I_E = 200 \mu A$	2.0			---
$V_{CE(SAT)}$	$I_C = 10 \text{ mA}$	$I_B = 0.5 \text{ mA}$			0.15	V
$V_{EC(ofs)}$	$I_E = 0 \text{ A}$	$I_B = 200 \mu A$			0.5	V
$V_{EC(ofs)}$	$I_E = 0 \text{ A}$	$I_B = 1.0 \text{ mA}$			1.0	V
f_t	$V_{CE} = 5.0 \text{ V}$	$I_C = 1.0 \text{ mA}$	1.0	$f = 20 \text{ MHz}$		---
$r_{ec(on)}$	$I_E = 0 \text{ A}$	$I_B = 1.0 \text{ mA}$		$f = 1.0 \text{ KHz}$	20	Ω
C_{ob}	$V_{CB} = 0 \text{ V}$			$f = 140 \text{ KHz}$	12	pF
C_{ib}	$V_{EB} = 0 \text{ V}$			$f = 140 \text{ KHz}$	12	pF