

TV Time/Channel Display Circuits

FEATURES

- Channel Display 1 to 16
- 4 Digit Clock Display option
- Color character on black background or color character on color background
- 14 or 24 DIL package

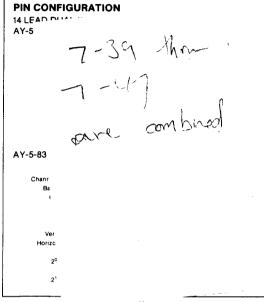
OPTIONS

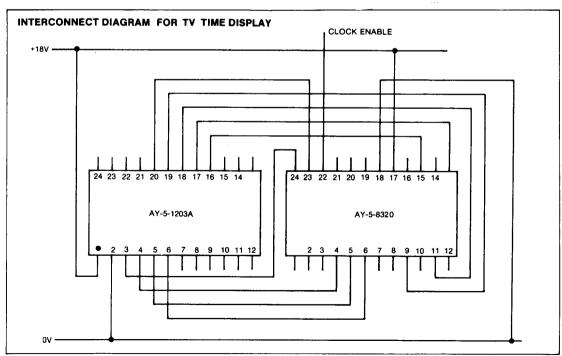
Part Number	Channel	Time	
AY-5-8301	1-16	No	
AY-5-8320/21	1-16		

^{*}The AY-5-8320/21 are capable of either simultaneous or separate time and channel display and have automatic display enable.

DESCRIPTION

The AY-5-8300 series is a family of MOS circuits designed to display channel and time information on the screen of a TV set. The information is displayed as colored characters on a black or color background. Channel information is displayed as a single character 1 to 16. Time is provided as a 4 digit hours and minutes display. The display is positioned at the top right hand corner or at the bottom center of the screen; the display may be permanent or momentary. Any of the AY-5-8300 series may be used for either 525 or 625 line systems.





Name Function

ALL TYPES:

Vertical Sync Input

Horizontal Sync Input

1.1MHz Clock Input

Channel Inputs 20-23

Resets the circuit at the end of each frame. At logic '0' during vertical flyback.

Activates the line counter. At logic '0' during horizontal flyback.

Determines character position and width. Must be synchronized by horizontal sync pulse to prevent ragged edges on character.

Code			Display	
·3	2 ²	21	2 ⁰	AY-5-8301/20/21
)	0	0	0	1
)	0	0	1	2
)	0	1	0	3
)	0	1	1	4
)	1	0	0	5
)	1	0	1	6
)	1	1	0	7
)	1	1	1	8
ı	0	0	0	9
ı	0	0	1	10
ı	0	1	0	11
ı	0	1	1	12
ı	1	0	0	13
ı	1	0	1	14
ı	1	1	0	15
ı	1	1	1	16

AY-5-8301 Display Enable

AY-5-8301

Character Output Color Output

AY-5-8320/21

Clock Inputs 20-23

Mx1-Mx4

Strobe Input

AY-5-8320/21

Character Output

Background Output

Channel Display Enable

Clock Display Enable Seconds Colon Input When taken to logic '0', the display is enabled. If an RC network is connected to this pin, a momentary display can be obtained.

Defines the background border and the character.

Determines the character color. Goes to logic '1' during a character block.

Multiplexed 4 digit BCD clock data inputs such as available from the AY-5-1203A clock circuit.

Multiplex inputs, at logic '1' during multiplex time slot. For the AY-5-8310/11, when operating in the 00-99 channel mode, Mx1 and Mx2 time slots are used.

This input must go to a logic '1' during the middle of each Mx time slot to load the clock data into the chip.

Defines the character outlines. At logic '1' when displaying a character.

Defines the background block. At logic '1' when outputting background.

When taken to logic '1', the channel display is enabled. The display is automatically enabled when the channel is changed.

When taken to logic '1', the clock display is enabled.

This input controls the colon between the hours and minutes display. When at logic '0', the colon is blanked. If connected to the DP output of the AY-5-1203A clock circuit, the colon will flash once per second.

ENTER. TAINMENT

OPERATION

The display is positioned digitally in both the vertical and horizontal directions. The vertical position is determined by counting horizontal sync pulses (the counting is initiated by the vertical sync pulse). The timing relationships are shown in Figs. 8a and 8b. Additionally, for the AY-5-8320/21, the time display is positioned 35 lines further down so that it appears immediately below the channel display.

In the horizontal direction the display is positioned by counting pulses from an external 1.1MHz oscillator which is synchronized with the horizontal sync pulse to prevent ragged edges on each character.

Each character is made up of 15 dots in a 3×5 matrix. With a one dot border around each character a total matrix of 35 dots in a 5×7 format is utilized. Each dot lasts 0.9 μ sec in the horizontal direction and is 5 lines high. This gives a rectangular dot and characters as shown in Fig. 1.

The various channel/time display formats are illustrated in Figs. 4, 6 and 7. The display positioning on the TV screen is shown in Figs. 6a and 6b.

In the AY-5-8301, the character display is controlled by two outputs. Character and Color. The video channels are controlled in the following manner:

(a) Black/white display

Character	Color	
0	0	Normal picture
1	0	Black (luminance channel full off)
1	1	Black
0	1	White

(b) Black/Yellow display

haracter	Color	Normal picture
1	0	Black (luminance full off)
1	1	Black (luminance full off and blue suppressed)
0	1	Yellow (luminance full on and blue suppressed)

Other color displays are generated by suppressing one or two chrominance channels.

In the AY-5-8320/21, one video output defines the characters and the other a background block. Using these outputs, a display of any color character on a background of any color may be obtained. these outputs, a display of any color character on a backgound of any color may be obtained.

The channel data is input on four lines; in 1—16 channel mode, this information is applied in binary from a diode encoder attached to the varactor tuning drivers. Binary numbers greater than 9 are detected and displayed at a two digit character.

In the clock mode, data is entered on a 4 line BCD bus multiplexed into 4 time slots. A strobe signal occurring in the middle of each time slot is used to read the data into the chip.

When the AY-5-1203A clock is used it can be directly connected to the AY-5-8320/21 with no external components. The AY-5-8320/21 displays the time with hours, minutes and a flashing colon for seconds (Fig. 5).



Fig. 1 CHARACTER SET (AY-5-8301/20/21)



Fig. 2 CHANNEL DISPLAY

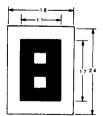
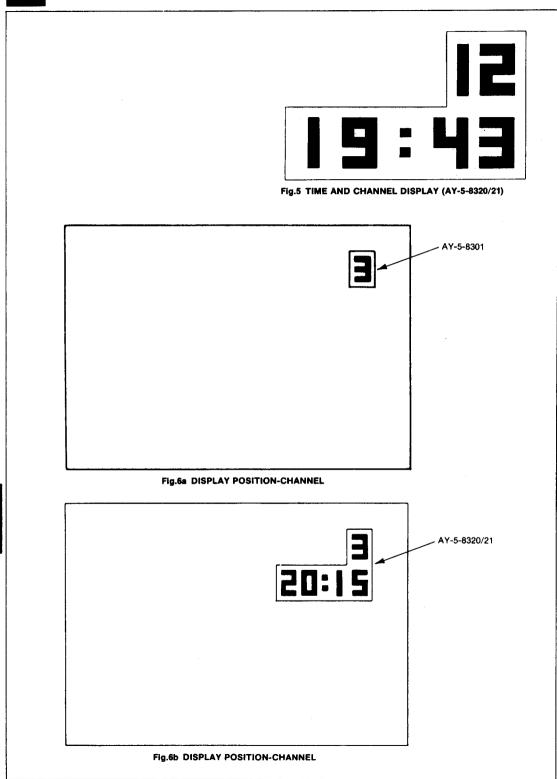


Fig. 3 CHARACTER SIZE (25/26 INCH SCREEN)



ELECTRICAL CHARACTERISTICS

Maximum Ratings*

Voltage on any pin with respect to V_{ss} pin ± 0.3 to $\pm 20 \text{V}$ Ambient Operating temperature range 0°C to +85°C Storage temperature range. -65°C to +150°C

*Exceeding these ratings could cause permanent damage. Functional operation of these devices at these conditions is not implied -operating ranges are specified below.

Standard Conditions (unless otherwise noted)

 $V_{GG} = 0V$

 $V_{SS} = +17V \text{ to } +19V$

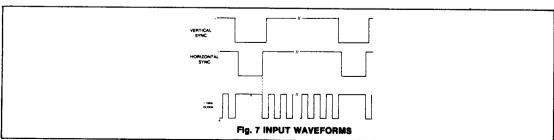
Operating Temperature $(T_A) = 0$ °C to +85°C

Characteristic	Min	Тур"	Max	Units	Conditions
Vertical Sync Input (Note 1)		1			
Logic '0'	l o		7	l v	
Logic '1'	V _{ss} -5	_	Vss +0.5	V	
Rise & Fall Time	_	_	5	μS	10% to 90%
		j		,	Min slew rate 5V/µsec
Horizontal Sync Input	i	1	Į.	l	1
Logic '0'	0	l –	7	V	1
Logic '1'	V _{ss} -1.5	_	Vss +0.3	v	
Rise & Fall Time	_	-	1	μS	10% to 90%
1.1MHz Clock Input	1.0	1.1	1.15	MHz	
Logic '0'	0	<u> </u>	7	V	
Logic '1'	Vss -5	-	Vss +0.3	v	
Rise & Fall Time	_		300	ns	10% to 90%
Pulse width	250	-	_	ns	at logic 0 and logic 1 levels
Channel Inputs (Note 1)		İ	i		·
Logic '0'	0	-	7	V	
Logic '1'	V ₈₈ -5	-	Vss +0.5	V	
Leakage	_		10	μΑ	V _{IN} = (V _{SS} -19) Volts
Display Enable Inputs	i	1			
Switch point positive edge	V _{ss} ~8		Vss -5	v	i
Outputs		ł			
On resistance	_		1.5	kΩ	V _{01.1} = V ₅₅ -2V
Off leakage	_	_	1	μA	V _{01.1} = 0V
Turn ON time	_	_	200	ns	10-90% load 25K & 20pF to ground
Down AV E BOOT			400	-	_ ' *
Power: AY-5-8301 AY-5-8320	_	-	400	mW	V _{SS} = +19V
A 1-3-032U		-	750	mW	V _{ss} = +19V

^{**}Typical values are at +25°C and nominal voltages.

NOTE:

1. These inputs are diode clamped to V_{ss} . Maximum clamp current $50\mu A$.



SPECIFICATIONS

Maximum Ratings*

Voltage on any pin with respect to V_{SS} pin +0.3 to -20V Ambient Operating temperature range 0°C to +70°C Storage temperature range -65°C to +150°C

*Exceeding these ranges could cause permanent damage. Functional operation of this device at these conditions is not implied—operating ranges are specified below.

Standard Conditions (unless otherwise noted)

 $V_{GG} = 0V$

 $V_{SS} = +11.4V \text{ to } +12.6V$

Operating Temperature (T_A) = 0°C to +70°C

Characteristic	Min	Тур"	Max	Units	Conditions
Vertical Sync Input (Note 1)					
Logic '0'	0	_	3	V	
Logic '1'	V _{ss} −3.5	_	V _{ss} +0.3	V	
Rise & Fall Time	_	-	5	μS	10% to 90%
					Min slew rate 5V/µsec
Horizontal Sync Input	İ			ı	
Logic '0'	0		3	V	
Logic '1'	V _{ss} −3.5	-	Vss +0.3	٧	
Rise & Fall Time	-	-	1	μS	10% to 90%
1.1MHz Clock Input	1.0	1.1	1.15	MHz	
Logic '0'	0	-	3	V	
Logic '1'	V _{SS} −3.5	-	Vss +0.3	٧	
Rise & Fall Time	-	-	100	ns	10% to 90%
Pulse width	300	-	-	ns	at logic 0 and logic 1 levels
Channel Inputs (Note 1)		l			
Logic '0'	0	-	3	V	
Logic '1'	V _{ss} −3.5	-	V _{ss} +0.3	٧	
Clock Inputs, Multiplex,]		
Strobe Inputs					
Logic '0'	0	-	3	V	
Logic '1'	V _{ss} 0.5	-	V _{ss} +0.3	V	
Input Resistance	_	20	_	kΩ	To V _{GG}
Display Enable Inputs					
Switch point negative edge	V _{ss} −5.5	_	Vss -3.5	Volts	
Outputs]	1	1		
On resistance	_	_	1	kΩ	$V_{OC1} = V_{SS} - 2V$
Off leakage	_	_	1	μΑ	$V_{OUI} = 0V$
Turn ON time	_	_	200	ns	10-90% load 25K & 20pF to ground
_	l l	150	1	mW	V _{SS} = +12V
Power	_	150		mvv	VSS = 712V

^{**}Typical values are at +25°C and nominal voltages.

NOTE

^{1.} These inputs are diode clamped to V_{SS} . Maximum clamp current 0.5mA.

