



MOTOROLA

SEMICONDUCTORS

3501 ED BLUESTEIN BLVD., AUSTIN, TEXAS 78721

**MC2661A/MC68661A
MC2661B/MC68661B
MC2661C/MC68661C
Addendum**

ENHANCED PROGRAMMABLE COMMUNICATIONS INTERFACE (EPCI) Advance Information Data Sheet ADI-868-R1

The following footnote should be added to the **AC ELECTRICAL CHARACTERISTICS** table found on page 11 of the MC2661A, B, C/MC68661A, B, C Advance Information Data Sheet (ADI-868-R1).

- 10. In asynchronous local loopback mode, using the X1 clock, the following parameters apply:
 - $f_{R/T} = 0.83$ MHz maximum
 - $t_{R/TL} = 700$ ns minimum

This footnote is referenced by the TxC or RxC Input Clock Frequency ($f_{R/T}$) and the TxC or RxC Low Clock Width ($t_{R/TL}$) entries to the **AC ELECTRICAL CHARACTERISTICS** table.

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