# M-986 A/μ-Law MF R1 Transceiver

The M-986-2A2 (dual channel) MF R1 Transceiver is similar to the M-986-XR1, but is A/μ-Law selectable. It can be configured for various operational characteristics by writing two configuration bytes to the coprocessor port. The format of the two configuration bytes is as follows:

**PRELIMINARY INFORMATION**

| CONFIGURATION BYTE 1 | | | | | | | |
|---|---|---|---|---|---|---|---|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 0 | 0 | ECLK | IOM | ENC1 | KPL1 | KPEN1 | 0 |

ECLK   Channels 1 & 2   1 = External Codec Clock, 0 = Internal Codec Clock

IOM   Channels 1 & 2   1 = Binary input/output, 0 = 2-of-6 input/output

ENC1   Channel 1   1 = Enable channel, 0 = Disable channel

KPL1   Channel 1   1 = 55 ms. detection time for KP, 0 = 30 ms. detection time for KP

KPEN1   Channel 1   1 = Enable MF tone detection after KP detection
0 = MF tone detection always on

| CONFIGURATION BYTE 2 | | | | | | | |
|---|---|---|---|---|---|---|---|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 0 | 1 | 0 | AMU | ENC2 | KPL2 | KPEN2 | 0 |

AMU   Channels 1 & 2   1 = A-law Encoding,   0 = μ-law Encoding

ENC2   Channel 2   1 = Enable channel, 0 = Disable channel

KPL2   Channel 2   1 = 55 ms. detection time for KP, 0 = 30 ms. detection time for KP

KPEN2   Channel 2   1 = Enable MF tone detection after KP detection
0 = MF tone detection always on