SEMI CONDUCTOR

Ultra Low Capacitance ESD Protection

Voltage

ΡΛΝ

5 V

Features

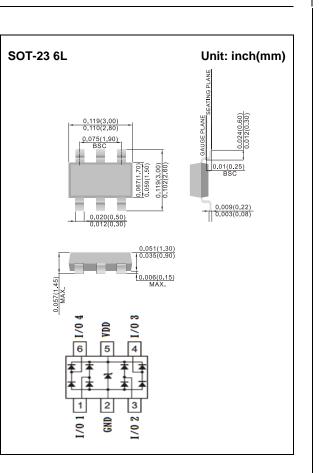
- IEC61000-4-2(ESD): ±20kV Air, ±15kV Contact Compliance
- IEC61000-4-4(EFT): 40A(5/50nS)
- IEC61000-4-5(Lightning): 5A(8/20µS)
- Low leakage current, maximum 1µA at rated voltage
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: SOT-23 6L, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026

Applications

- USB2.0 Data Line Protection
- Video Graphics Cards
- Monitors and Flat Panel Displays Notebook computers
- Digital Video Interface(DVI)
- 10/100/1000 Ethernet
- ATM Interfaces
- Control Signal Lines Protection



Maximum Ratings

PARAMETER	SYMBOL	VALUE	UNITS	
ESD IEC61000-4-2(Air)	N	±20	kV	
ESD IEC61000-4-2(Contact)	V_{ESD}	±15		
Operating Junction Temperature Range	ТJ	-55 to +150	°C	
Storage Temperature Range	T _{STG}	-55 to +150	°C	

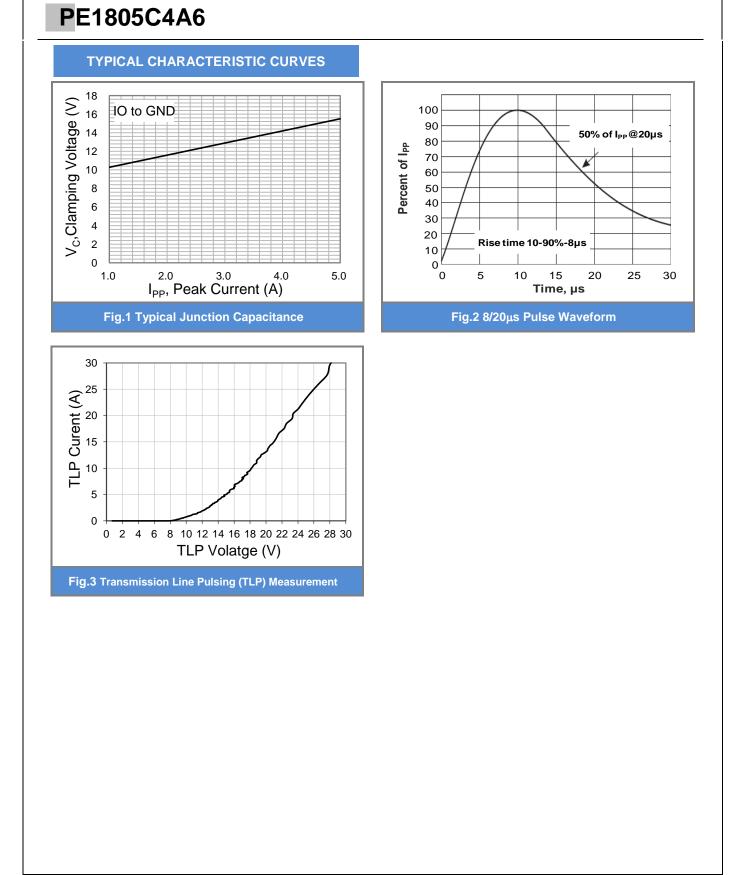


Electrical Characteristics

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage (Note 1)	V_{RWM}	-	-	-	5	V
Reverse Breakdown Voltage	V_{BR}	I _{BR} =1mA, Pin5 to Pin2	6	-	9	V
Reverse Leakage Current	I _R	V _R =5.0V	-	-	1	μA
Forward Voltage	V _F	I _F =1mA, Pin2 to Pin5	-	-	1	V
Clamping Voltage	V _{CL}	I _{PP} =1A, t _P =8/20μs, any I/O pin to Pin2	-	-	12	V
		I _{PP} =5A, t _P =8/20μs any I/O pin to Pin2	-	-	20	
Off State Junction Capacitance	CJ	0Vdc Bias f=1MHz, Between any I/O pins to Pin2	-	-	0.8	pF
		0Vdc Bias f=1MHz, Between any I/O pins	-	-	0.4	

Note : 1.A transient suppressor is selected according to the working peak reverse voltage(V_{RWM}), which should be equal to or greater than the DC or continuous peak operation voltage level.

December 9,2016-REV.00





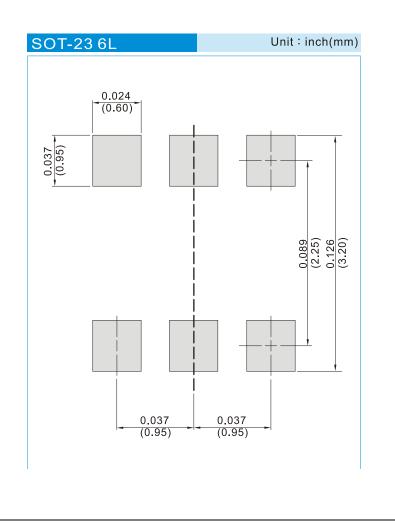




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PE1805C4A6_R1_00001	SOT-23 6L	3K pcs / 7" reel	C2B	Halogen free
PE1805C4A6_R2_00001	SOT-23 6L	10K pcs / 13" reel	C2B	Halogen free

MOUNTING PAD LAYOUT





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